



SBR1A400P1

1.0A SBR SURFACE MOUNT SUPER BARRIER RECTIFIER PowerDI123

Features

- Ultra-Low Forward Voltage Drop
- Low Leakage Current
- Superior Reverse Avalanche Capability
- Excellent High-Temperature Stability
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier Technology (SBR[®])
- Soft, Fast Switching Capability
- +150°C Operating Junction Temperature
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: PowerDI[®]123
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- · Polarity Indicator: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.018 grams (Approximate)

PowerDI123



Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
SBR1A400P1-7	PowerDI123	3,000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

PowerDI123



SDE = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 3 = March)

Date Code Key

	- 410 0040 110,												
ſ	Year	201	5 2016 2017		2017	20	2018 2019			2020		2021	
	Code	С		D		Е		F	G		Н		1
Ī	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Ī	Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	400	V
Average Rectified Output Current (See Figure 1)	lo	1.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	40	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Junction to Ambient (Note 5)	R _{θJA}	138	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

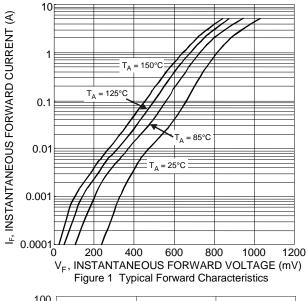
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage	V _F	_	0.88	1.1 1.0	V	I _F = 1.0A, T _J = +25°C
		_		0.05		$I_F = 1.0A$, $T_J = +125$ °C $V_R = 400V$, $T_J = +25$ °C
Reverse Current (Note 6)	IR	_	0.013 0.073	0.36 2	mA	$V_R = 400V, T_J = +85^{\circ}C$ $V_R = 400V, T_J = +125^{\circ}C$
Reverse Recovery Time	t _{RR}	_	_	85	ns	$I_F = 0.5A, I_R = 1A,$ $I_{RR} = 0.25A$

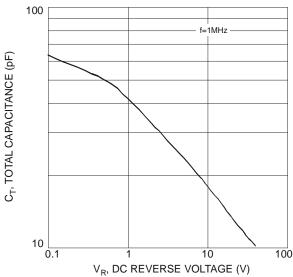
Notes:

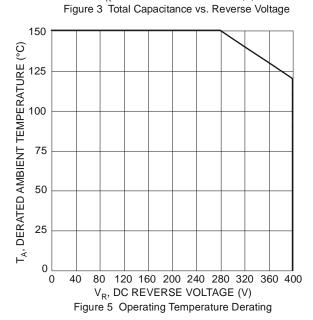
^{5.} FR-4 substrate PC board, with minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

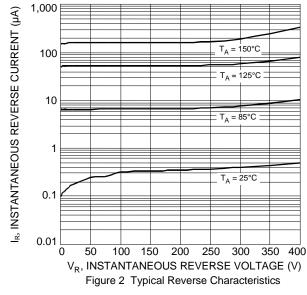
^{6.} Short duration pulse test used to minimize self-heating effect.

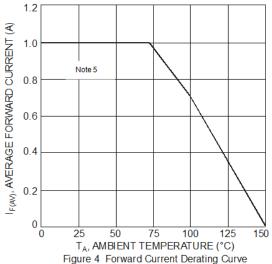










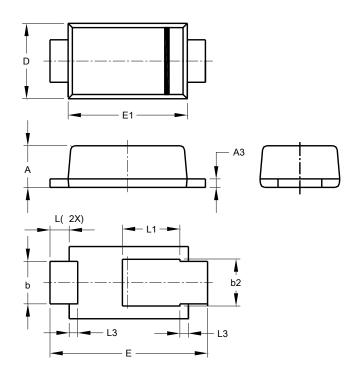




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123

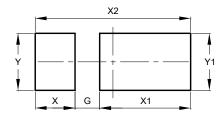


PowerDI123						
Dim	Min	Max	Тур			
Α	0.93	1.00	0.98			
А3	0.15	0.25	0.20			
b	0.85	1.25	1.00			
b2	1.025	1.125	1.10			
D	1.63	1.93	1.78			
Е	3.50	3.90	3.70			
E1	2.60	3.00	2.80			
L	0.40	0.50	0.45			
L1	1.25	1.40	1.35			
L3	0.125	0.275	0.20			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
Х	1.05
X1	2.40
X2	4.10
Υ	1.50
Y1	1.50



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