## Features

- Low On-Resistance
- ESD Protected Gate to 500V
- Low Input Capacitance
- Fast Switching Speed
- Lead Free By Design/RoHS Compliant (Note 3)
- "Green" Device (Note 4)
- Qualified to AEC-Q 101 Standards for High Reliability


## Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish - Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)

SOT-563


ESD protected to 500 V


TOP VIEW


TOP VIEW Internal Schematic

Maximum Ratings $@ T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Value |  |
| :--- | :---: | :---: | :---: |
| Drain-Source Voltage | $V_{\text {DSS }}$ | -50 |  |
| Drain-Gate Voltage (Note 1) | $V_{D G R}$ | -50 | V |
| Gate-Source Voltage | $\mathrm{V}_{\mathrm{GSS}}$ | V |  |
| Drain Current (Note 2) | $\mathrm{I}_{\mathrm{D}}$ | -20 |  |

## Thermal Characteristics @ $T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Value | Units |
| :--- | :---: | :---: | :---: |
| Total Power Dissipation (Note 2) | $P_{D}$ | 400 | mW |
| Thermal Resistance, Junction to Ambient (Note 2) | $\mathrm{R}_{\theta J \mathrm{~A}}$ | 313 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{J}, \mathrm{~T}_{\text {STG }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics $@ \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OFF CHARACTERISTICS (Note 5) |  |  |  |  |  |  |
| Drain-Source Breakdown Voltage | BV ${ }_{\text {DSS }}$ | -50 | - | - | V | $\mathrm{V}_{G S}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-250 \mu \mathrm{~A}$ |
| Zero Gate Voltage Drain Current | Idss | - | - | -1 | $\mu \mathrm{A}$ | $V_{D S}=-50 \mathrm{~V}, \mathrm{~V}_{G S}=0 \mathrm{~V}$ |
| Gate-Body Leakage | IGss | - | - | $\pm 5$ | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{GS}}= \pm 20 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ |
| ON CHARACTERISTICS (Note 5) |  |  |  |  |  |  |
| Gate Threshold Voltage | $\mathrm{V}_{G S}($ th) | -0.8 | - | -2.1 | V | $\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{GS}}, \mathrm{I}_{\mathrm{D}}=-250 \mu \mathrm{~A}$ |
| Static Drain-Source On-Resistance | $\mathrm{R}_{\text {DS ( }}$ ON) | - | 6 | 8 | $\Omega$ | $V_{G S}=-5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-0.100 \mathrm{~A}$ |
| Forward Transconductance | gFs | 0.05 | - | - | S | $V_{D S}=-25 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-0.1 \mathrm{~A}$ |
| DYNAMIC CHARACTERISTICS |  |  |  |  |  |  |
| Input Capacitance | $\mathrm{C}_{\text {iss }}$ | - | 27 | - | pF |  |
| Output Capacitance | $\mathrm{C}_{\text {oss }}$ | - | 4 | - | pF | $\mathrm{V}_{\mathrm{DS}}=-25 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{f}=1.0 \mathrm{MHz}$ |
| Reverse Transfer Capacitance | $\mathrm{C}_{\text {rss }}$ | - | 1.4 | - | pF |  |

Notes: 1. $\mathrm{R}_{\mathrm{GS}} \leq 20 \mathrm{~K} \Omega$.
2. Device mounted on FR-4 PCB, 1 inch $\times 0.85$ inch $\times 0.062$ inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
3. No purposefully added lead.
4. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
5. Short duration pulse test used to minimize self-heating effect.

DMP58D0SV


Fig. 1 Typical Output Characteristics

$-_{D}$, DRAIN CURRENT (A)
Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage


Fig. 5 On-Resistance Variation with Temperature


Fig. 2 Typical Transfer Characteristics


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature


Fig. 6 Typical Capacitance


Fig. 7 Gate Threshold Variation vs. Ambient Temperature


Fig. 8 Diode Forward Voltage vs. Current


Fig. 9 Transient Thermal Response
Ordering Information (Note 6)

| Part Number | Case | Packaging |
| :---: | :---: | :---: |
| DMP58DOSV -7 | SOT-563 | 3000/Tape \& Reel |

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## Marking Information (Note 7)



MPA = Product Type Marking Code
YM = Date Code Marking
$Y=$ Year (ex: $U=2007$ )
$M=$ Month (ex: $9=$ September)

Date Code Key


Notes: 7. Package is non-polarized. Parts may be on reel in orientation illustrated, $180^{\circ}$ rotated, or mixed (both ways).

DMP58D0SV

Package Outline Dimensions


| SOT-563 |  |  |  |
| :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |
| A | 0.15 | 0.30 | 0.20 |
| B | 1.10 | 1.25 | 1.20 |
| C | 1.55 | 1.70 | 1.60 |
| D | - | - | 0.50 |
| G | 0.90 | 1.10 | 1.00 |
| H | 1.50 | 1.70 | 1.60 |
| K | 0.55 | 0.60 | 0.60 |
| L | 0.10 | 0.30 | 0.20 |
| M | 0.10 | 0.18 | 0.11 |
| All Dimensions in | mm |  |  |

## Suggested Pad Layout



| Dimensions | Value (in $\mathbf{~ m m}$ ) |
| :---: | :---: |
| $\mathbf{Z}$ | 2.2 |
| $\mathbf{G}$ | 1.2 |
| $\mathbf{X}$ | 0.375 |
| $\mathbf{Y}$ | 0.5 |
| $\mathbf{C 1}$ | 1.7 |
| $\mathbf{C 2}$ | 0.5 |

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