



60V 175°C P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	155mΩ @ V _{GS} = -10V	-2.4A
-60V	240mΩ @ V _{GS} = -4.5V	-1.9A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Load Switch

Features and Benefits

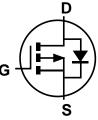
- Rated to +175°C Ideal for High Ambient Temperature Environments
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMPH6250SQ</u>)

Mechanical Data

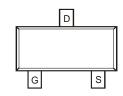
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)







Internal Schematic



Top View

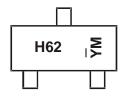
Ordering Information (Note 4)

Part Number	Case	Packaging
DMPH6250S-7	SOT23	3000/Tape & Reel
DMPH6250S-13	SOT23	10000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- $4. For packaging details, go to our website at \ https://www.diodes.com/design/support/packaging/diodes-packaging/. \\$

Marking Information



H62 = Product Type Marking Code YM = Date Code Marking Y= Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	2020	202	21	2022	2023	2024	202	25	2026	2027
Code	F	G	Н	- 1		J	K	L	M		N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	-60	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = -10V	I _D	-2.4 -1.5	А		
Pulsed Drain Current (380µs Pulse, Duty Cycle =		I _{DM}	-13	Α	
Maximum Continuous Body Diode Forward Curre	Is	-1.6	Α		
Pulsed Body Diode Forward Current (380µs Puls	I _{SM}	-13	A		
Avalanche Current , L = 0.1mH		I _{AS}	-12	А	
Avalanche Energy , L = 0.1mH	E _{AS}	8	mJ		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	0.92	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{\theta JA}$	165	°C/W
Power Dissipation (Note 6)	P _D	1.62	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	$R_{\theta JA}$	93.1	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +175	°C

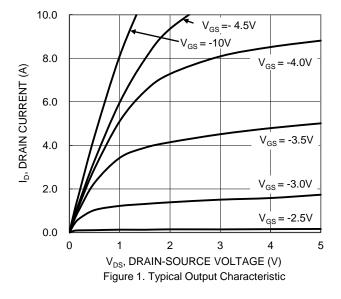
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-60	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	1	_	-1.0	μA	$V_{DS} = -60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1.0	-1.9	-3.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	Pageon	-	112	155	mΩ	$V_{GS} = -10V, I_D = -2A$
Static Drain-Source On-Nesistance	R _{DS(ON)}	_	149	240	11122	$V_{GS} = -4.5V$, $I_{D} = -2A$
Diode Forward Voltage	V _{SD}		-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}		512	_	pF	.,
Output Capacitance	Coss	1	31.3	_	pF	$V_{DS} = -30V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	-	23.2	_	рF	1 – 1.0101112
Gate Resistance	Rg	I	11.9	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)	Qg	I	4.0		nC	
Total Gate Charge (V _{GS} = -10V)	Q_{g}	I	8.3		nC	V _{DS} = -30V. I _D = -2A
Gate-Source Charge	Qgs	I	1.2		nC	$V_{DS} = -30V$, $I_{D} = -2A$
Gate-Drain Charge	Q_{gd}	I	1.7		nC	
Turn-On Delay Time	t _{D(ON)}		12.5	_	ns	
Turn-On Rise Time	t _R	_	13.4	_	ns	$V_{DD} = -30V, V_{GS} = -10V,$
Turn-Off Delay Time	t _{D(OFF)}	1	96.0	_	ns	$I_D = -1.0A, R_G = 50\Omega$
Turn-Off Fall Time	t _F	1	39.1	_	ns	
Body Diode Reverse Recovery Time	t _{RR}	1	9.6	_	ns	I _F = -1A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	1	3.1	_	nC	$I_F = -1A$, di/dt = 100A/ μ s

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

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 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





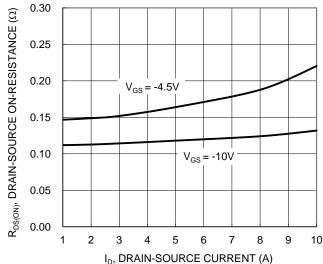


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

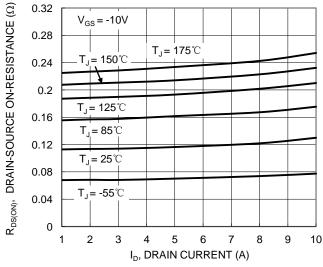
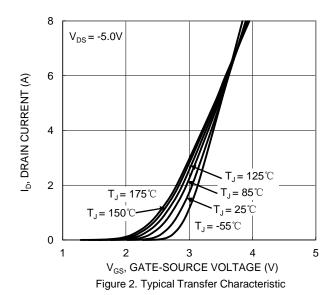
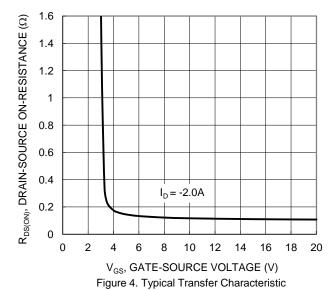


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





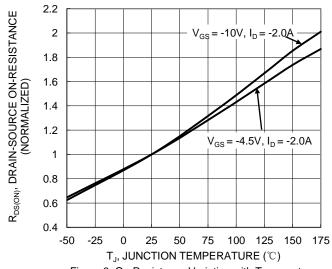


Figure 6. On-Resistance Variation with Temperature



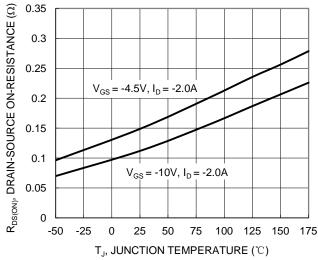


Figure 7. On-Resistance Variation with Temperature

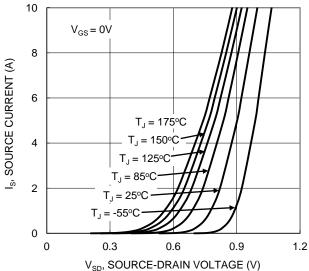


Figure 9. Diode Forward Voltage vs. Current

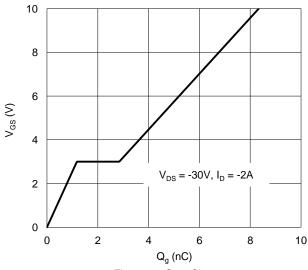


Figure 11. Gate Charge

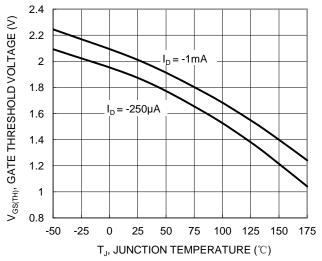
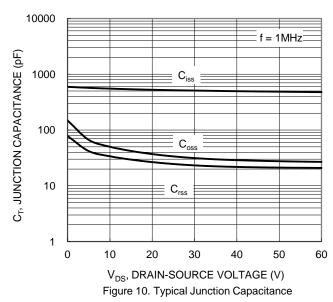
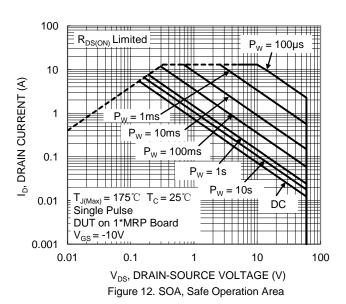


Figure 8. Gate Threshold Variation vs. Junction Temperature







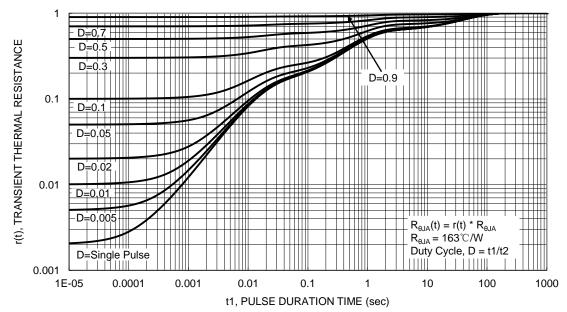


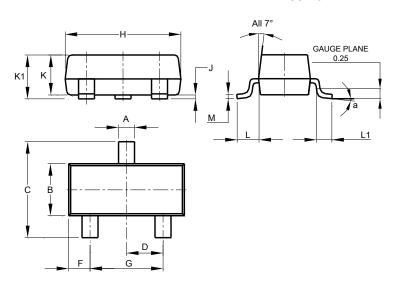
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

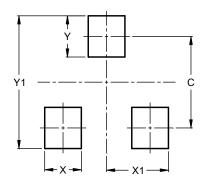


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	29



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