



# 30V N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C	
	3.8mΩ @ V <sub>GS</sub> = 10V	140A	
30V	6mΩ @ V <sub>GS</sub> = 4.5V	110A	

#### **Features and Benefits**

- Low R<sub>DS(ON)</sub> Minimizes On-State Losses
- Excellent Q<sub>gd</sub> x R<sub>DS(ON)</sub> Product (FOM)
- Advanced Technology for DC-DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher **Density End Products**
- 100% Unclamped Inductive Switching Ensures More Reliability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

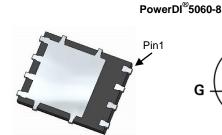
- Backlighting
- **Power Management Functions**
- DC-DC Converters

#### **Mechanical Data**

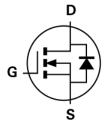
- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



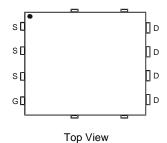
Notes:



**Bottom View** 



Internal Schematic



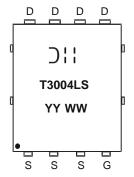
Pin Configuration

#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMT3004LPS-13	PowerDI <sup>®</sup> 5060-8	2,500/Tape & Reel

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
  2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



⊃¦¦ = Manufacturer's Marking T3004LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 15 = 2015)WW = Week (01 to 53)

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## **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		$V_{DSS}$	30	V
Gate-Source Voltage		$V_{GSS}$	+20 -16	V
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	21 17	А
Continuous Drain Current, $V_{GS} = 10V$ $ T_C = +25^{\circ}C $ $ T_C = +70^{\circ}C $		I <sub>D</sub>	140 110	А
Maximum Continuous Body Diode Forward Current (Note 5) T <sub>A</sub> = +25°C		Is	3	Α
Maximum Continuous Body Diode Forward Current $T_C = +25^{\circ}C$		I <sub>S</sub>	48	Α
Maximum Body Diode Forward Pulse Current $T_C = +25^{\circ}C$		I <sub>SM</sub>	180	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	180	Α
Avalanche Current, L=0.3mH		I <sub>AS</sub>	27	Α
Avalanche Energy, L=0.3mH	E <sub>AS</sub>	110	mJ	

#### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit	
Total Power Dissipation	T <sub>A</sub> = +25°C (Note 5)	P <sub>D</sub>	2.7	W	
	$T_C = +25^{\circ}C$		113		
Thermal Resistance, Junction to Ambient (Note 5)  Steady State		$R_{\theta JA}$	47	°C/W	
Thermal Resistance, Junction to Case		R <sub>0</sub> JC	1.1	C/VV	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

## **Electrical Characteristics** (T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		30		_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	l	1	±100	nA	$V_{GS} = +20V, V_{DS} = 0V$ $V_{GS} = -16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	_	3.8	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	1	6		$V_{GS} = 4.5V, I_D = 7A$	
Diode Forward Voltage	$V_{SD}$	_	0.70	1	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C <sub>iss</sub>	_	2,370	_		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss	-	1,360	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	240	_			
Gate Resistance	Rg	_	0.7	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	43.7	_			
Gate-Source Charge	Q <sub>gs</sub>	_	6.9	_	nC	$V_{DS} = 15V, I_D = 20A$	
Gate-Drain Charge	$Q_{gd}$	_	8	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.2	_			
Turn-On Rise Time	t <sub>R</sub>	_	4.2	_		$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	21	_	ns	$R_G=3\Omega,R_L=0.75\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	8	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	25	_	ns L 454 divis 5004/ss		
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	37	_	nC	$I_F = 15A$ , di/dt = 500A/ $\mu$ s	

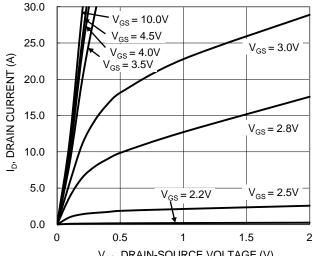
Notes: 5. R<sub>BJA</sub> is determined with the device mounted on FR-4 substrate PC board, 2oz copper, with 1in. square copper plate. R<sub>BJC</sub> is guaranteed by design while R<sub>BJA</sub> is determined by the user's board design.

<sup>6.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>7.</sup> Guaranteed by design. Not subject to product testing.









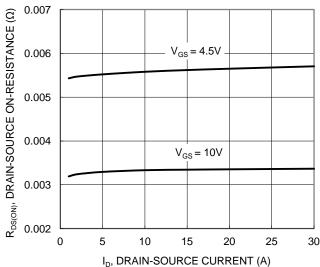


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

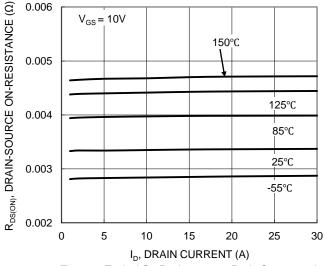


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

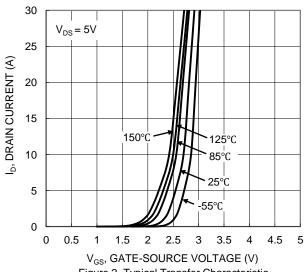
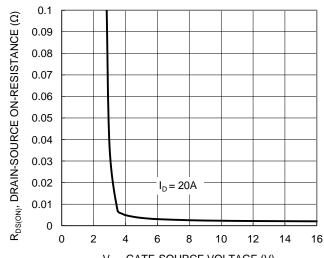
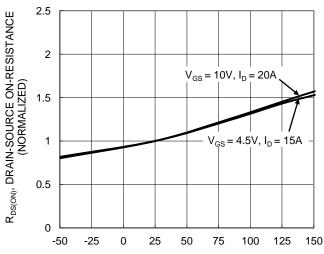


Figure 2. Typical Transfer Characteristic



V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V) Figure 4. Typical Transfer Characteristic



 $\rm T_{\rm J}, JUNCTION$  TEMPERATURE (°C) Figure 6. On-Resistance Variation with Junction Temperature





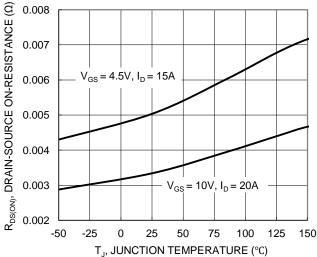
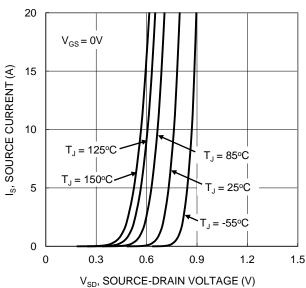
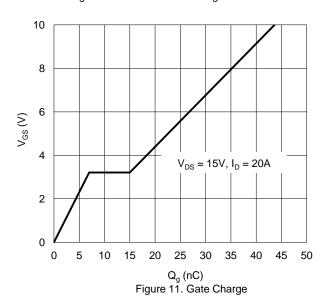


Figure 9. On-Resistance Variation with Junction Temperature



V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V)
Figure 9. Diode Forward Voltage vs. Current



DM13004LPS

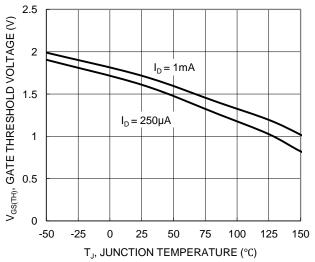


Figure 8. Gate Threshold Variation vs. Junction Temperature

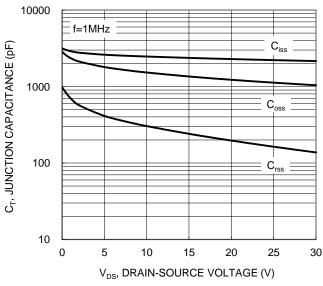
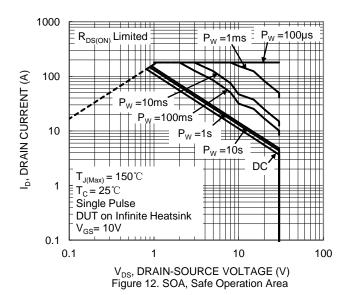


Figure 10. Typical Junction Capacitance





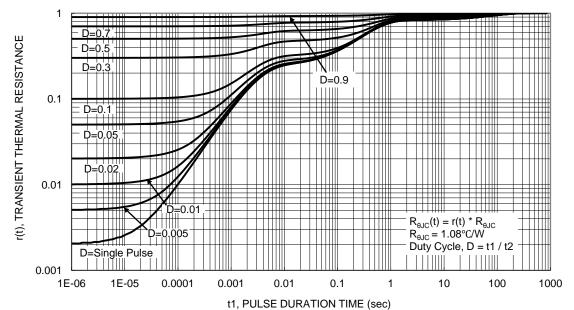


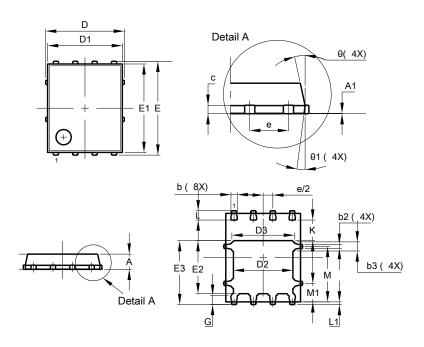
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.

#### POWERDI®5060-8

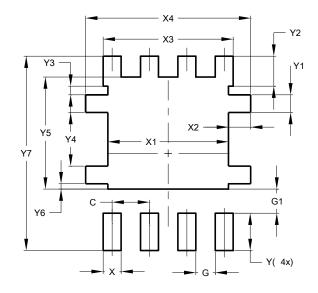


POWERDI®5060-8				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0.00	0.05	_	
b	0.33	0.51	0.41	
b2	0.200	0.350	0.273	
b3	0.40	0.80	0.60	
С	0.230	0.330	0.277	
D	5.15 BSC			
D1	4.70	5.10	4.90	
D2	3.70	4.10	3.90	
D3	3.90	4.30	4.10	
Е	6	.15 BS	$\circ$	
E1	5.60	6.00	5.80	
E2	3.28	3.68	3.48	
E3	3.99	4.39	4.19	
е	1.27 BSC			
G	0.51	0.71	0.61	
K	0.51	ı	_	
L	0.51	0.71	0.61	
L1	0.100	0.200	0.175	
М	3.235	4.035	3.635	
М1	1.00	1.40	1.21	
Θ	10°	12°	11°	
Θ1	6°	8°	7°	
All Dimensions in mm				

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.

#### POWERDI®5060-8



Dimensions	Value (in mm)				
С	1.270				
G	0.660				
G1	0.820				
Х	0.610				
X1	4.100				
X2	0.755				
Х3	4.420				
X4	5.610				
Υ	1.270				
Y1	0.600				
Y2	1.020				
Y3	0.295				
Y4	1.825				
Y5	3.810				
Y6	0.180				
Y7	6.610				



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