



#### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
-20V	$28m\Omega$ @ $V_{GS} = -4.5V$	-6.1A
	$43m\Omega$ @ V <sub>GS</sub> = -2.5V	-4.9A

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Load Switch

### **Features**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

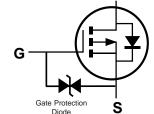
### **Mechanical Data**

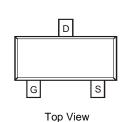
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (§3)
- Terminal Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)





SOT23





Top View

Internal Schematic

### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMP2037U-7	SOT23	3,000 / Tape & Reel
DMP2037U-13	SOT23	10,000 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

## **Marking Information**



MP4 = Product Type Marking Code YM = Date Code Marking Y or $\overline{Y}$  = Year (ex: H = 2020) M = Month (ex: 9 = September)

#### Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н	ı	J	K	L	М	N	0	Р	R	S
								A	Sep	Oct	Nov	Dec
Month	lan	Eah	Mar	Anr	May							
MOTILII	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	þ	5	100	1



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage	VDSS	-20	V	
Gate-Source Voltage		Vgss	±10	V
Continuous Drain Current (Note 6) VGS = -4.5V	lο	-6.1 -4.8	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-38	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	•	Is	-2.2	Α

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		$P_{D}$	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	158	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	81	°C/W
Thermal Resistance, Junction to Case (Note 7)		R <sub>θ</sub> JC	14.3	C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						•
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	_	_	V	$V_{GS} = 0V$ , $I_{D} = -1mA$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	1	_	-1	μA	$V_{DS} = -16V, V_{GS} = 0V$
Gate-Source Leakage	Igss	-	_	±10	μΑ	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	Vgs(TH)	-0.5	_	-1.2	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$
Static Drain-Source On-Resistance	Dagger	l	20.3	28	mΩ	$V_{GS} = -4.5V$ , $I_{D} = -2A$
Static Drain-Source On-Nesistance	RDS(ON)	l	26.5	43	11177	$V_{GS} = -2.5V$ , $I_{D} = -2A$
Diode Forward Voltage	VsD	_	-0.66	-1.1	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = -1A
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	Ciss	I	803	_		101/11/ 01/
Output Capacitance	Coss		114	_	pF	$V_{DS} = -10V$ , $V_{GS} = 0V$ f = 1MHz
Reverse Transfer Capacitance	Crss	_	51	_		I = IIVII IZ
Gate Resistance	Rg	_	65.5	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = -8V)	$Q_g$	_	14.5	_		
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	8.4	_	nC	\/ 40\/ I- 20A
Gate-Source Charge	Qgs	_	1.5	_	IIC	$V_{DD} = -10V, I_{D} = -20A$
Gate-Drain Charge	$Q_{gd}$	_	1.9	_		
Turn-On Delay Time	td(ON)	_	12	_		
Turn-On Rise Time	t <sub>R</sub>	_	6	_	200	$V_{GS} = -4.5V, V_{DD} = -10V,$
Turn-Off Delay Time	tD(OFF)		81	_	ns	$R_G = 1\Omega$ , $I_D = -10A$
Turn-Off Fall Time	tF	_	46	_		

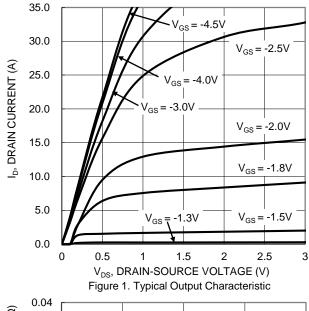
Notes:

<sup>5.</sup> Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
7. Thermal resistance from junction to soldering point (on the exposed drain pad).
8. I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.

<sup>9.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>10.</sup> Guaranteed by design. Not subject to product testing.





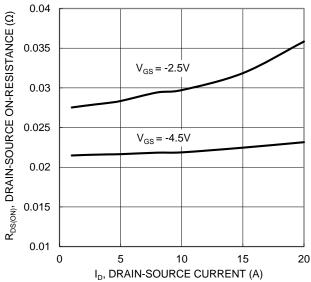


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

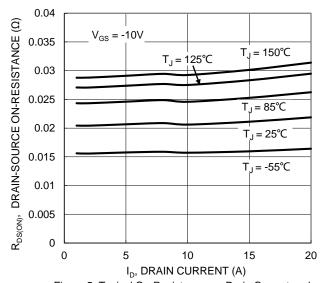
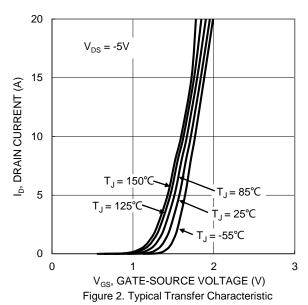
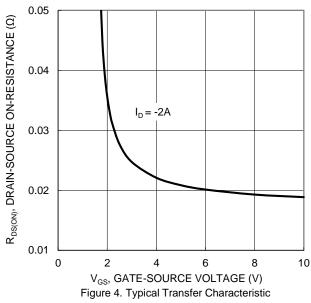


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





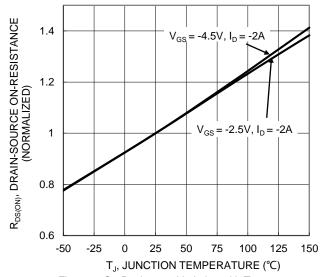


Figure 6. On-Resistance Variation with Temperature



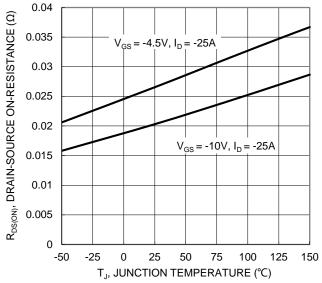


Figure 7. On-Resistance Variation with Temperature

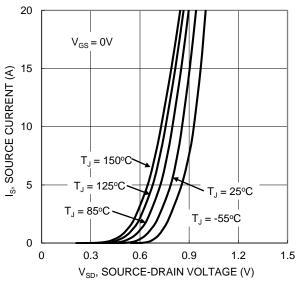


Figure 9. Diode Forward Voltage vs. Current

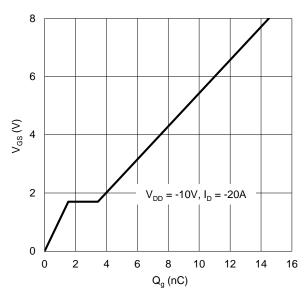


Figure 11. Gate Charge

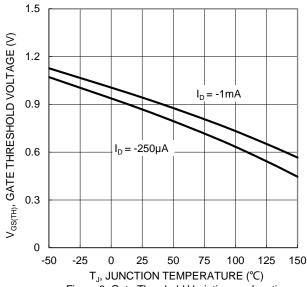
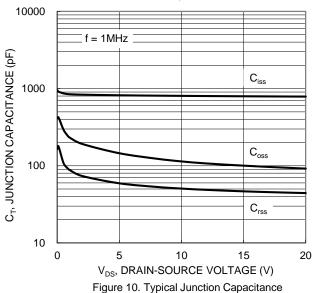


Figure 8. Gate Threshold Variation vs. Junction Temperature



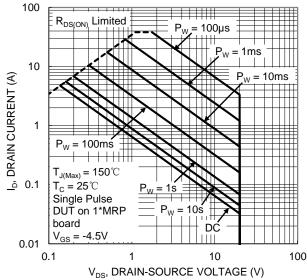


Figure 12. SOA, Safe Operation Area



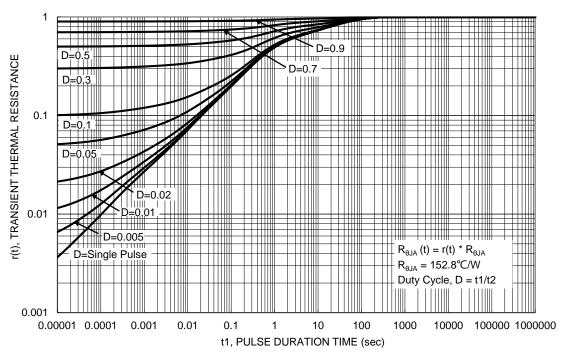


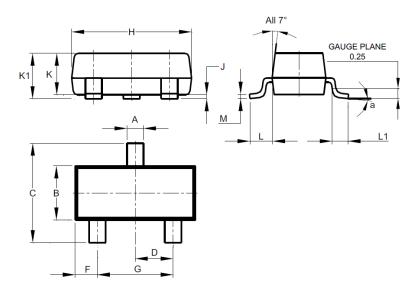
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23

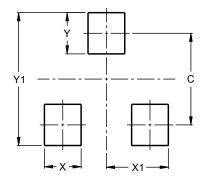


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
C	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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