

Features

- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Leakage Current
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/guality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DFLS240Q</u>)

Mechanical Data

- Package: PowerDI[®]123
- Package Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (63)
- Weight: 0.01 grams (Approximate)



Top View

Ordering Information (Note 4)

Part Number	Package	Packing			
Fait Nullber	Fackage	Qty.	Carrier		
DFLS240-7	PowerDI123	3000	Tape & Reel		

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



F04A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 9 = September)

Date Code Key

Year	2006		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Т		J	К	L	М	Ν	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} Vrwm Vr	40	V
RMS Reverse Voltage	Vr(rms)	28	V
Average Forward Current	lf(AV)	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	40	А

Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Ambient Air (Note 5)	Reja	73	—	°C/W
Thermal Resistance, Junction to Soldering Point (Note 6)	R _{ejs}	—	13	°C/W
Operating Temperature Range	TJ	-65 to	+125	°C
Storage Temperature Range	Tstg	-65 to	+150	°C

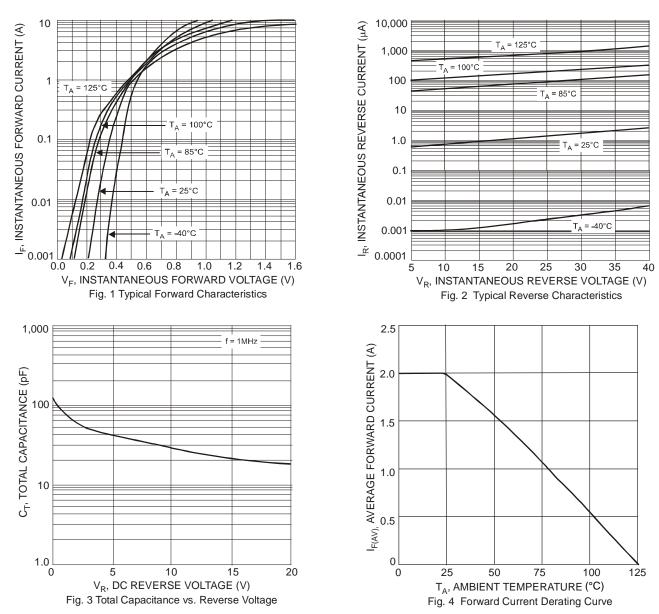
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V _{(BR)R}	40	—	_	V	$I_R = 20\mu A$
Forward Voltage	VF		0.52 0.65	0.58 0.7	V	IF = 1.0A IF = 2.0A
Leakage Current (Note 7)	IR		_	20		V _R = 40V, T _A = +25°C
	IK	—	—	6.0	mA	$V_R = 40V, T_A = +100^{\circ}C$
Total Capacitance	Ст	_	28	—	pF	$V_{R} = 10V, f = 1.0MHz$

Notes:

5. Part mounted on Polymide board with 2oz copper, 74mm² pad layout. $T_A = +25^{\circ}C$. 6. Theoretical R_{0JS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction. 7. Short duration pulse test used to minimize self-heating effect.

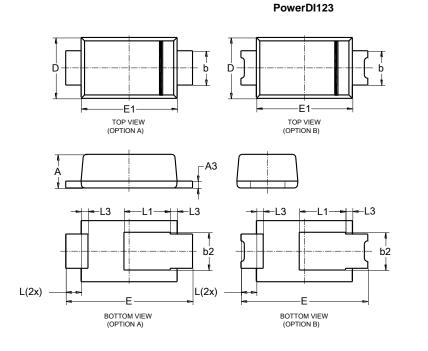






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

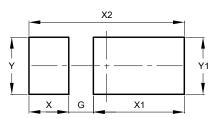


PowerDI123						
Dim	Min	Max	Тур			
Α	0.93	1.00	0.98			
A3	0.15	0.25	0.20			
b	0.85	1.25	1.00			
b2	1.025	1.125	1.10			
D	1.63	1.93	1.78			
E	3.50	3.90	3.70			
E1	2.60	3.00	2.80			
L	0.40	0.50	0.45			
L1	1.25	1.40	1.35			
L3	0.125	0.275	0.20			
All I	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
Х	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50



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