





### NPN SURFACE MOUNT TRANSISTOR

## **Features**

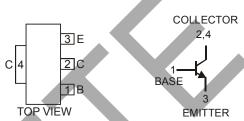
- Epitaxial Planar Die Construction
- Complementary PNP Type Available (DCX52)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

## **Mechanical Data**

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking & Type Code Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)



SOT89-3L



Schematic and Pin Configuration

# Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	60	V
Collector-Emitter Voltage	V <sub>CEO</sub>	60	V
Emitter-Base Voltage	V <sub>EBO</sub>	5	V
Peak Pulse Current	I <sub>CM</sub>	1.5	А
Continuous Collector Current	Ic	1	A

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	P <sub>D</sub>	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ T <sub>A</sub> = 25°C	$R_{ heta JA}$	125	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics @TA = 25°C unless otherwise specified

Charac	teristic	Symbol	Min	Тур	Max	Unit	Test Conditions
OFF CHARACTERISTICS (Note 4)							
Collector-Base Breakdown Vo	Itage	$V_{(BR)CBO}$	60	_	_	V	$I_C = 100 \mu A, I_E = 0A$
Collector-Emitter Breakdown	/oltage	V <sub>(BR)CEO</sub>	60	_	_	V	I <sub>C</sub> = 10mA, I <sub>B</sub> = 0A
Emitter-Base Breakdown Volta	age	$V_{(BR)EBO}$	5	_	_	V	$I_E = 10 \mu A, I_C = 0 A$
Collector Cut-off Current		I <sub>CBO</sub>	_	_	100	nA	$V_{CB} = 30V, I_{E} = 0$
				_	20	μΑ	V <sub>CB</sub> = 30V, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C
Emitter Cut-off Current		I <sub>EBO</sub>			100	nA	$V_{EB} = 5V$ , $I_C = 0A$
ON CHARACTERISTICS (Note 4)							
Collector-Emitter Saturation V	oltage	$V_{CE(SAT)}$		_	0.5	V	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$
Base-Emitter Turn-On Voltage		V <sub>BE(ON)</sub>			1.0	V	I <sub>C</sub> = 500mA, V <sub>CE</sub> = 2V
DC Current Gain	DCX55, DCX55-16	h <sub>FE</sub>	63	_	_	_	I <sub>C</sub> = 5mA, V <sub>CE</sub> = 2V
	DCX33, DCX33-10		40	_	_		$I_C = 500 \text{mA}, V_{CE} = 2V$
	DCX55		63	_	250		I <sub>C</sub> = 150mA, V <sub>CE</sub> = 2V
	DCX55-16		100	_	250	_	I <sub>C</sub> = 150mA, V <sub>CE</sub> = 2V
SMALL SIGNAL CHARACTERISTICS							
Transition Frequency		f⊤		200	_	MHz	$I_{C}$ = 50mA, $V_{CE}$ = 5V, f = 100MHz
Output Capacitance	_	$C_{obo}$	_	_	15	pF	V <sub>CB</sub> = 10V, f = 1MHz

Notes:

- No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- 3. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Measured under pulsed conditions. Pulse width =  $300\mu$ s. Duty cycle  $\leq 2\%$ .



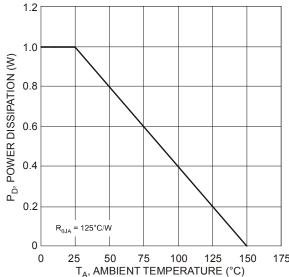


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 3)

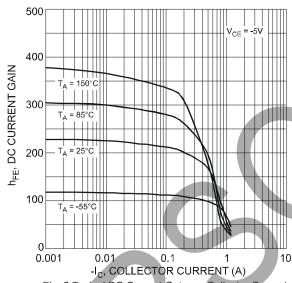


Fig. 3 Typical DC Current Gain vs. Collector Current

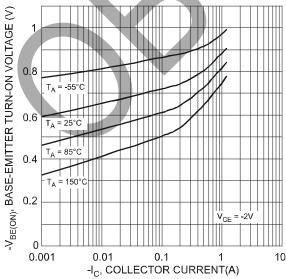
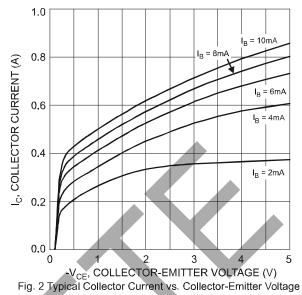


Fig 5. Typical Base-Emitter Turn-On Voltage vs. Collector Current



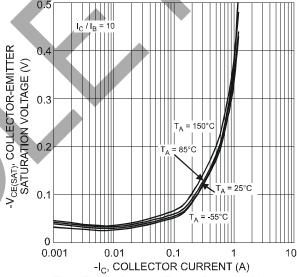


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

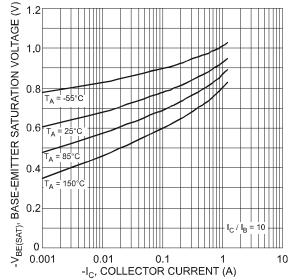


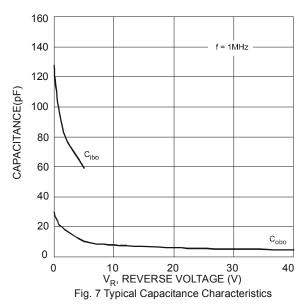
Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

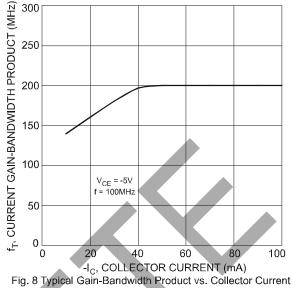
2 of 5 www.diodes.com

DCX55/-16

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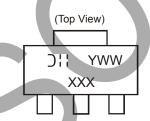


# **Ordering Information** (Note 5)

Device	Packaging	Shipping
DCX55-13	SOT89-3L	2500/Tape & Reel
DCX55-16-13	SOT89-3L	2500/Tape & Reel

5. For packaging details, go to our website at http://www.diodes.com/ap02007.pdf.

# **Marking Information**

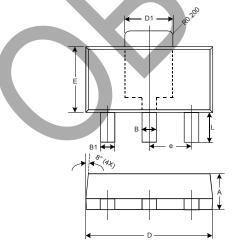


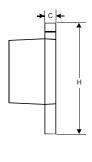
☐ = Manufacturer's code marking XXX = Product type marking code Ex:

YWW = Date code marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

N16 = DCX55 N16-16 = DCX55 -16

# **Package Outline Dimensions**

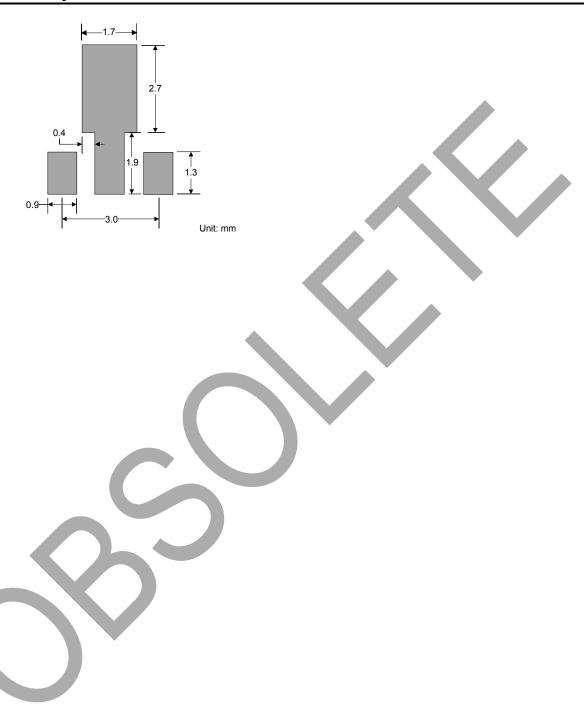




SOT89-3L					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.45	0.55	0.50		
B1	0.37	0.47	0.42		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.50	1.70	1.60		
E	2.40	2.60	2.50		
е	_	_	1.50		
Н	3.95	4.25	4.10		
L	0.90	1.20	1.05		
All Dimensions in mm					



# Suggested Pad Layout





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