



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _{D Max} T _A = +25°C
60V	16mΩ @ V _{GS} = 10V	8.9A
000	$27m\Omega @ V_{GS} = 4.5V$	6.8A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

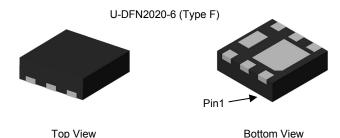
- Load Switch
- Adaptor Switch
- Notebook PC

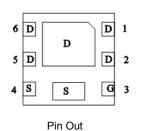
Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test in Production
- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

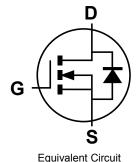
Mechanical Data

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 4
- Weight: 0.007 grams (Approximate)





Bottom View



Ordering Information (Note 4)

Part Number	Marking	Reel Size (inches)	Quantity per Reel
DMT6016LFDF-7	T6	7	3000
DMT6016LFDF-13	T6	13	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



Marking Information

Site 1:



T6 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Ī	Year	2013	 2019	2020	2021	2022	2023	2024	2025	2026	2027	2028
	Code	Α	 G	Н		J	K	L	М	N	0	Р

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Site 2:



T6 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: H = 2020) W = Week (ex: a = week 27; z represents week 52 and 53) X = Internal code (ex: U = Monday)

Date Code Key

Ī	Year	2020	2021	2022	2023	2024	2025	2026	2027	2028
	Code	0	1	2	3	4	5	6	7	8

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	Х	Υ	Z



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	60	V		
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Prain Current (Note 6) / = 10//	Steady State	T _A = +25°C T _A = +70°C	ΙD	8.9 7.1	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		I _D	11.1 8.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	60	Α
Maximum Body Diode Continuous Current		Is	2.2	Α	
Avalanche Current (Note 7) L = 0.1mH	valanche Current (Note 7) L = 0.1mH			15.3	Α
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	11.7	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	В	0.82	W	
Total Power Dissipation (Note 5)	T _A = +70°C	P_{D}	0.52		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0,JA}	153	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	Көја	97	C/VV	
Total Power Discination (Note 6)	$T_A = +25^{\circ}C$	В	1.9	- w	
Total Power Dissipation (Note 6)	T _A = +70°C	P_{D}	1.2	٧٧	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	66	°C/W	
Themal Resistance, Junction to Ambient (Note 0)	t<10s	$R_{\theta JA}$	42		
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	14.7		
Operating and Storage Temperature Range		$T_{J_{I}}T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV_{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(TH)}$	1.0	_	3.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance)	_	12.2	16	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}		17.2	27	11122	$V_{GS} = 4.5V, I_D = 6A$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}		864	_		V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	282	_	pF		
Reverse Transfer Capacitance	Crss	_	27.1	_		1 - 1.000112	
Gate Resistance	R_g	_	1.35	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg		17	_			
Total Gate Charge (V _{GS} = 4.5V)	Q_g		8.4	_	nC	V _{DS} = 30V, I _D = 10A	
Gate-Source Charge	Q_{gs}	_	3.1	_	IIC	V _{DS} = 30V, I _D = 10A	
Gate-Drain Charge	Q_{gd}		4.3	_			
Turn-On Delay Time	t _{D(ON)}		3.4	_			
Turn-On Rise Time	t_R		5.2	_	nS	$V_{GS} = 10V, V_{DD} = 30V, R_q = 6\Omega,$	
Turn-Off Delay Time	t _{D(OFF)}	_	12.9	_	113	I _D = 10A	
Turn-Off Fall Time	t _F		6.8	_			
Body Diode Reverse Recovery Time	t _{RR}		22	_	nS	I _S = 10A, dI/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q_{RR}		11.1	_	nC	$I_S = 10A$, $dI/dt = 100A/\mu s$	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

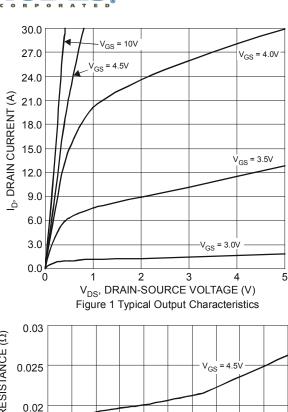
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

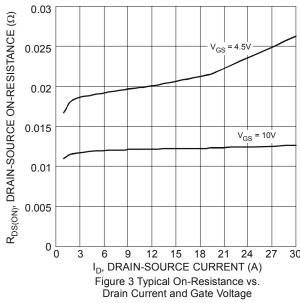
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.

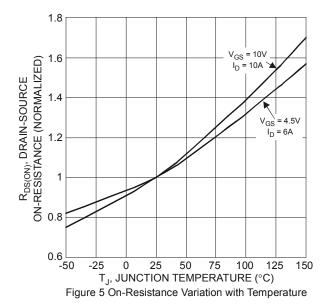
8. Short duration pulse test used to minimize self-heating effect.

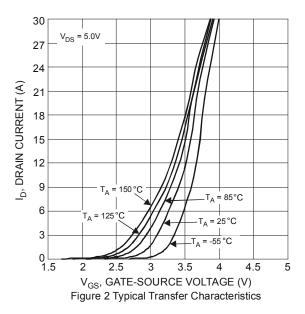
9. Guaranteed by design. Not subject to product testing.

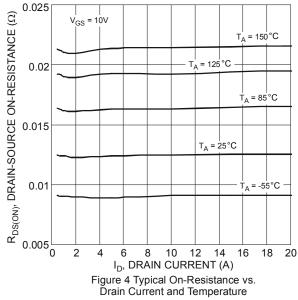


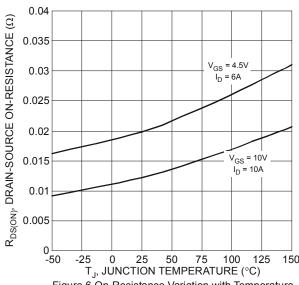














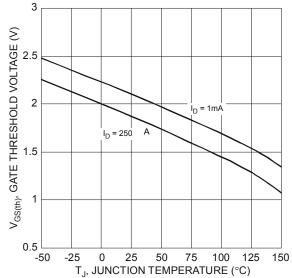


Figure 7 Gate Threshold Variation vs. Ambient Temperature

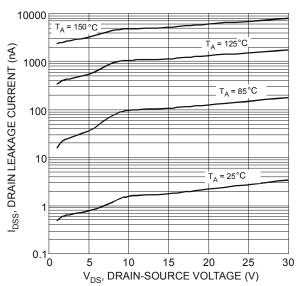
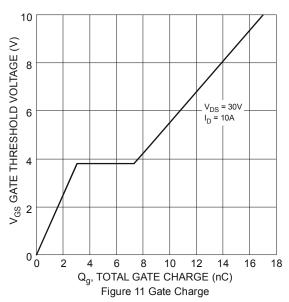
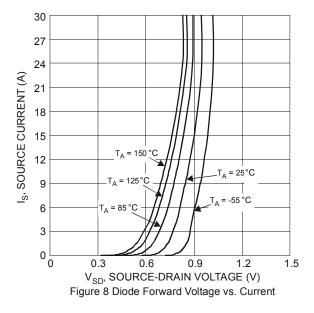
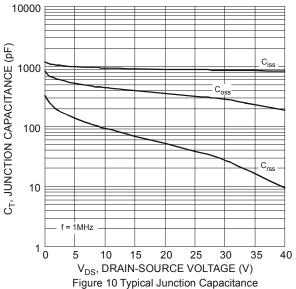
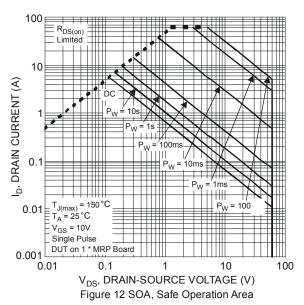


Figure 9 Typical Drain-Source Leakage Current vs. Voltage

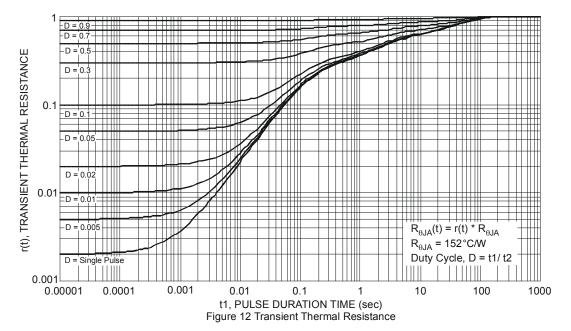










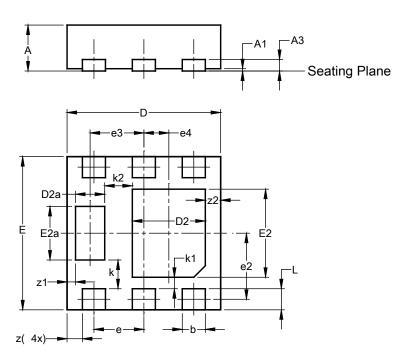




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

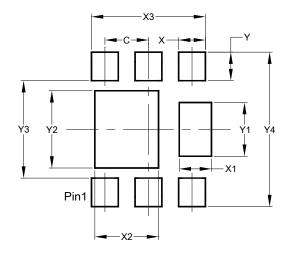


	U-DFN	2020-6			
		oe F)			
Dim	Min	Max	Тур		
Α	0.57	0.63	0.60		
A1	0.00	0.05	0.03		
A3	-	1	0.15		
b	0.25	0.35	0.30		
D	1.95	2.05	2.00		
D2	0.85	1.05	0.95		
D2a	0.33	0.43	0.38		
Е	1.95	2.05	2.00		
E2	1.05	1.25	1.15		
E2a	0.65	0.75	0.70		
е		0.65 BS	С		
e2	().863 BS	SC		
е3		0.70 BS	С		
e4	().325 BS	SC		
k		0.37 BS	С		
k1		0.15 BS	С		
k2		0.36 BS	С		
L	0.225	0.325	0.275		
Z		0.20 BS	С		
z1	0.110 BSC				
z2		0.20 BS	С		
All C	imens	ions in	mm		

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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