



60V 175°C P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-60V	155mΩ @ V _{GS} = -10V	-2.4A
	240mΩ @ V _{GS} = -4.5V	-1.9A

Features and Benefits

- Rated to +175°C—Ideal for High Ambient Temperature Environments
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

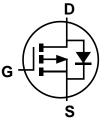
- Battery Charging
- Power Management Functions
- DC-DC Converters
- Load Switch

Mechanical Data

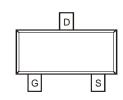
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



Top View



Internal Schematic



Top View

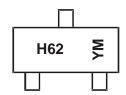
Ordering Information (Note 5)

Part Number	Case	Packaging
DMPH6250SQ-7	SOT23	3000/Tape & Reel
DMPH6250SQ-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



H62 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	2017		2018	2019		2020	2021		2022	2023	1	2024
Code	Е		F	G		Н			J	K		L
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-60	V	
Gate-Source Voltage		V_{GSS}	±20	V	
Continuous Drain Current (Note 7) $V_{GS} = -10V$ Steady $T_A = +25^{\circ}C$ State $T_A = +100^{\circ}C$			I _D	-2.4 -1.5	А
Pulsed Drain Current (380µs Pulse, Duty Cycle =	: 1%)		I _{DM}	-13	A
Maximum Continuous Body Diode Forward Curre	ent (Note 7	7)	Is	-1.6	A
Pulsed Body Diode Forward Current (380µs Puls	e, Duty C	ycle = 1%)	I _{SM}	-13	A

Thermal Characteristics

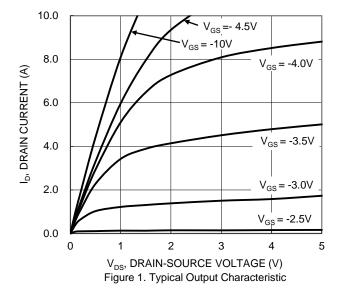
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P _D	0.92	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	R _{OJA}	165	°C/W
Power Dissipation (Note 7)	P _D	1.62	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7)	R _{OJA}	93.1	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-60	_		V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}		_	-1.0	μΑ	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	1	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-1.0	-1.9	-3.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	D	1	112	155	mΩ	$V_{GS} = -10V, I_D = -2A$	
Static Dialii-Source Off-Nesistance	R _{DS(ON)}		149	240	11152	$V_{GS} = -4.5V$, $I_D = -2A$	
Diode Forward Voltage	V_{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}		512	_	pF	.,	
Output Capacitance	Coss	1	31.3	_	pF	$V_{DS} = -30V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	C_{rss}	I	23.2	_	рF	1 – 1.0101112	
Gate Resistance	R_g	I	11.9	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Q_g	1	4.0	_	nC		
Total Gate Charge (V _{GS} = -10V)	Q_{g}	_	8.3	_	nC	V 20V I 24	
Gate-Source Charge	Q_{gs}	_	1.2	_	nC	$V_{DS} = -30V, I_{D} = -2A$	
Gate-Drain Charge	Q_{gd}	_	1.7		nC		
Turn-On Delay Time	t _{D(ON)}	_	12.5	_	ns		
Turn-On Rise Time	t _R	_	13.4	_	ns	$V_{DD} = -30V, V_{GS} = -10V,$	
Turn-Off Delay Time	t _{D(OFF)}		96.0	_	ns	$I_D = -1.0A, R_G = 50\Omega$	
Turn-Off Fall Time	t _F	_	39.1	_	ns	1	
Body Diode Reverse Recovery Time	t _{RR}	ı	9.6	_	ns	I _F = -1A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q_{RR}	_	3.1	_	nC	$I_F = -1A$, di/dt = 100A/ μ s	

- 6. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
 7. Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.





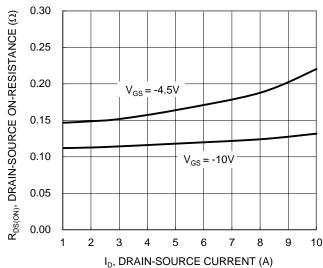


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

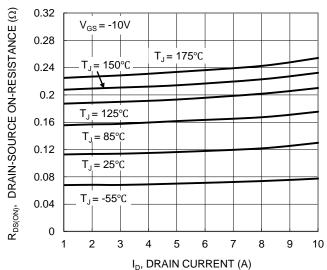
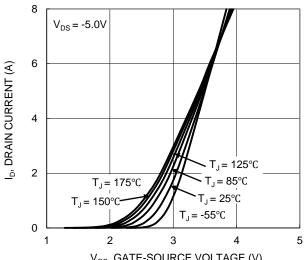


Figure 5. Typical On-Resistance vs. Dráin Current and Temperature



 V_{GS} , GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

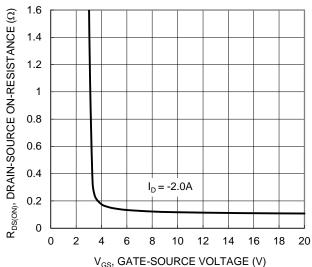


Figure 4. Typical Transfer Characteristic

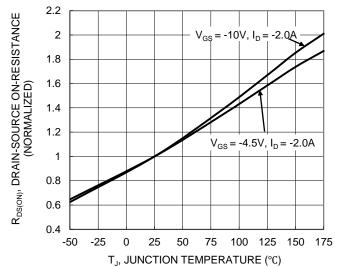


Figure 6. On-Resistance Variation with Temperature



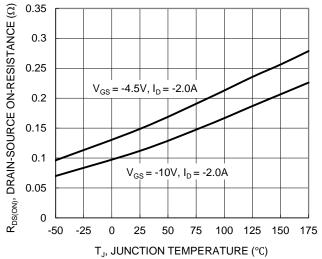
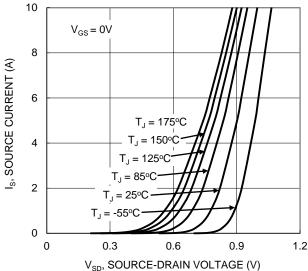


Figure 7. On-Resistance Variation with Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

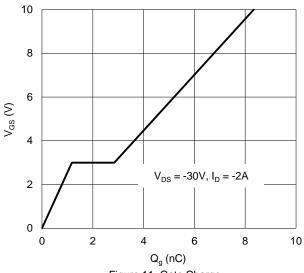


Figure 11. Gate Charge

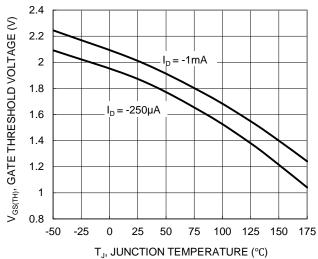


Figure 8. Gate Threshold Variation vs. Junction Temperature

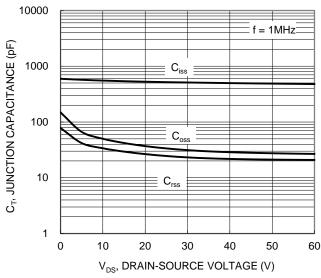


Figure 10. Typical Junction Capacitance

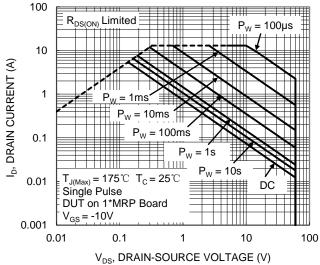


Figure 12. SOA, Safe Operation Area



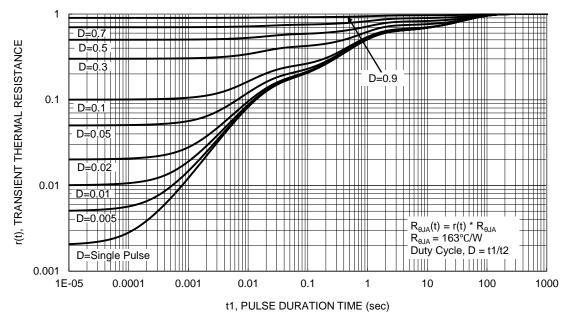
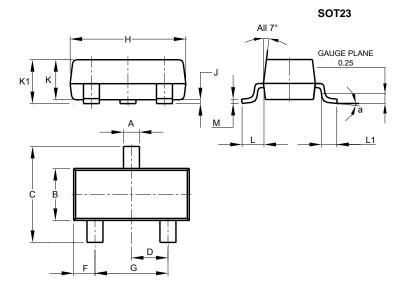


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

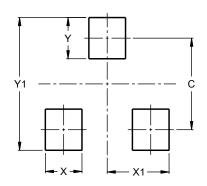
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
а	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Υ	0.9
Y1	2.9

SOT23

May 2018



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