

NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART



DMS3014SFG

30V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	13mΩ @ V _{GS} = 10V	9.5A
30V	14mΩ @ V _{GS} = 4.5V	9.0A

Features and Benefits

- DIOFET utilizes a unique patented process to monolithically integrate a MOSFET and a Schottky in a single die to deliver:
 - Low R_{DS(ON)} minimize conduction losses
 - Low V_{SD} reducing the losses due to body diode conduction
 - Low Q_{RR} lower Q_{RR} of the integrated Schottky reduces body diode switching losses
 - Low gate capacitance (Q_g/Q_{gs}) ratio reduces risk of shootthrough or cross conduction currents at high frequencies
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- 100% UIS (Avalanche) Rated
- 100% R_d Tested
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMS3014SFGQ)

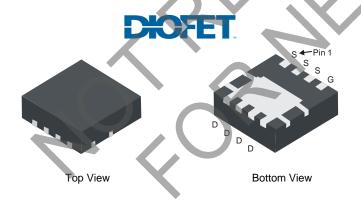
Description and Applications

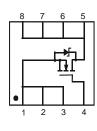
This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

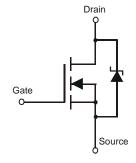
Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)









Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging		
DMS3014SFG-7	PowerDI3333-8	2000/Tape & Reel		
DMS3014SFG-13	PowerDI3333-8	3000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

PowerDI is a registered trademark of Diodes Incorporated.

DMS3014SFG

Marking Information



S29 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 18 = 2018) WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±12	V		
Continuous Dunin Courset (Note C) // 40)/	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	9.5 7.6	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	13.0 9.7	А
Continuous Dunin Courset (Note C) // 45/	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	l _D	9.0 7.4	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	12.2 9.3	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	80	А		
Maximum Continuous Body Diode Forward Current (I	Is	3.0	А		
Avalanche Current (Note 7) L = 0.1mH			I _{AR}	30	А
Repetitive Avalanche Energy (Note 7) L = 0.1mH			ÉAR	45	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

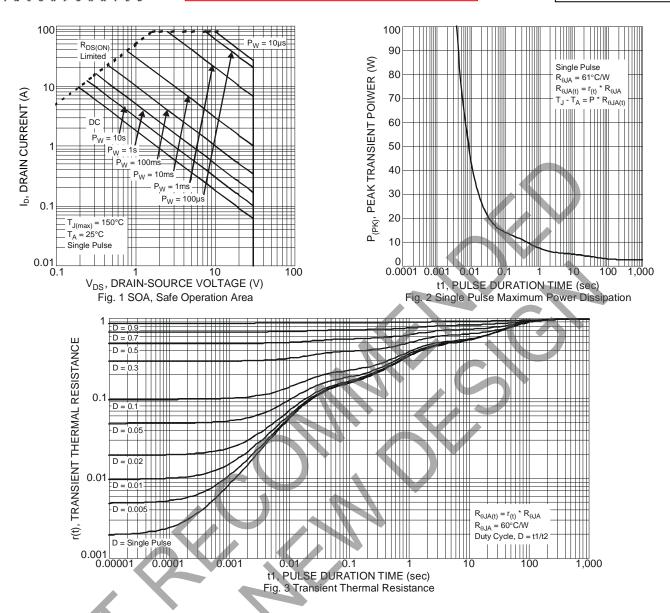
Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		P_{D}	1	W
Thermal Resistance, Junction to Ambient (Note 5) Steady Statute 10s		$R_{ hetaJA}$	131	°C/W
			72	°C/W
Total Power Dissipation (Note 6)		P_{D}	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)		D	63	°C/W
Thermal Resistance, sunction to Ambient (Note o)	t<10s	$R_{ heta JA}$	35	°C/W
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	7.1	°C/W	
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C	

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep T_J = +25°C.









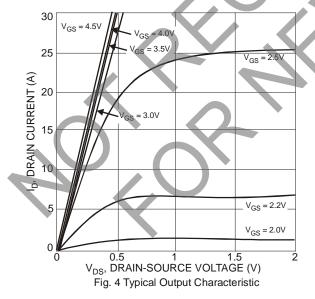


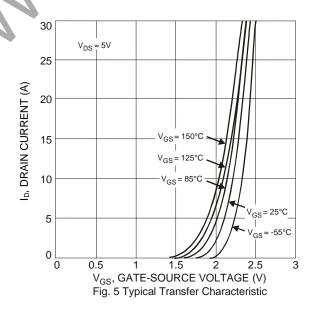
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	100	μΑ	$V_{DS} = 30V$, $V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	2.2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	D	_	9	13	mΩ	$V_{GS} = 10V, I_D = 10.4A$
Static Dialit-Source Off-Resistance	R _{DS(ON)}	_	10	14	11122	$V_{GS} = 4.5V$, $I_D = 10.4A$
Forward Transfer Admittance	Y _{fs}	_	23	_	S	$V_{DS} = 5V, I_D = 10.4A$
Diode Forward Voltage	V _{SD}	_	0.4	0.55	V	$V_{GS} = 0V$, $I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	_	2296	4310	pΕ	V 45V V 9V
Output Capacitance	Coss	_	164	_	ρF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	120		рF	1.0W112
Gate Resistance	Rg	0.26	1.3	2.34	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge V _{GS} = 4.5V	Qg	_	19.3	1	nC	
Total Gate Charge V _{GS} = 10V	Qg	_	45.7	1	nC	$V_{DS} = 15V$, $V_{GS} = 10V$, $I_{D} = 10.4A$
Gate-Source Charge	Qgs	_	5.0	_	nC	$V_{DS} = 15V, V_{GS} = 10V, I_{D} = 10.4A$
Gate-Drain Charge	Q_{gd}	_	2.9		nC	
Turn-On Delay Time	t _{D(ON)}		5.5	<i></i>	ns	
Turn-On Rise Time	t _R	-//	24.4	_	ns	$V_{GS} = 10V, V_{DS} = 15V,$
Turn-Off Delay Time	t _{D(OFF)}	N-1	33.1		ns	$R_G = 3\Omega$, $R_L = 1.2\Omega$
Turn-Off Fall Time	t _F	1-1	6.6	7-6	ns	*
Reverse Recovery Time	t _{RR}		12.9	-	ns	I _F = 13A, di/dt = 500A/µs
Reverse Recovery Charge	Q_{RR}		8.0	-	nC	I _F = 13A, di/dt = 500A/μs

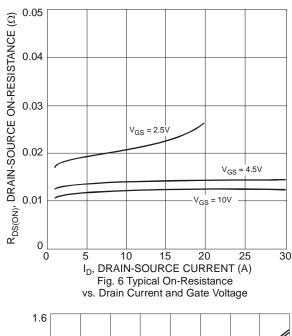
Notes: 8. Short duration pulse test used to minimize self-heating effect.

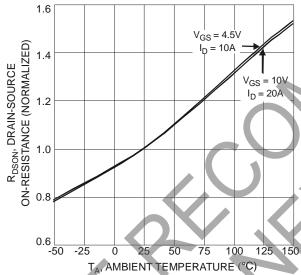
9. Guaranteed by design. Not subject to product testing.











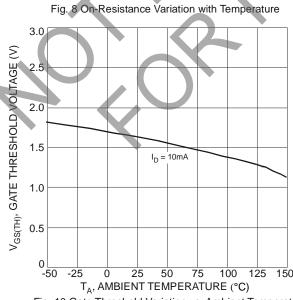
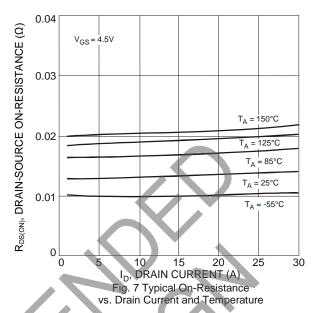


Fig. 10 Gate Threshold Variation vs. Ambient Temperature



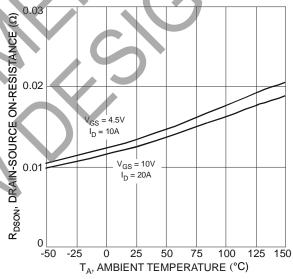


Fig. 9 On-Resistance Variation with Temperature

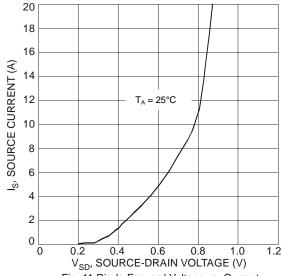
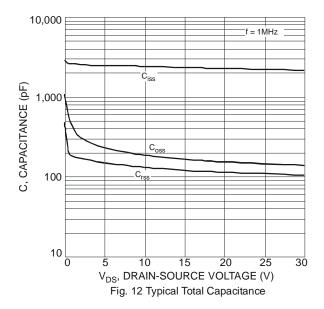


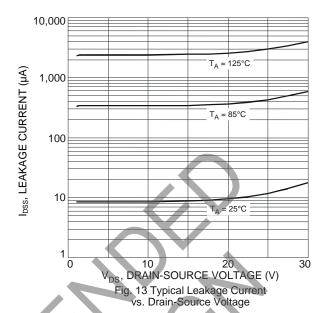
Fig. 11 Diode Forward Voltage vs. Current

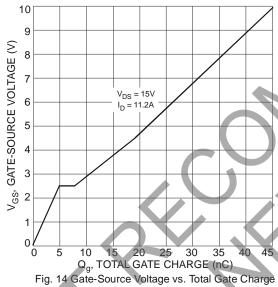


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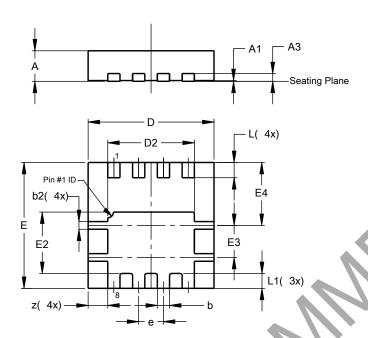




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8

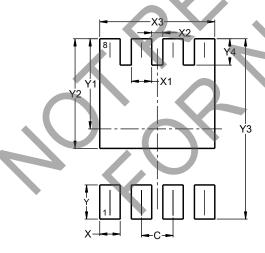


PowerDI3333-8						
Dim	Min	in Max T				
A	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	_		0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
П	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
E	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
Ð	-6	=	0.65			
4	0.35	0.45	0.40			
L1		_	0.39			
Z	-	_	0.515			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)		
С	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
Х3	2.370		
Ý	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		



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