



Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	l _D max T _A = +25°C
20V	14 mΩ @ V _{GS} = 4.5V	9 A
200	20 mΩ @ V _{GS} = 2.5V	7.5 A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Power management functions
- Load Switch

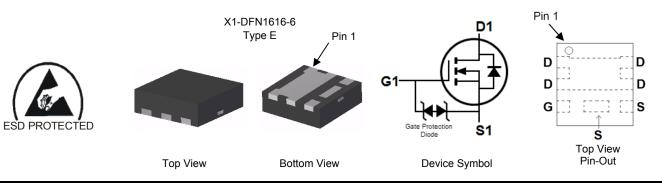
20V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Typical off board profile of 0.5mm ideally suited for thin applications
- Low R_{DS(ON)} minimizes conduction losses
- PCB footprint of 2.56mm²
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: X1-DFN1616-6 Type E
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead Free Plating (NiPdAu Finish over Copper leadframe).
- Terminals: Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.04 grams (approximate)



Ordering Information (Note 4)

Product	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN2020UFCL-7	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

20N	
YM	

20N = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key	y
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Notes:

2410 0040.109												
Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	I	3	С		D		E
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	20	V	
Gate-Source Voltage	V _{GSS}	±10	V	
Continuous Drain Current (Note 6) V_{GS} = 4.5V Steady T_A = +25°C T_A = +70°C			9 7.1	А
Pulsed Drain Current (Note 7)	I _{DM}	45	А	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

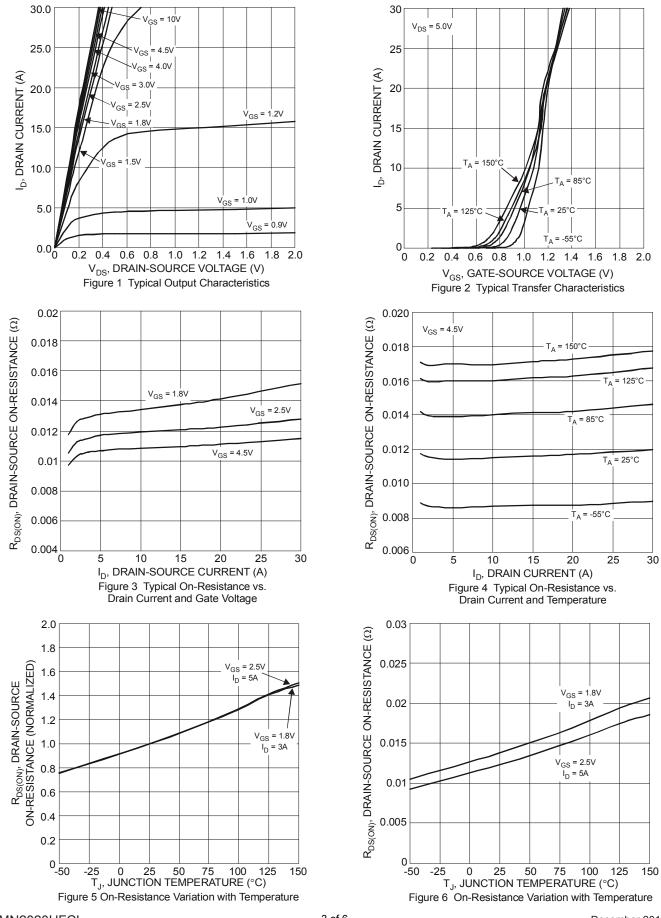
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	0.61	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{0JA}	205	°C/W
Power Dissipation (Note 6)	PD	2.0	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 6)	R _{0JA}	62	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics N-CHAI	NNEL – Q1 (@	⊉T _A = +25	5°C, unless	otherwise	e specified	l.)
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						•
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	_	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	—	1.0	μA	V _{DS} = 16V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	—	10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	0.4	—	0.9	V	V_{DS} = V_{GS} , I_D = 250 μ A
			10	14		V _{GS} = 4.5V, I _D = 9A
Static Drain-Source On-Resistance	R _{DS (ON)}	—	12	20 26	mΩ	V _{GS} = 2.5V, I _D = 7.5A
			14			V _{GS} = 1.8V, I _D = 7A
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 1.6A
DYNAMIC CHARACTERISTICS (Note 9)						<u>.</u>
Input Capacitance	Ciss	—	1788	—	pF	
Output Capacitance	C _{oss}	—	162	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	150	_	pF	
Gate Resistance	R _g	_	1.36	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Qg	_	21.5	—	nC	· · · · · · · · · · · · · · · · · · ·
Gate-Source Charge	Q _{gs}		2.2	_	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 3A
Gate-Drain Charge	Q _{gd}	_	2.3	—	nC	ID - SA
Turn-On Delay Time	t _{D(on)}	_	3.8	_	ns	
Turn-On Rise Time	tr	_	5.7	—	ns	V _{DD} = 10V, V _{GS} = 4.5V, I _D = 4A
Turn-Off Delay Time	t _{D(off)}	_	33	_	ns	$R_G = 2\Omega$
Turn-Off Fall Time	t _f	_	6.8	_	ns	1

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect. Notes:

9. Guaranteed by design. Not subject to product testing.



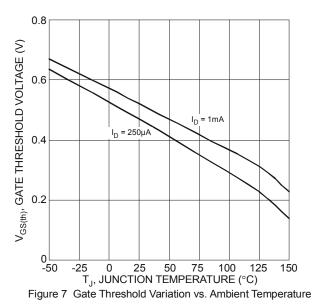


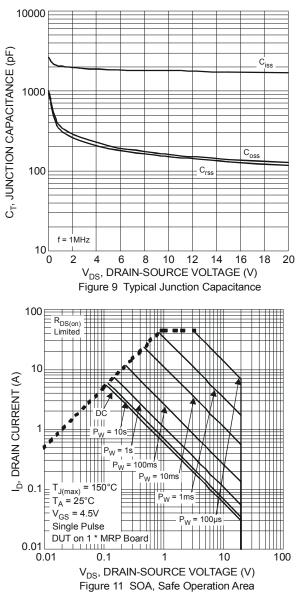
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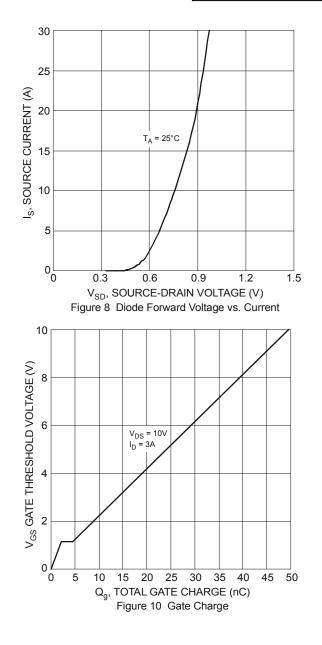
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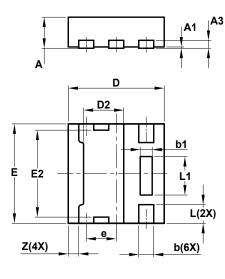


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Package Outline Dimensions

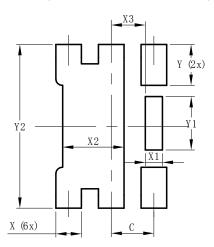
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



X1-DFN1616-6							
Туре Е							
Dim	Min Max Ty						
Α	0.47	0.53	0.50				
A1	0	0.05	0.02				
A3		I	0.13				
b	0.20	0.30	0.25				
b1	0.10	0.30	0.20				
D	1.55	1.65	1.60				
D2	0.57	0.77	0.67				
Е	1.55	1.65	1.60				
E2	1.30	1.50	1.40				
е	_	_	0.50				
L	0.25	0.35	0.30				
L1	0.52	0.72	0.62				
Z			0.175				
All	Dimens	ions in	mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)	
С	0.500	
Х	0.300	
X1	0.200	
X2	0.720	
X3	0.400	
Y	0.475	
Y1	0.620	
Y2	1.900	



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