

Description

This bipolar junction transistor (BJT) is designed to meet the stringent requirement of automotive applications

Features

- BV_{CEO} > 100V
- I_C = 6A High Continuous Collector Current
- I_{CM} = 10A Peak Collector Current
- P_D up to 3.2W
- 43% Smaller than SOT223; 60% Smaller than TO252
- Maximum Height just 1.1mm
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DXT2011P5Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

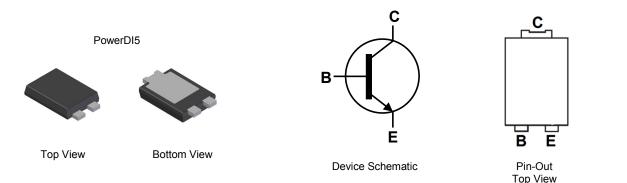
https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: PowerDI[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.093 grams (Approximate)

Applications

- Motor Drive
- Voltage Regulator Using Emitter-Follower
- DC-DC Converter
- Telecoms
- Power Management



Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DXT2011P5Q-13	Automotive	DXT2011	13	16	5000
Notes: 1 No numosely added lead. Fully FLI Directive 2002/05/FC (RoHS): 2011/65/FLI (RoHS 2) & 2015/863/FLI (RoHS 3) compliant					

No purposely added real. Fully ED Directive 2002/90/EC (RORS), 20 17/05/ED (RORS 2) & 20 15/050/ED (RORS 3) Compliant.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





DXT2011 = Product Type Marking Code D'I'= Manufacturers' Code Marking K = Factory Designator YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 for 2019) WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated. DXT2011P5Q Document number: DS39881 Rev. 2 - 2



Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	200	V
Collector-Emitter Voltage	V _{CEO}	100	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	Ι _C	6	А
Peak Pulse Current	I _{CM}	10	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 5)		3.2		
Power Dissipation	(Note 6)	PD	1.7	W	
	(Note 7)	1	0.74		
	(Note 5)		39	°C/W	
Thermal Resistance, Junction to Ambient Air	(Note 6)	R _{0JA}	75		
	(Note 7)		169		
Thermal Resistance, Junction to Leads	(Note 8)	R _{θJL}	5.6		
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C		

ESD Ratings (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

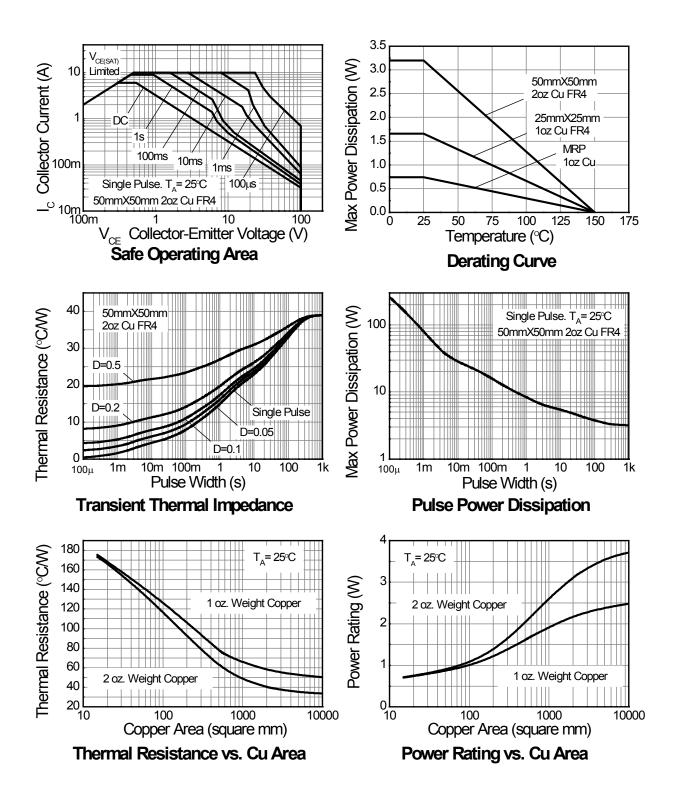
Notes: 5. For a device mounted with the exposed collector pad on 50mm × 50mm 2oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.

6. Same as Note 6, except mounted on 25mm 25mm 1oz copper.
7. Same as Note 6, except mounted on minimum recommended pad (MRP) layout.

B. Thermal resistance from junction to solder-point (on the exposed collector pad).
 Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

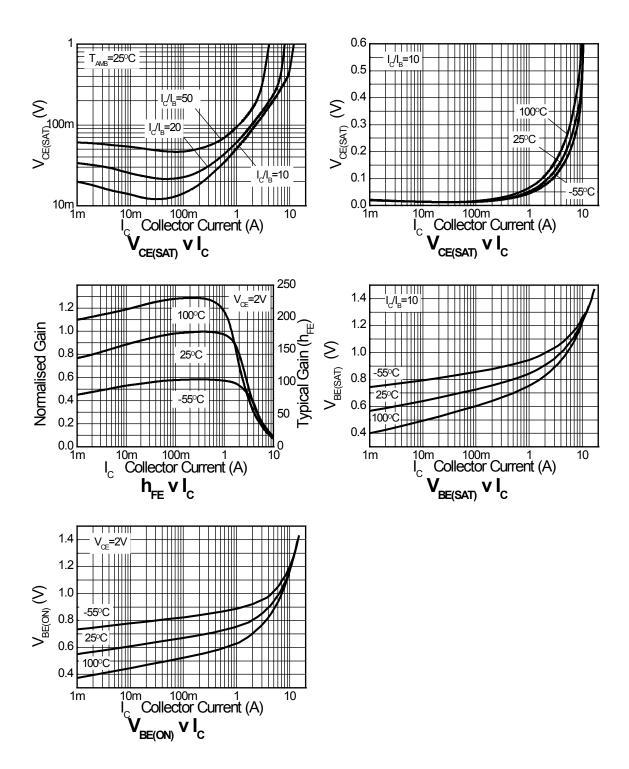
Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	200	235	_	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 10)	BV _{CEO}	100	115	_	V	I _C = 10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	7	8.1	_	V	I _E = 100μA
Collector Cutoff Current	I _{CBO}		_	20 0.5	nA µA	V _{CB} = 150V V _{CB} = 150V, T _A = +100°C
Collector Cutoff Current	I _{CER} R≤1kΩ	_	_	20 0.5	nA μA	$V_{CB} = 150V, T_A = +100^{\circ}C$ $V_{CB} = 150V, T_A = +100^{\circ}C$
Emitter Cutoff Current	I _{EBO}	_	—	10	nA	V _{EB} = 6V
Collector-Emitter Saturation Voltage (Note 10)	V _{CE(sat)}		21 50 95 180	35 65 125 220	mV	$I_{C} = 0.1A, I_{B} = 5mA$ $I_{C} = 1A, I_{B} = 100mA$ $I_{C} = 2A, I_{B} = 100mA$ $I_{C} = 5A, I_{B} = 500mA$
Base-Emitter Saturation Voltage (Note 10)	V _{BE(sat)}	_	1020	1120	mV	I _C = 5A, I _B = 500mA
Base-Emitter Turn-On Voltage (Note 10)	V _{BE(on)}	_	920	1000	mV	$V_{CE} = 2V, I_C = 5A$
DC Current Gain (Note 10)	h _{FE}	100 100 30 10	 	 300 		$V_{CE} = 2V, I_C = 10mA$ $V_{CE} = 2V, I_C = 2A$ $V_{CE} = 2V, I_C = 5A$ $V_{CE} = 2V, I_C = 10A$
Transition Frequency	f⊤	_	130	_	MHz	V _{CE} = 10V, I _C = 100mA, f = 50MHz
Output Capacitance	C _{obo}	_	26	_	pF	V _{CB} = 10V, f = 1MHz
Switching Times	t _{on} t _{off}	_	41 1010	_	ns	$V_{CC} = 10V, I_C = 1A,$ $I_{B1} = -I_{B2} = 100mA$

Note: 10. Pulse Test: Pulse width \leq 300µs. Duty cycle \leq 2.0%.



DXT2011P5Q

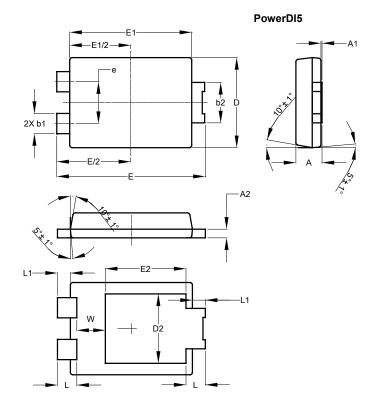
Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)





Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

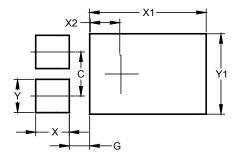


PowerDI5					
Dim	Min	Max	Тур		
Α	1.05	1.15	1.10		
A1	0.00	0.05			
A2	0.33	0.43	0.381		
b1	0.80	0.99	0.89		
b2	1.70	1.88	1.78		
D	3.90	4.05	3.966		
D2			3.054		
E	6.40	6.60	6.51		
е			1.84		
E1	5.30	5.45	5.37		
E2		-	3.549		
L	0.75	0.95	0.85		
L1	0.50	0.65	0.57		
W	1.10	1.41	1.255		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	1.400
X1	4.860
X2	1.310
Y	1.390
Y1	3.360

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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