



### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
300V	4.0Ω @ V <sub>GS</sub> = 10V	0.43A
300 V	5.0Ω @ V <sub>GS</sub> = 4.5V	0.39A

## **Description**

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) yet maintain superior switching performance, which makes the device ideal for high-efficiency power-management applications.

## **Applications**

- DC-DC Converters
- Power-Management Functions
- · Battery-Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, and so on

# **Features and Benefits**

- Low Gate-Threshold Voltage
- Low-Input Capacitance
- Fast-Switching Speed
- Small Surface-Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

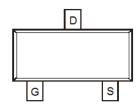
### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208
  Lead-Free Plating—Matte Tin Finish Annealed over Alloy 42
  Leadframe (3)
- Terminal Connections—See Diagram
- Weight: 0.008 grams (Approximate)

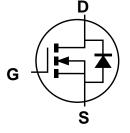
### SOT23







Top View Pin Configuration



**Equivalent Circuit** 

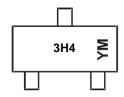
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN30H4D1S-7	SOT23	3,000/Tape & Reel
DMN30H4D1S-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



3H4 = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	2018	3	2019	2020	2	021	2022	2	023	2024	2	2025
Code	F		G	Н		I	J		K	L		М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	ain-Source Voltage			300	V
Gate-Source Voltage	$V_{GSS}$	±20	V		
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	I <sub>D</sub>	0.43 0.34	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle ≤ 1%)	I <sub>DM</sub>	2	Α		
Maximum Body Diode Continuous Current (Note 6)	Is	1.3	Α		
Pulsed Source Current (10µs Pulse, Duty Cycle ≤ 19	I <sub>SM</sub>	2	Α		

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		$P_{D}$	0.36	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\Theta JA}$	162	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	0.43	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>OJA</sub>	111	°C/W
Thermal Resistance, Junction to Case (Note 6)		R <sub>eJC</sub>	31	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

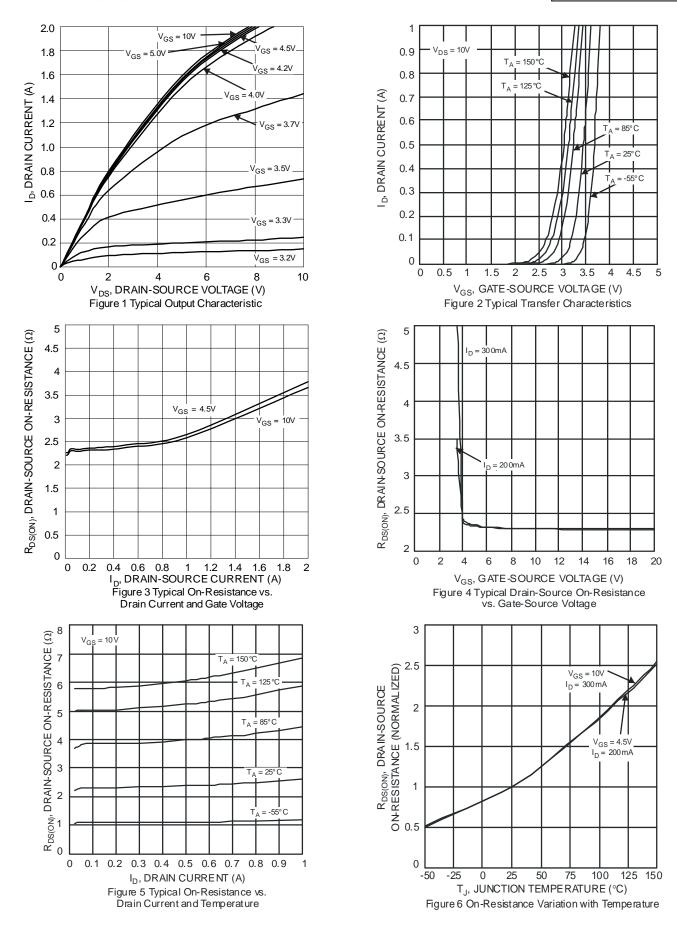
## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	300	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1.0	μA	V <sub>DS</sub> = 240V, V <sub>GS</sub> = 0V	
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	3	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	_	2.29	4.0	Ω	$V_{GS} = 10V, I_D = 0.3A$	
Static Dialif-Source Off-Resistance	R <sub>DS(ON)</sub>	_	2.34	5.0	12	$V_{GS} = 4.5V, I_D = 0.2A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 0.3A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	174	_			
Output Capacitance	Coss	_	12	_	pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	7	_		1 - 1.000112	
Gate Resistance	$R_g$	_	2.96	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	_	4.8	_			
Gate-Source Charge	Q <sub>gs</sub>	_	0.6	_	nC	$V_{DS} = 192V, V_{GS} = 10V,$ $I_{D} = 0.5A$	
Gate-Drain Charge	Q <sub>gd</sub>	_	2.1	_		ID = 0.5A	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.1	_			
Turn-On Rise Time	t <sub>R</sub>	_	3.5	_	ns	$V_{DS} = 60V, R_{L} = 200\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	20.6	_	115	$V_{GS} = 10V$ , $R_G = 25\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	13.8	_			
Reverse Recovery Time	t <sub>RR</sub>	_	43	_	ns	I <sub>F</sub> =0.5A, di/dt=100A/µs	
Reverse Recovery Charge	$Q_{RR}$	_	51	_	nC	11-0.5Λ, ui/ui=100A/μ5	

Notes:

- 5. Device mounted on FR-4 PC board with minimum recommended pad layout, single sided.6. Device mounted on FR-4 substrate PC board, 2oz copper with 1-inch square copper pad layout.7 .Short-duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.







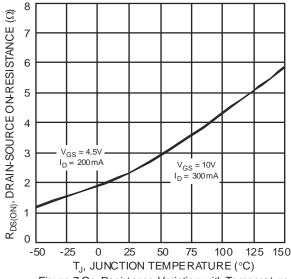
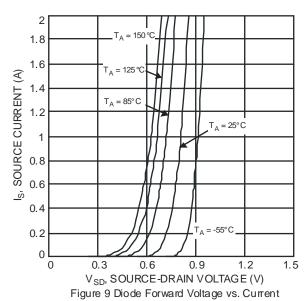
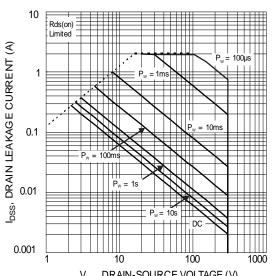


Figure 7 On-Resistance Variation with Temperature





 ${\rm V_{DS}, DRAIN\text{-}SOURCE\ VOLTAGE\ (V)}$  Figure 10 Typical Drain-Source Leakage Current vs. Voltage

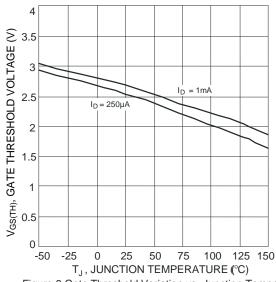
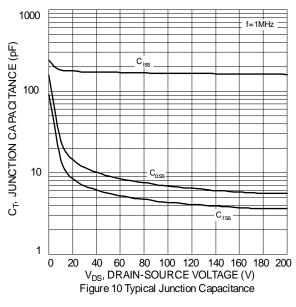
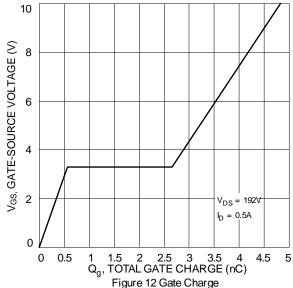


Figure 8 Gate Threshold Variation vs. Junction Temperature







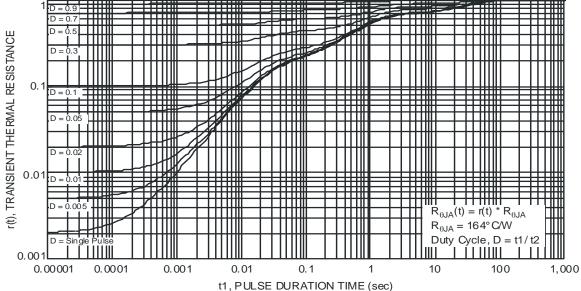


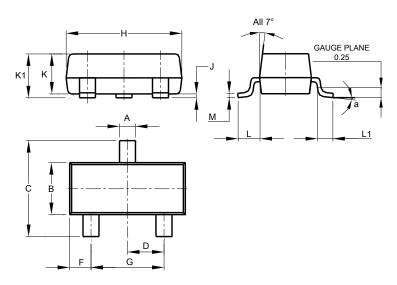
Figure 13 Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23

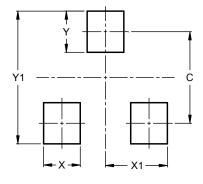


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

# **Suggested Pad Layout**

 $\label{please} Please see \ http://www.diodes.com/package-outlines.html \ for the \ latest \ version.$ 

### SOT23



Dimensions	Value (in mm)					
С	2.0					
X	0.8					
X1	1.35					
Y	0.9					
Y1	2.9					



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