



### **Product Summary**

BV <sub>DSS</sub>	Rds(on)	Package	Ι <sub>D</sub> T <sub>A</sub> = +25°C
60V	8Ω @ V <sub>GS</sub> = 5V	SOT363	170mA
000	6Ω @ V <sub>GS</sub> = 10V	301303	200mA

### Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

# **Applications**

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

HBM Class

### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

### Features

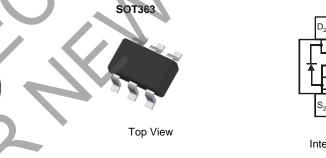
- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- HBM Class 1C
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 2N7002DWAQ is suitable for automotive applications requiring specific change control; it is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.
- https://www.diodes.com/quality/product-definitions/

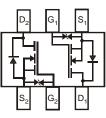
# **Mechanical Data**

- Case: SOT363
- Case Material: Molded Plastic.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020

Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe

- Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)





Top View Internal Schematic

# Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
2N7002DWA-7	Standard	SOT363	3,000/Tape & Reel
2N7002DWA-13	Standard	SOT363	10,000/Tape & Reel
2N7002DWAQ-7	Automotive	SOT363	3,000/Tape & Reel
2N7002DWAQ-13	Automotive	SOT363	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

 $L_{2}$  been https://www.uldes.com/duality/lead-nee/ for more information about blodes incorporated's definitions of halogen- and Antimony-nee, "Green and Lead-free. Lead-free. 2 Hologon, and Antimony free "Croop" products are defined as these which contains 2000 ppm browing. (2000 ppm bloging (21500 ppm total Pr + Cl) and Pr + Cl) and <math>Pr + Cl and Pr + Cl) and Pr + Cl + Cl and Pr + Cl and Pr

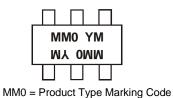
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Notes:



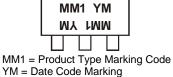
# **Marking Information**



YM = Date Code Marking

M = Month (ex: 9 = September)

Y = Year (ex: E = 2017)



M = Month (ex: 9 = September)

Y = Year (ex: E = 2017)



MM4 YM

Date Code Key

Year	2012	-	20	17	2018	2019	2020	2021	20	22	2023	2024
Code	Z	-	E		F	G	Н			J	К	L
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	Characteristic			Symbol	Value	Unit
Drain-Source Voltage				V <sub>DSS</sub>	60	V
Gate-Source Voltage				V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5)	V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	lD	180 140	mA
Continuous Drain Current (Note 5)	$V_{GS} = 5V$	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι <sub>D</sub>	150 120	mA
Continuous Drain Current (Note 6)	V <sub>GS</sub> = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	200 160	mA
Continuous Drain Current (Note 6)	V <sub>GS</sub> = 5V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	170 140	mA
Pulsed Drain Current (10µs Pulse, D	uty Cycle = 1%)			I <sub>DM</sub>	700	mA

# Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	435	°C/W
Total Power Dissipation (Note 6)	PD	400	mW
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	330	°C/W
Thermal Resistance, Junction to Case (Note 6)	R <sub>eJC</sub>	139	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-55 to +150	°C

 Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout. Notes:



# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

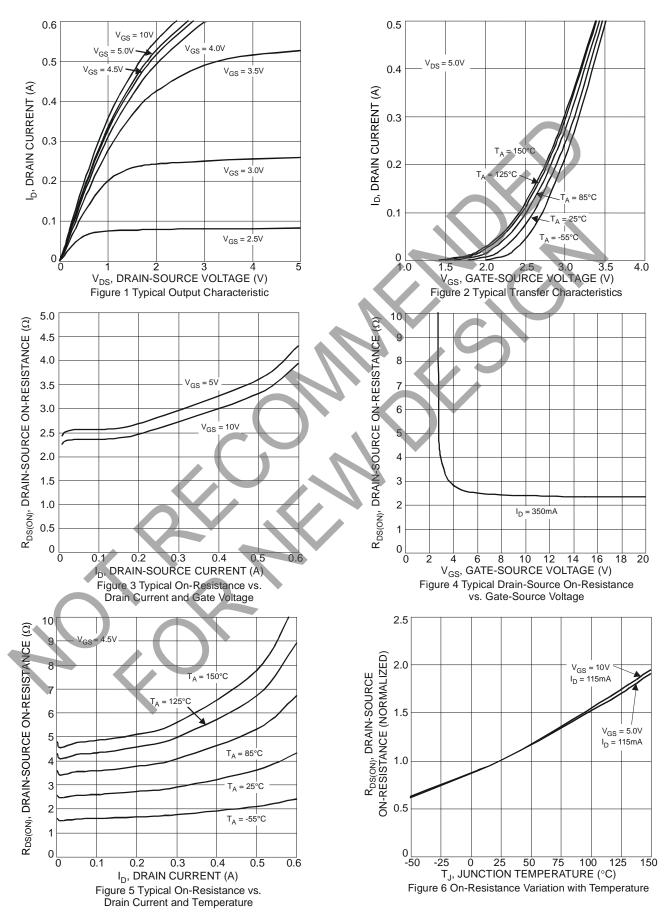
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Body Leakage	I <sub>GSS</sub>			±5	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.8		2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
Static Drain-Source On-Resistance			_	8	Ω	$V_{GS} = 5.0V, I_D = 0.115A$	
	R <sub>DS(ON)</sub>		—	6	Ω	$V_{GS} = 10.0V, I_D = 0.115A$	
Forward Transconductance	<b>g</b> fs	80		—	mS	$V_{DS} = 10V, I_D = 0.115A$	
Diode Forward Voltage	V <sub>SD</sub>	-	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		22.0				
Output Capacitance	C <sub>oss</sub>		3.2	—	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	2.0	_			
Gate Resistance	R <sub>G</sub>	_	88	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge V <sub>GS</sub> = 10V	Qg	—	0.87				
Total Gate Charge V <sub>GS</sub> = 4.5V	Qg	—	0.43		nC	$V_{GS} = 10V, V_{DS} = 30V,$	
Gate-Source Charge	Q <sub>gs</sub>	—	0.11	_	no	$I_D = 150 \text{mA}$	
Gate-Drain Charge	Q <sub>gd</sub>		0.11	—			
Turn-On Delay Time	t <sub>D(ON)</sub>	4	3.3	(			
Turn-On Rise Time	t <sub>R</sub>		3.2			$V_{DD} = 30V, I_D = 0.115A, V_{GEN} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>		12.0		ns	$R_{GEN} = 25\Omega$	
Turn-Off Fall Time	t <sub>F</sub>		6.3				

Notes: 7. Short duration pulse test used to minimize self-heating effect 8. Guaranteed by design. Not subject to production testing.



### NOT RECOMMENDED FOR NEW DESIGN USE <u>DMN65D8LDW</u>

2N7002DWA

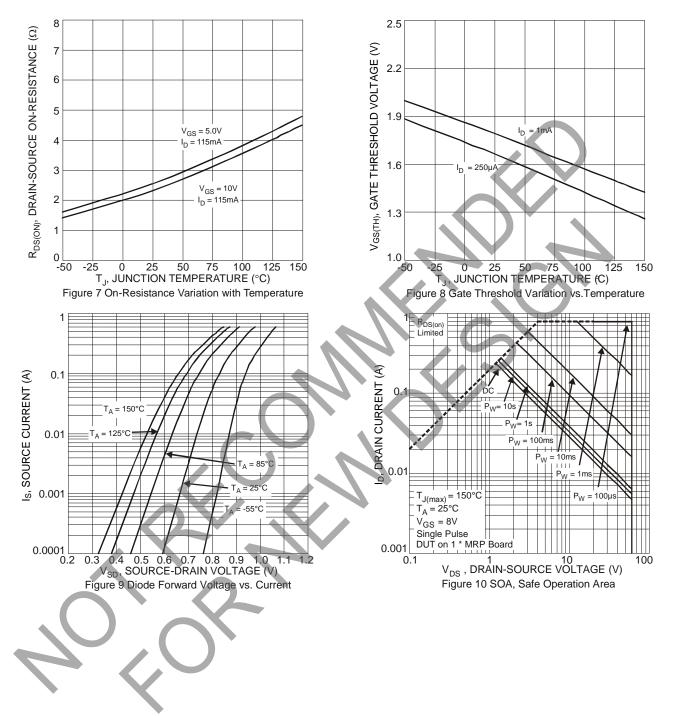


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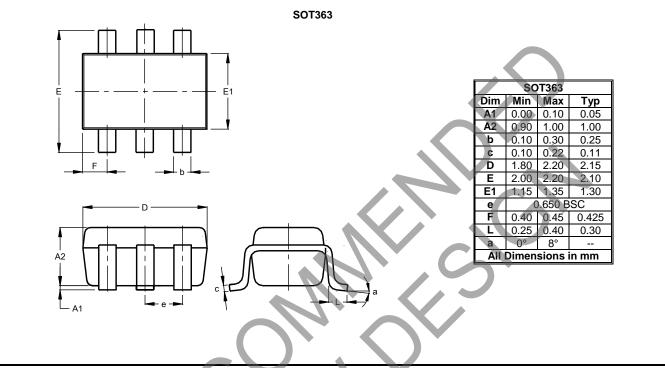
# 2N7002DWA





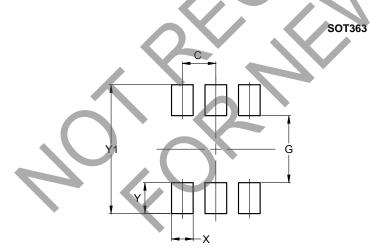
# Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Ŷ	0.600
Y1	2.500



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