

# 2.5/3.3V CMOS XO

# **WX501**



#### **Product Features**

- Very low phase jitter 1.0ps RMS
- Thicker crystal for improved reliability
- Industrial Temperature Range
- Pb-free & RoHS compliant

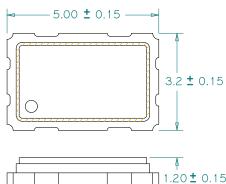
## **Product Description**

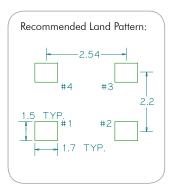
The WX501 XO series is a high performance CMOS crystal oscillator family with very low jitter performance. It supports various options including wider frequency range, 2.5/3.3 voltage, various stabilities, and different package sizes. It is designed to meet the clock source specifications for communication systems, and other high performance equipment.

# **Applications**

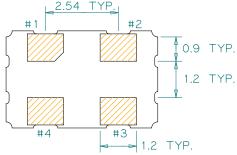
- Networking systems
- Servers and storage systems
- Profession video equipments
- Test and measurement
- FPGA/ASIC clock generation

### Package:







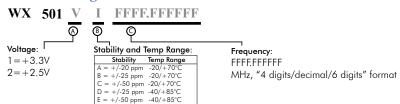


## **Pin Functions:**

Pin	Function
1	OE
2	GND
3	Q
4	$V_{\mathrm{DD}}$

\*Extended high frequency power decoupling is recommended (see test circuit for minimum recommendation). To ensure optimal performance, do not route RF traces beneath the package.

#### **Part Ordering Information:**



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# Ultra Low Jitter CMOS Crystal Oscillator 5.0 x 3.2mm

#### **Electrical Performance**

Parameter	Min.	Тур.	Max.	Units	Notes		
Output Frequency	50		212.5	MHz			
Complex Welfer an	3.135	3.3	3.465	3.7	See andering antions		
Supply Voltage	2.375	2.5	2.5 2.625 V	V	See ordering options		
Supply Current, Output Enabled			70	mA			
Supply Current, Output Disabled			50	mA			
Frequency Stability			±50	ppm	See ordering options		
Operating Temperature Range	-40		+85	°C	See ordering options		
Output Logic 0, V <sub>OL</sub>			0.4	V			
Output Logic 1, V <sub>OH</sub>	V <sub>DD</sub> -0.4			V			
Output Load			15	pF			
Duty Cycle	45		55	%	Measured 50% V <sub>DD</sub>		
Rise and Fall Time			1.5	ns	Measured 20/80% of waveform		
Jitter, Phase RMS (1-σ)			1	ps	12kHz to 20 MHz frequency band		
Jitter, pk-pk			30	ps	100,000 random periods		

#### Notes:

#### **Output Enable / Disable Function**

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (pin 1), Output Enable	0.7 V <sub>DD</sub>			V	or open
Input Voltage (pin 1), Output Disable (low power standby)			0.3 V <sub>DD</sub>	V	Output is Hi-Z
Internal Pullup Resistance		100		$k\Omega$	
Output Disable Delay			80	ns	
Output Enable Delay			80	ns	
Setup Time			10	ms	

## **Absolute Maximum Ratings**

Parameter	Min.	Тур.	Max.	Units	Notes
Storage Temperature	-55		+125	°C	

For the latest product information visit: http://www.pericom.com/products/timing/oscillators/

For test circuit go to: <a href="http://www.pericom.com/pdf/sre/tc\_hcmos.pdf">http://www.pericom.com/pdf/sre/tc\_hcmos.pdf</a>

For soldering reflow profile and reliability test ratings go to: http://www.pericom.com/pdf/sre/reflow.pdf

For tape and reel information go to: http://www.pericom.com/pdf/sre/tr\_5032\_xo.pdf



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WX501

Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C), aging (1 year at 25°C average effective ambient temperature), shock and vibration.

<sup>2.</sup> For specifications othere than those listed, please contact sales.