



SuperSOT4™

DUAL 20V NPN SILICON LOW SATURATION SWITCHING TRANSISTOR

SUMMARY

 $V_{ceo} = 20V$; $R_{sat} = 40m\Omega$; $I_c = 3.5A$

DESCRIPTION

This new 4th generation ultra low saturation transistor utilises the Zetex matrix structure combined with advanced assembly techniques to give extremely low on state losses. This makes it ideal for high efficiency, low voltage switching applications.

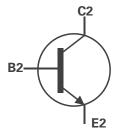
MSOP8

FEATURES

- Extremely Low Equivalent On Resistance
- **Extremely Low Saturation Voltage**
- h_{FF} characterised up to 10A
- I_C=3.5A Continuous Collector Current
- MSOP8 package

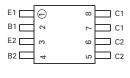
- Power switches
- Motor control

APPLICATIONS DC - DC Converters **Power Management Functions**



ORDERING INFORMATION

DEVICE	REEL SIZE (inches)	TAPE WIDTH (mm)	QUANTITY PER REEL
ZXT12N20DXTA	7	12mm embossed	1000 units
ZXT12N20DXTC	13	12mm embossed	4000 units



Top View

DEVICE MARKING

T12N20DX

ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Collector-Base Voltage	V _{CBO}	50	V
Collector-Emitter Voltage	V _{CEO}	20	V
Emitter-Base Voltage	V _{EBO}	7.5	V
Peak Pulse Current	I _{CM}	15	А
Continuous Collector Current	I _C	3.5	А
Base Current	I _B	500	mA
Power Dissipation at TA=25°C (a)(d) Linear Derating Factor	P _D	0.87 6.9	W mW/°C
Power Dissipation at TA=25°C (a)(e) Linear Derating Factor	P _D	1.04 8.3	W mW/°C
Power Dissipation at TA=25°C (b)(d) Linear Derating Factor	P _D	1.25 10	W mW/°C
Operating and Storage Temperature Range	T _j :T _{stg}	-55 to +150	°C

THERMAL RESISTANCE

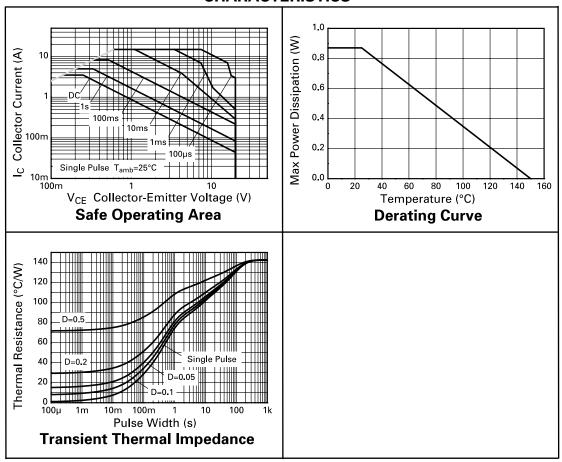
PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)(d)	$R_{ heta JA}$	143	°C/W
Junction to Ambient (b)(d)	$R_{ heta JA}$	100	°C/W
Junction to Ambient (a)(e)	$R_{ heta JA}$	120	°C/W

NOTES

- (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
- (b) For a device surface mounted on FR4 PCB measured at t≤5 secs.
- (c) Repetitive rating pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.
- (d) For device with one active die.
- (e) For device with two active die running at equal power.



CHARACTERISTICS





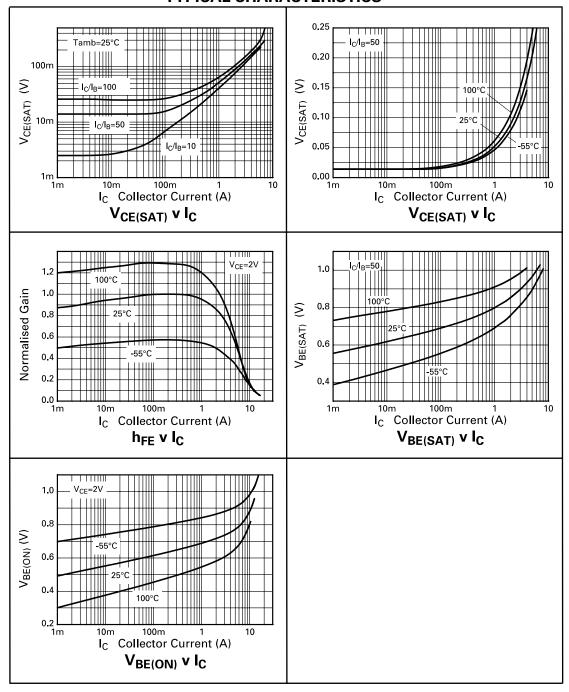
ELECTRICAL CHARACTERISTICS (at T_{amb} = 25°C unless otherwise stated).

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.	
Collector-Base Breakdown Voltage	V _{(BR)CBO}	50	100		V	I _C =100μA	
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	20	30		V	I _C =10mA*	
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	7.5	8.5		V	I _E =100μA	
Collector Cut-Off Current	I _{CBO}			100	nA	V _{CB} =40V	
Emitter Cut-Off Current	I _{EBO}			100	nA	V _{EB} =6V	
Collector Emitter Cut-Off Current	I _{CES}			100	nA	V _{CES} =40V	
Collector-Emitter Saturation Voltage	V _{CE(sat)}		7.0 65 120 160	10 100 160 200	mV mV mV	I _C =0.1A, I _B =10mA* I _C =1A, I _B =10mA* I _C =3A, I _B =150mA* I _C =3.5A, I _B =50mA*	
Base-Emitter Saturation Voltage	V _{BE(sat)}		0.9	1.0	V	I _C =3.5A, I _B =50mA*	
Base-Emitter Turn-On Voltage	V _{BE(on)}		0.85	0.9	V	I _C =3.5A, V _{CE} =2V*	
Static Forward Current Transfer Ratio	h _{FE}	250 300 200 40	400 450 320 70	900		I _C =10mA, V _{CE} =2V* I _C =1A, V _{CE} =2V* I _C =3.5A, V _{CE} =2V* I _C =10A, V _{CE} =2V*	
Transition Frequency	f _T		112		MHz	I _C =50mA, V _{CE} =10V f=50MHz	
Output Capacitance	C _{obo}		43		pF	V _{CB} =10V, f=1MHz	
Turn-On Time	t _(on)		65		ns	V _{CC} =10V, I _C =2A	
Turn-Off Time	t _(off)		400		ns	I _{B1} =I _{B2} =40mA	

^{*}Measured under pulsed conditions. Pulse width=300 μ s. Duty cycle $\leq 2\%$

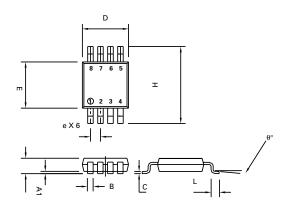


TYPICAL CHARACTERISTICS





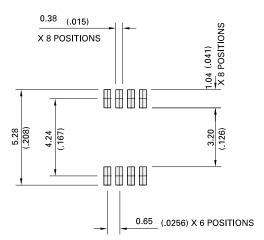
PACKAGE DIMENSIONS



Conforms to JEDEC MO-187 Iss A

DIM	Millim	Millimetres		
	MIN	MAX	MIN	MAX
А		1.10		0.043
A1	0.05	0.15	0.002	0.006
В	0.25	0.40	0.010	0.016
С	0.13	0.23	0.005	0.009
D	2.90	3.10	0.114	0.122
е	0.65	BSC	0.0256	BSC
Е	2.90	3.10	0.114	0.122
Н	4.90	BSC	0.193	BSC
L	0.40	0.70	0.016	0.028
a°	0°	6°	0°	6°

PAD LAYOUT DETAILS





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