



DMP6110SFDFQ

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	110mΩ @ V _{GS} = -10V	-4.2A
-60V	130mΩ @ V _{GS} = -4.5V	-3.9A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

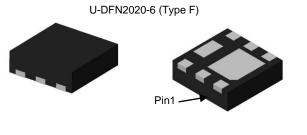
- **Battery Management Application**
- **Power Management Functions**
- **DC-DC Converters**

Features and Benefits

- 100% Unclamped Inductive Switching Test in Production -Ensures More Reliable and Robust End Application
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

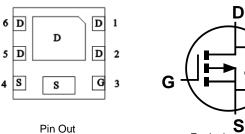
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.007 grams (Approximate)



Top View

Bottom View



Bottom View

S Equivalent Circuit

Ordering Information (Note 5)

	Part Number	Case	Packaging						
	DMP6110SFDFQ-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel						
	DMP6110SFDFQ-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel						
Notes:	tes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.								

com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds. 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

	P0	ΥM	
•			

P0 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019)

M = Month (ex: 9 = September)

Date Code Key	у												
Year	2	018	2019	2020	202	1	2022	2023	2024	202	5 2	2026	2027
Code		F	G	Н			J	K	L	М		Ν	0
Month		Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code		1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	-60	V		
Gate-Source Voltage		V _{GSS}	±20	V	
	Steady State	T _A = +25°C T _A = +70°C	ID	-3.5 -2.8	А
Continuous Drain Current (Note 7) $V_{GS} = -10V$	t<10s	T _A = +25°C T _A = +70°C	ID	-4.2 -3.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%))		I _{DM}	-20	A
Continuous Source-Drain Diode Current (Note 7)		T _A = +25°C	Is	-2.1	А
Pulsed Source Current (10µs Pulse, Duty Cycle = 1		I _{SM}	-20	А	
Avalanche Current (Note 8) L = 0.1mH	I _{AS}	-19	А		
Avalanche Energy (Note 8) L = 0.1mH	E _{AS}	18	mJ		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Bower Dissinction (Note 6)	T _A = +25°C	Р	0.76	W	
Total Power Dissipation (Note 6)	T _A = +70°C	$r_A = +70^{\circ}C$ P _D		vv	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Devi	167	°C/W	
memai resistance, sunction to Amplent (Note 6)	t<10s	R _{0JA}	121		
Total Power Dissipation (Note 7)	T _A = +25°C	D -	1.97	W	
Total Power Dissipation (Note 7)	T _A = +70°C	PD	1.30		
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	Р	64	°C/W	
memai resistance, sunction to Amblent (Note 7)	t<10s	$R_{ hetaJA}$	42		
Thermal Resistance, Junction to Case (Note 7)	Steady State	R _{θJC}	8		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

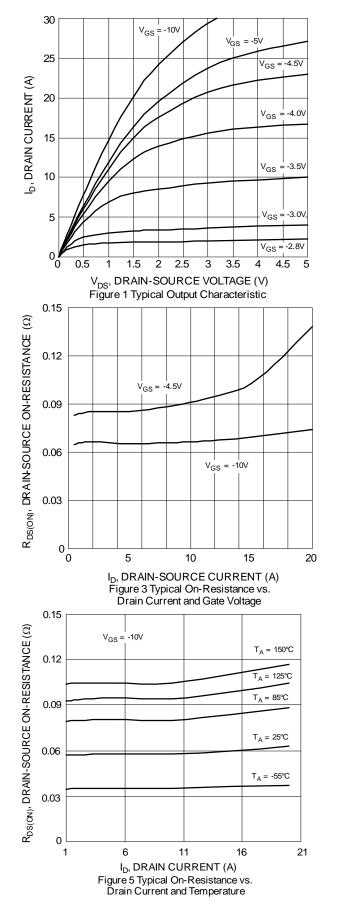
			_			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)	1		1			1
Drain-Source Breakdown Voltage	BV _{DSS}	-60	—	—	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I _{DSS}	—	—	-1	μA	$V_{DS} = -48V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	—	—	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	V _{GS(TH)}	-1	—	-3	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance	Р		56	110	mΩ	$V_{GS} = -10V, I_D = -4.5A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	72	130	11122	V _{GS} = -4.5V, I _D =-3.5A
Diode Forward Voltage	V _{SD}	—	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 10)						*
Input Capacitance	Ciss	—	969	—		
Output Capacitance	Coss	—	58	—	pF	$V_{DS} = -30V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	—	44	—		
Gate Resistance	Rg	—	14	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = -4.5V)	Qg	—	8.2	—		
Total Gate Charge (V _{GS} = -10V)	Qg	—	17.2	—	nC	
Gate-Source Charge	Q _{gs}	—	3.0	—	nc	$V_{DS} = -30V, I_D = -12A$
Gate-Drain Charge	Q _{gd}	—	3.1	—		
Turn-On Delay Time	t _{D(ON)}	—	4.4	—		
Turn-On Rise Time	t _R	—	23	—	20	$V_{GS} = -10V, V_{DS} = -30V,$
Turn-Off Delay Time	t _{D(OFF)}	—	34	—	ns	$R_{GEN} = 6\Omega$, $I_D = -12A$
Turn-Off Fall Time	t _F	—	42	—		
Reverse Recovery Time	t _{RR}	—	13.2	—	ns	I _S = -12A, di/dt = -100A/µs
Reverse Recovery Charge	Q _{RR}	_	6.2	_	nC	I _S = -12A, di/dt = -100A/µs

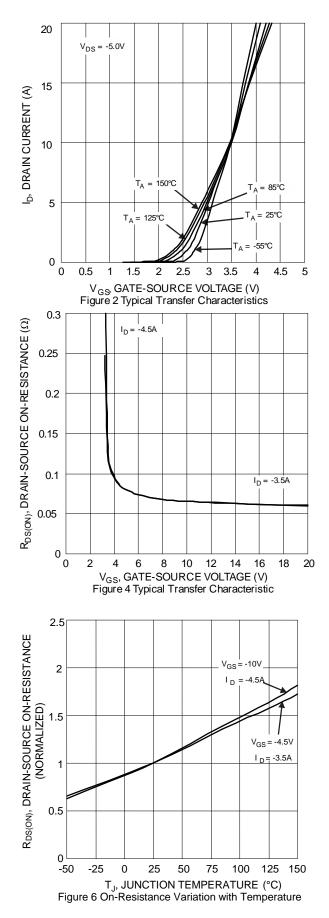
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate. Notes:

8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$. 9. Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to product testing.





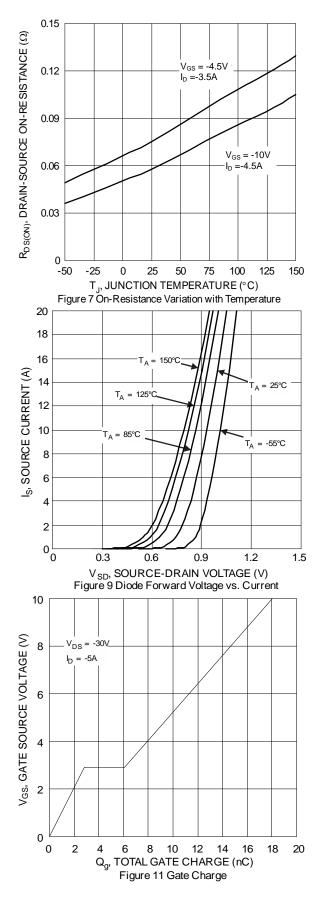


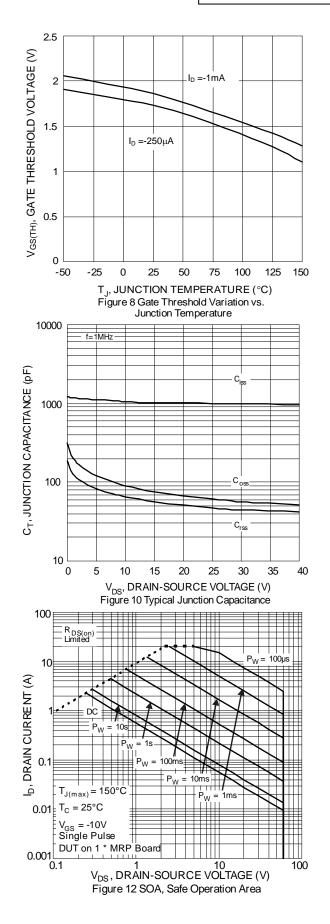


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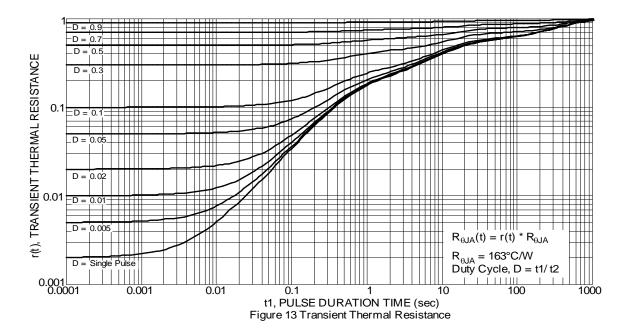








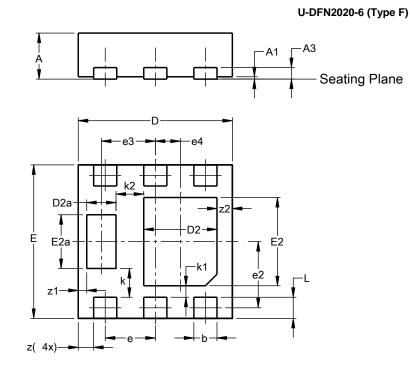






Package Outline Dimension

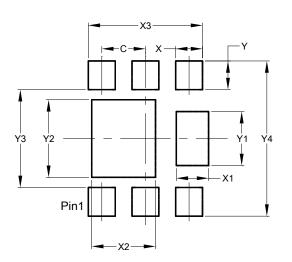
Please see http://www.diodes.com/package-outlines.html for the latest version.



	U-DFN2020-6 (Type F)								
Dim	Min								
Α	0.57	0.63	0.60						
A1	0.00	0.05	0.03						
A3	-	-	0.15						
b	0.25	0.35	0.30						
D	1.95	2.05	2.00						
D2	0.85	0.85 1.05 0.95							
D2a	0.33 0.43 0.38								
E	1.95	2.05	2.00						
E2	1.05	1.25	1.15						
E2a	0.65 0.75 0.70								
е	0.65 BSC								
e2).863 BS							
e3		0.70 BS							
e4).325 BS							
k		0.37 BS	С						
k1		0.15 BS							
k2		0.36 BS							
L	0.225	0.325	0.275						
z		0.20 BS							
z1).110 BS							
z2		0.20 BS	-						
All D	imens	ions in	mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



U-DFN2020-6 (Type F)

Dimensions	Value (in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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