



BSS84WQ

#### 50V P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

### **Product Summary**

BV <sub>DSS</sub>	Rds(ON)	I <sub>D</sub> T <sub>A</sub> = +25°C
-50V	10Ω @ V <sub>GS</sub> = -5V	-164mA

# **Description and Applications**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

### **Features and Benefits**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The BSS84WQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

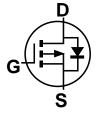
https://www.diodes.com/quality/product-definitions/

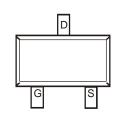
### **Mechanical Data**

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
   Lead Free Plating (Matte Tin Finish Annealed over Alloy 42
   Leadframe). <a href="mailto:3">(3)</a>
- Weight: 0.006 grams (Approximate)









Top View

**Equivalent Circuit** 

Top View

## Ordering Information (Note 4)

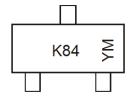
Part Number	Case	Packaging
BSS84WQ-7-F	SOT323	3,000 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**

### **SOT323**



K84 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н		J	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	-50	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 6) Vss = -5V	Steady State	Ю	-164 -131	mA	
Maximum Continuous Body Diode Forward Cur	rent (Note	Is	-164	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle =	: 1%) (Not	I <sub>DM</sub>	-800	mA	

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	330	mW	
Thermal Resistance, Junction to Ambient (Note 5)	Reja	388	°C/W		
Total Power Dissipation (Note 6)		PD	410	mW	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	307	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-50	-75		V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	IDSS			-1 -2 -100	μΑ μΑ nA	V <sub>DS</sub> = -50V, V <sub>GS</sub> = 0V, T <sub>J</sub> = +25°C V <sub>DS</sub> = -50V, V <sub>GS</sub> = 0V, T <sub>J</sub> = +125°C V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, T <sub>J</sub> = +25°C
Gate-Body Leakage	Igss			±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	-0.8	-1.6	-2.0	V	$V_{DS} = V_{GS}$ , $I_D = -1mA$
Static Drain-Source On-Resistance	RDS(ON)		3.1	10	Ω	V <sub>GS</sub> = -5V, I <sub>D</sub> = -0.1A
Forward Transconductance	grs	0.05	_	_	S	V <sub>DS</sub> = -25V, I <sub>D</sub> = -0.1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss			45	pF	
Output Capacitance		_	_	25	pF	V <sub>DS</sub> = -25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	_	12	pF	
SWITCHING CHARACTERISTICS (Note 8)						
Turn-On Delay Time	t <sub>D</sub> (ON)		10		ns	V <sub>DD</sub> = -30V, I <sub>D</sub> = -0.27A,
Turn-Off Delay Time	t <sub>D</sub> (OFF)	_	18	_	ns	$R_{GEN} = 50\Omega$ , $V_{GS} = -10V$

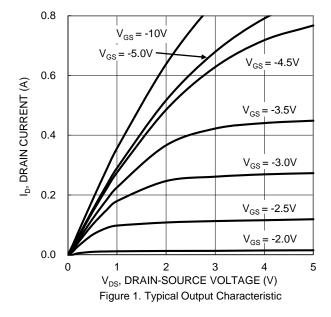
Notes:

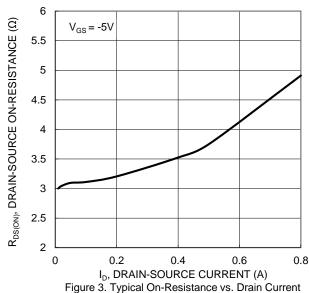
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout
  Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout
  Short duration pulse test used to minimize self-heating effect.

- 8. Guarantee by design. Not subject to production testing.









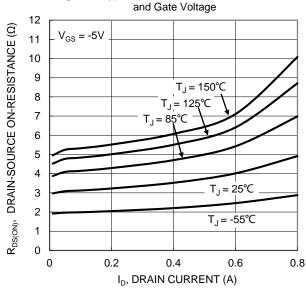
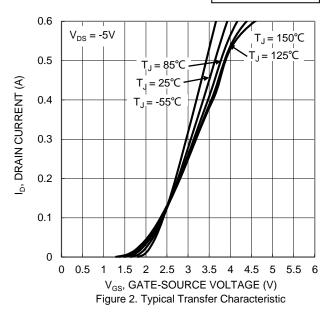
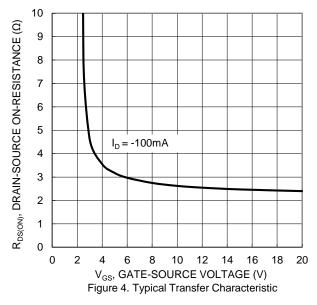


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





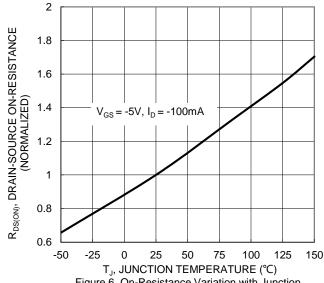
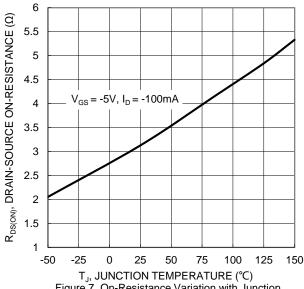


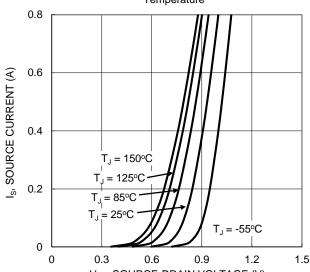
Figure 6. On-Resistance Variation with Junction Temperature



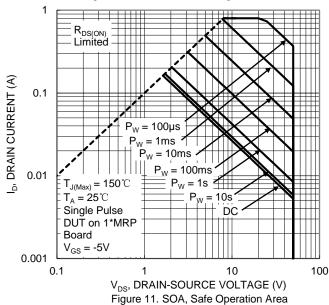




 $T_{\rm J},$  JUNCTION TEMPERATURE (°C) Figure 7. On-Resistance Variation with Junction Temperature



V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



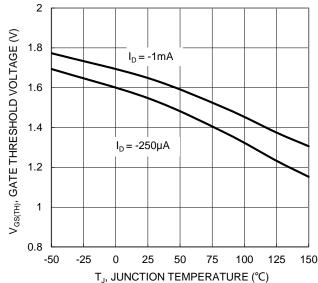
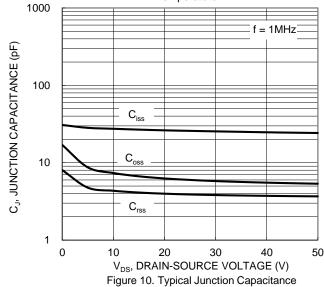


Figure 8. Gate Threshold Variation vs. Junction Temperature





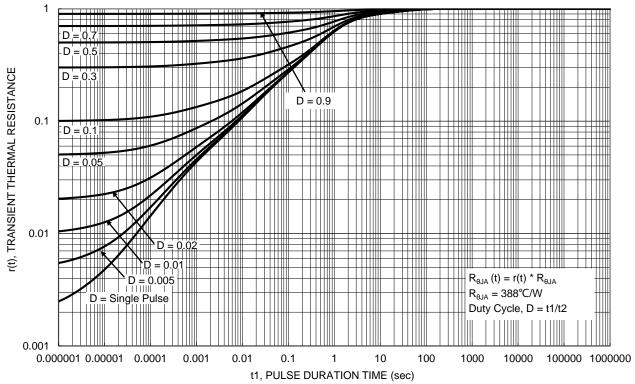


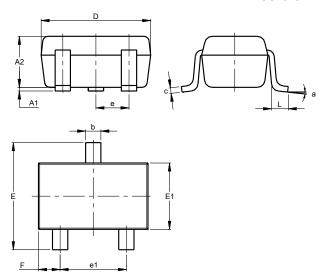
Figure 12. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT323

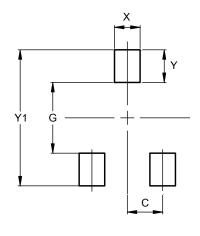


SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	).650 B	SC				
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT323**



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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