

ZVN4306A

60V N-CHANNEL ENHANCEMENT MODE VERTICAL DMOS FET IN E-LINE

Product Summary

V _{(BR)DSS}	Max R _{DS(on)}	Max I _D @ T _A = 25°C
60V	330mΩ @ V _{GS} = 10V	1.4A
	450mΩ @ V _{GS} = 5V	1.2A

Application

- DC DC convertors
- Solenoids / relay drivers for automotive

Features and Benefits

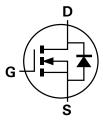
- Breakdown Voltage BV_{DSS} > 60V
- R_{DS(on)} ≤ 0.33Ω @ V_{GS} = 10V
- Maximum continuous drain current I_D = 1.1A
- "Green" component, Lead Free Finish / RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

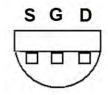
- Case: E-Line (TO-92 Compatible)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.159 grams (approximate)







Equivalent Circuit



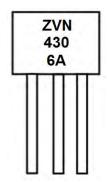
Pin Out - Bottom View

Ordering Information (Note 1)

Part Number	Package	Marking	Quantity
ZVN4306ASTZ	E-Line	ZVN4306A	2,000 per Ammo pack
ZVN4306A	E-Line	ZVN4306A	4,000 loose per box

Notes:

Marking Information



ZVN4306A = Product Type Marking Code On Rounded Face

^{1.} Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds. All applicable RoHS exemptions applied. Further information about Diodes Inc.'s "Green" Policy can be found on our website at http:// www.diodes.com



Maximum Ratings @TA = 25°C unless otherwise specified

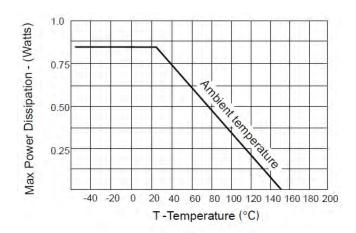
Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	60	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current	I _D	1.1	Α
Practical Continuous Drain Current	I _{DP}	1.3	A
Pulsed Drain Current	I _{DM}	15	Α

Thermal Characteristics @TA = 25°C unless otherwise specified

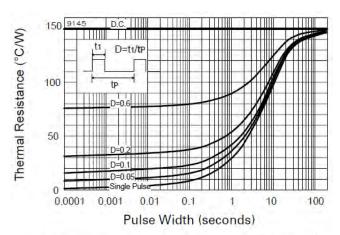
Characteristic		Symbol	Value	Unit
Power Dissipation		P _D	850	mW
Practical Power Dissipation	(Note 2)	P _{DP}	1.13	W
Thermal Resistance, Junction to Ambient		$R_{\theta JA}$	150	°C/W
Thermal Resistance, Junction to Ambient	(Note 2)	R _{θJA}	111	°C/W
Thermal Resistance, Junction to Leads	(Note 3)	$R_{ heta JL}$	50	°C/W
Operating and Storage Temperature Range	•	T_{J}, T_{STG}	-55 to +150	°C

Notes:

^{3.} Thermal resistance from junction to solder-point



Derating curve



Maximum transient thermal impedance

^{2.} For a device mounted on 25mm X 25mm X 1.6mm FR-4 PCB with high coverage of single sided 1oz copper, in still air condition.



Electrical Characteristics @T_A = 25°C unless otherwise specified

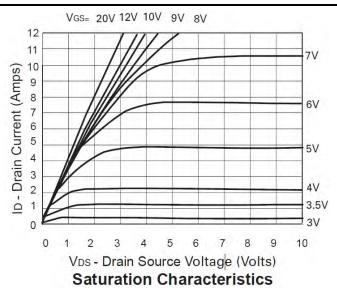
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	-	-	V	$V_{GS} = 0V$, $I_D = 1mA$
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	1 20	μΑ	V _{DS} = 60V, V _{GS} = 0V V _{DS} = 48V, V _{GS} = 0V, T _A = 125°C
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
On-State Drain Current	I _{D(on)}	12	-	-	Α	V _{GS} = 10V, V _{DS} = 10V
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	$V_{GS(th)}$	1.3	-	3	V	$V_{DS} = V_{GS}$, $I_D = 1mA$
Static Drain-Source On-Resistance	R _{DS (on)}	-	0.22 0.32	0.33 0.45	Ω	$V_{GS} = 10V, I_D = 3A$ $V_{GS} = 5V, I_D = 1.5A$
Forward Transconductance	9 _{fs}	700	-	-	mS	$V_{DS} = 10V, I_{D} = 3A$
DYNAMIC CHARACTERISTICS (Note 4)	-	•		•		
Input Capacitance	C _{iss}	-	-	350	pF	V 05V V 0V
Output Capacitance	Coss	-	-	140	pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	-	-	30	pF	
Turn-On Delay Time (Note 5)	t _{d(on)}	-	-	8	ns	
Turn-On Rise Time (Note 5)	t _r	-	-	25	ns), or, , , , , , , , , , , , , , , , , ,
Turn-Off Delay Time (Note 5)	t _{d(off)}	-	-	30	ns	$V_{DD} = 25V, I_D = 3A, V_{GEM} = 10V$
Turn-Off Fall Time (Note 5)	t _f	-	-	16	ns	

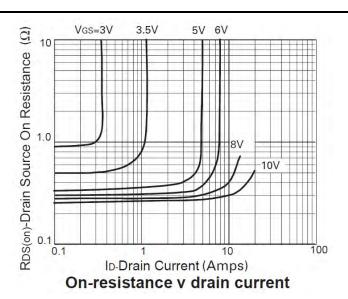
Notes:

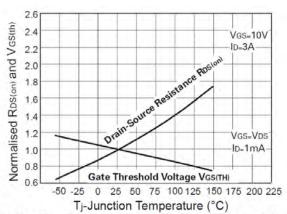
^{4.} Measured under pulsed conditions. Width = $300\mu s$. Duty cycle $\leq 2\%$ 5. Switching times measured with 50Ω source impedance and <5ns rise time on a pulse generator

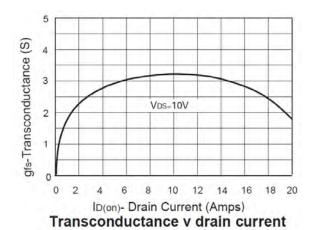


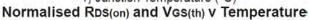
Electrical Characteristics

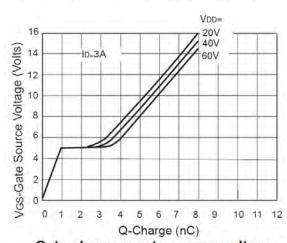


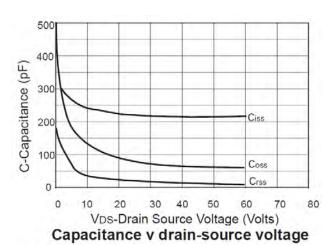








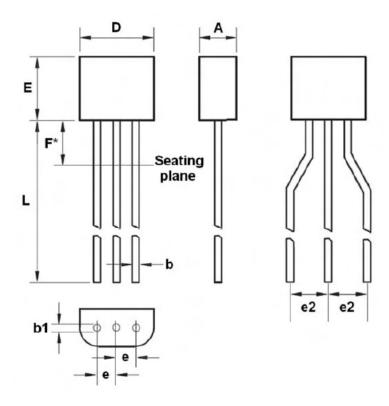




Gate charge v gate-source voltage



Package Outline Dimensions



DIM	Millin	neters	Inches		
	Min.	Max.	Min.	Max.	
Α	2.16	2.41	0.085	0.095	
b	0.41	0.495	0.016	0.0195	
b1	0.41	0.495	0.016	0.0195	
D	4.37	4.77	0.172	0.188	
E	3.61	4.01	0.142	0.158	
e*	1.27	1.27 NOM		NOM	
e [†]	2.54 NOM		0.100	NOM	
F [‡]		2.50	_	0.098	
L	13.00	13.97	97 0.512		

NOTES:

- * loose product only
- † taped product only
- ‡ leads uncontrolled above seating plane

Controlling dimensions are in millimeters. Approximate dimensions are provided in inches



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