

Product Summary

| BV_{DSS} | $R_{DS(ON)}$ Max | $I_{D Max}$ $T_A = +25^{\circ}C$ |
|------------|----------------------------------|-------------------------------------|
| -20V | 78m Ω @ $V_{GS} = -8V$ | -4.0A |
| | 100m Ω @ $V_{GS} = -4.5V$ | -3.5A |

Description

This new generation MOSFET is designed to minimize the footprint in handheld and Mobile application. It can be used to replace many small signals MOSFET with as really small footprint.

Applications

- Battery Management
- Load Switch
- Battery Protection
- Handheld and Mobile Application

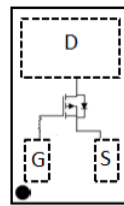
Features and Benefits

- Low Q_g & Q_{gd}
- Small Footprint
- Low Profile 0.20mm Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

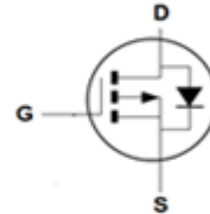
Mechanical Data

- Case: X4-DSN1006-3
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu or NiAu. Solderable per MIL-STD-202, Method 208 **(e4)**
- Weight: 0.00029 grams (Approximate)

X4-DSN1006-3



Top View



Equivalent Circuit

Ordering Information (Note 4)

| Part Number | Case | Packaging |
|----------------|--------------|-----------------|
| DMP2077UCA3-7 | X4-DSN1006-3 | 10k/Tape & Reel |
| DMP2077UCA3-7A | X4-DSN1006-3 | 10k/Tape & Reel |

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



Q = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: H = 2020)
 M or \bar{M} = Month (ex: 9 = September)

Date Code Key

| Year | 2018 | ... | 2020 | 2021 | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 |
|------|------|-----|------|------|------|------|------|------|------|------|------|------|
| Code | F | ... | H | I | J | K | L | M | N | O | P | R |

| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | O | N | D |

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | | | Symbol | Value | Unit |
|---|--------------|------------------------|------------------|-------|------|
| Drain-Source Voltage | | | V _{DSS} | -20 | V |
| Gate-Source Voltage | | | V _{GSS} | ±12 | V |
| Continuous Drain Current (Note 5) V _{GS} = -8V | Steady State | T _A = +25°C | I _D | -4.0 | A |
| | | T _A = +70°C | | -3.2 | |
| Continuous Drain Current (Note 5) V _{GS} = -4.5V | Steady State | T _A = +25°C | I _D | -3.5 | A |
| | | T _A = +70°C | | -2.8 | |
| Pulsed Drain Current (Note 6) | | | I _{DM} | -16 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Power Dissipation (Note 7) | P _D | 0.66 | W |
| Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7) | R _{θJA} | 197 | °C/W |
| Power Dissipation (Note 5) | P _D | 1.98 | W |
| Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5) | R _{θJA} | 65 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--|---------------------|------|-------|------|------|---|
| OFF CHARACTERISTICS (Note 8) | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -20 | — | — | V | V _{GS} = 0V, I _D = -250μA |
| Zero Gate Voltage Drain Current T _J = +25°C | I _{DSS} | — | — | -100 | nA | V _{DS} = -16V, V _{GS} = 0V |
| Gate-Source Leakage | I _{GSS} | — | — | ±50 | nA | V _{GS} = ±12V, V _{DS} = 0V |
| ON CHARACTERISTICS (Note 8) | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | -0.5 | -0.85 | -1.0 | V | V _{DS} = V _{GS} , I _D = -250μA |
| Static Drain-Source On-Resistance | R _{DS(ON)} | — | 66 | 78 | mΩ | V _{GS} = -8V, I _D = -0.5A |
| | | — | 78 | 100 | | V _{GS} = -4.5V, I _D = -0.5A |
| | | — | 112 | 165 | | V _{GS} = -2.5V, I _D = -0.5A |
| | | — | 165 | 600 | | V _{GS} = -1.8V, I _D = -0.1A |
| | | — | 295 | 900 | | V _{GS} = -1.5V, I _D = -0.1A |
| Diode Forward Voltage | V _{SD} | — | -0.73 | -1.0 | V | V _{GS} = 0V, I _S = -0.5A |
| Reverse Recovery Charge | Q _{RR} | — | 1.3 | — | nC | V _{DD} = -10V, I _F = -1A, |
| Reverse Recovery Time | t _{RR} | — | 7.7 | — | ns | di/dt = 100A/μs |
| DYNAMIC CHARACTERISTICS (Note 9) | | | | | | |
| Input Capacitance | C _{iss} | — | 143 | — | pF | V _{DS} = -10V, V _{GS} = 0V, f = 1MHz |
| Output Capacitance | C _{oss} | — | 76 | — | | |
| Reverse Transfer Capacitance | C _{rss} | — | 3.2 | — | | |
| Series Gate Resistance | R _g | — | 4.7 | — | Ω | f = 1MHz, V _{GS} = 0V, V _{DS} = 0V |
| Total Gate Charge | Q _g | — | 1.34 | — | nC | V _{GS} = -4.5V, V _{DS} = -10V, I _D = -0.5A |
| Gate-Source Charge | Q _{gs} | — | 0.12 | — | | |
| Gate-Drain Charge | Q _{gd} | — | 0.15 | — | | |
| Gate Charge at V _{TH} | Q _{g(TH)} | — | 0.24 | — | | |
| Turn-On Delay Time | t _{d(ON)} | — | 15.4 | — | ns | V _{DS} = -10V, V _{GS} = -4.5V, R _g = 2Ω, I _D = -0.5A |
| Turn-On Rise Time | t _r | — | 5.7 | — | | |
| Turn-Off Delay Time | t _{d(OFF)} | — | 5.8 | — | | |
| Turn-Off Fall Time | t _f | — | 5.4 | — | | |

- Notes:
- Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
 - Repetitive rating, pulse width limited by junction temperature.
 - Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

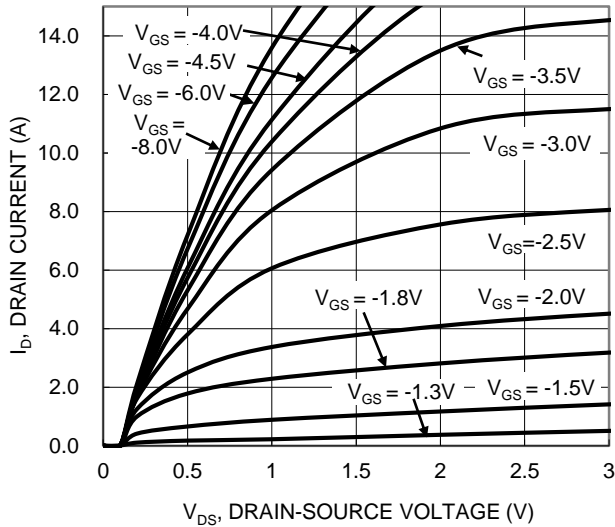


Figure 1. Typical Output Characteristic

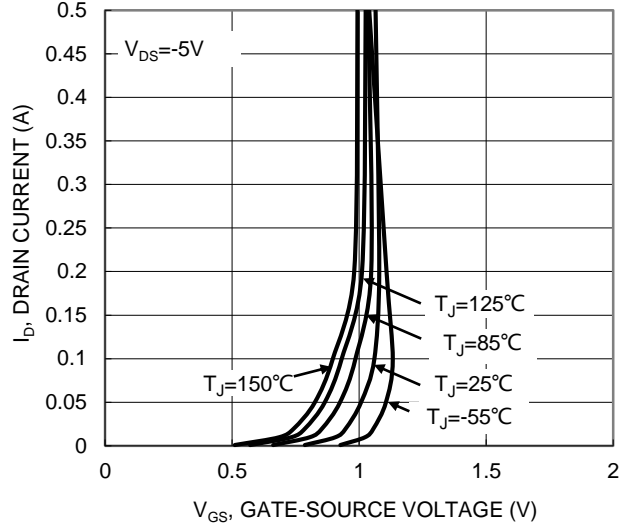


Figure 2. Typical Transfer Characteristic

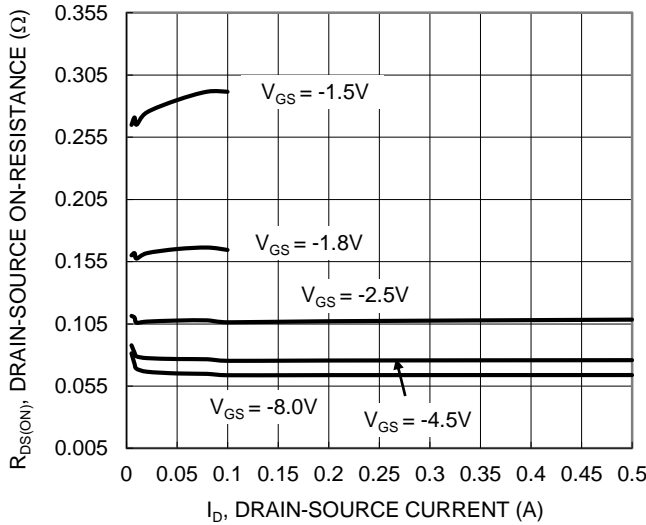


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

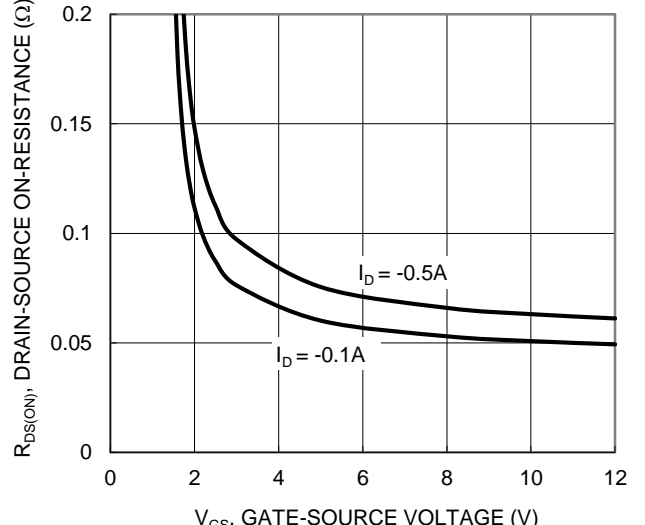


Figure 4. Typical Transfer Characteristic

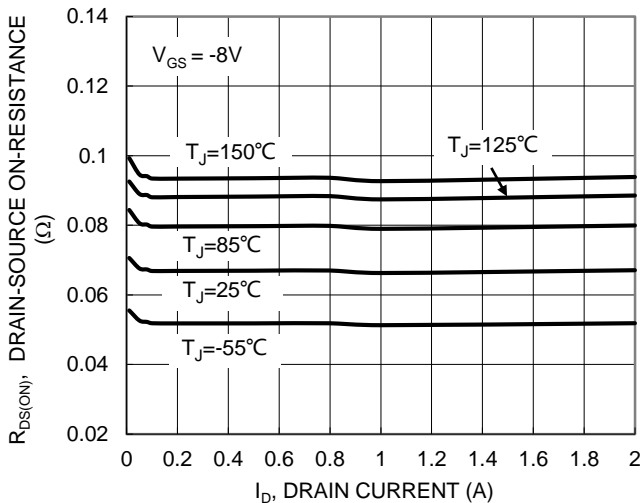


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

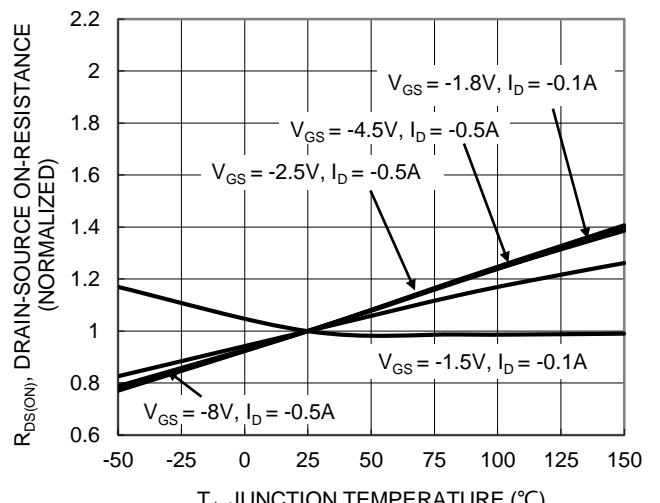


Figure 6. On-Resistance Variation with Junction Temperature

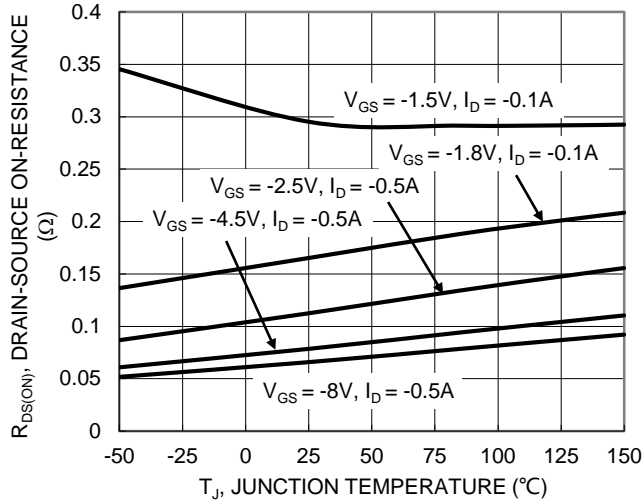


Figure 7. On-Resistance Variation with Junction Temperature

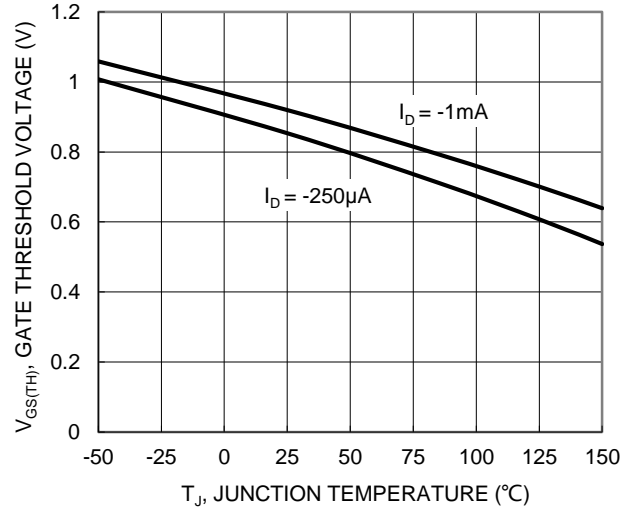


Figure 8. Gate Threshold Variation vs. Junction Temperature

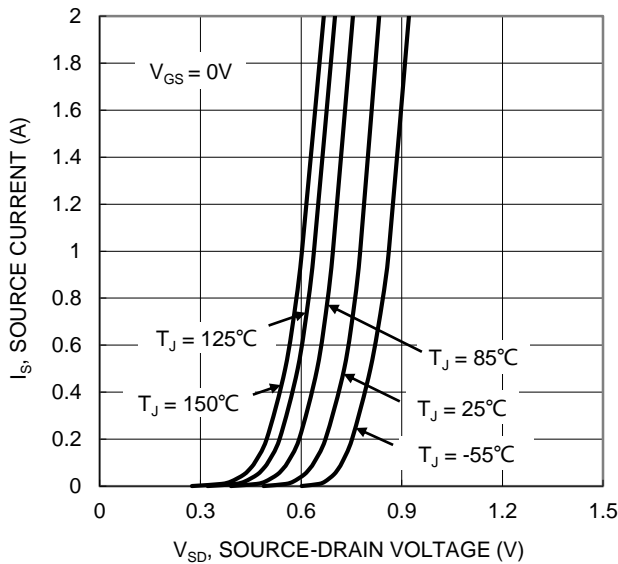


Figure 9. Diode Forward Voltage vs. Current

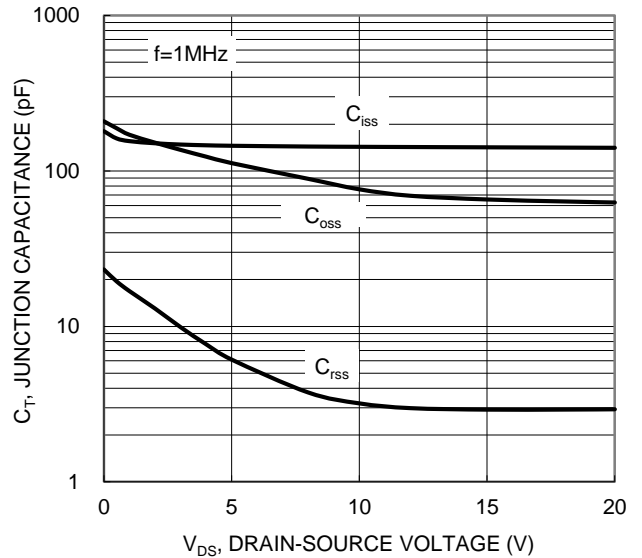


Figure 10. Typical Junction Capacitance

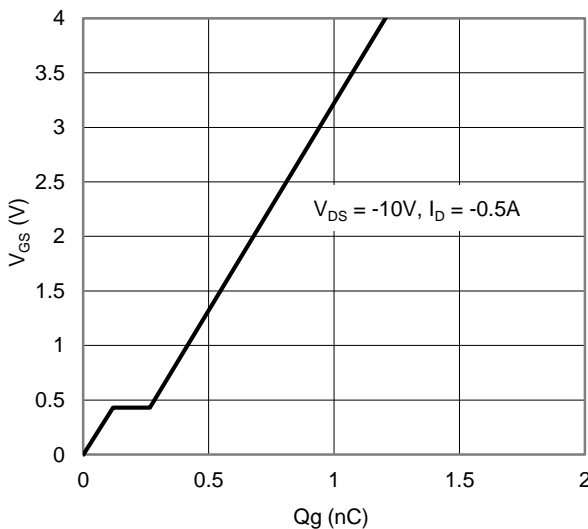


Figure 11. Gate Charge

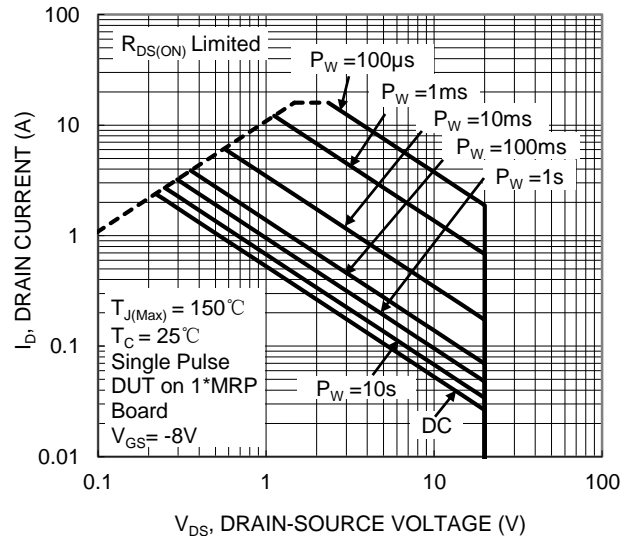


Figure 12. SOA, Safe Operation Area

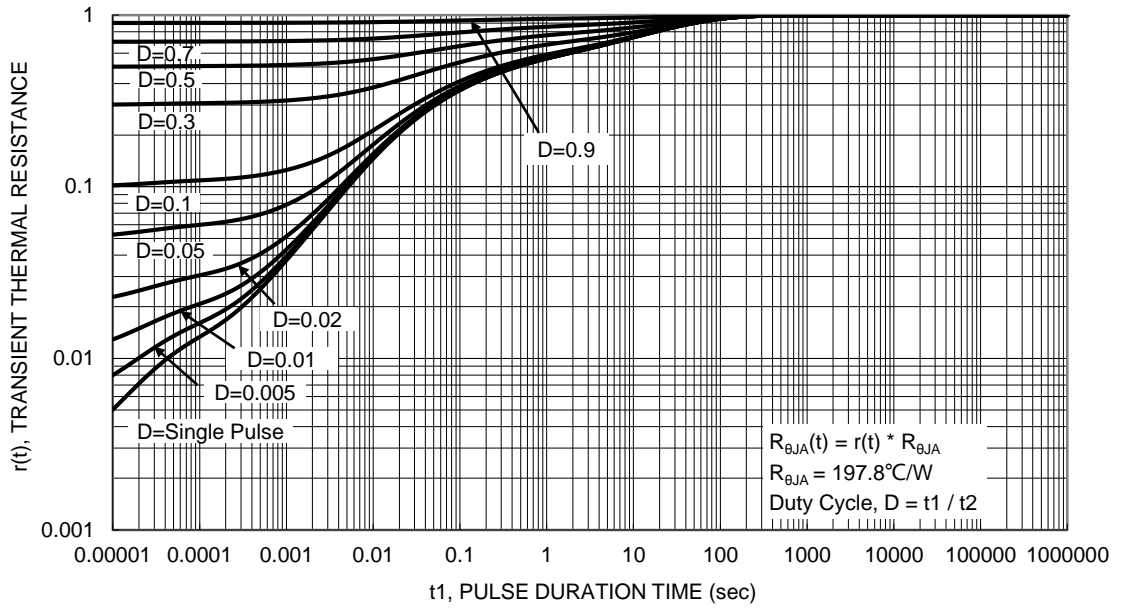
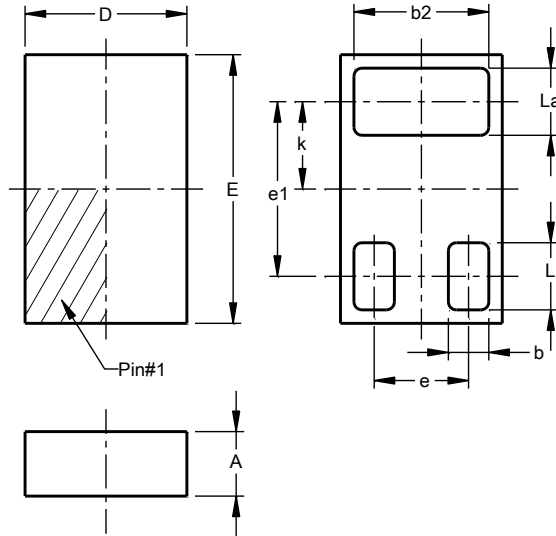


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X4-DSN1006-3

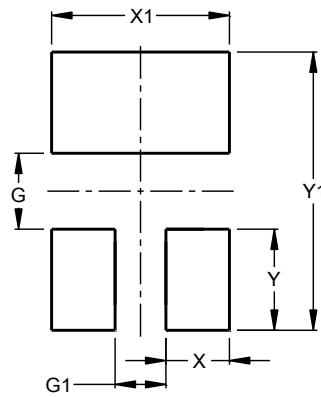


| X4-DSN1006-3 | | | |
|----------------------|------|------|-------|
| Dim | Min | Max | Typ |
| A | 0.18 | 0.22 | 0.20 |
| b | 0.14 | 0.16 | 0.15 |
| b2 | 0.49 | 0.51 | 0.50 |
| D | 0.56 | 0.64 | 0.60 |
| E | 0.96 | 1.04 | 1.00 |
| e | -- | -- | 0.35 |
| e1 | -- | -- | 0.65 |
| k | -- | -- | 0.325 |
| L | 0.24 | 0.26 | 0.25 |
| La | 0.24 | 0.26 | 0.25 |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X4-DSN1006-3

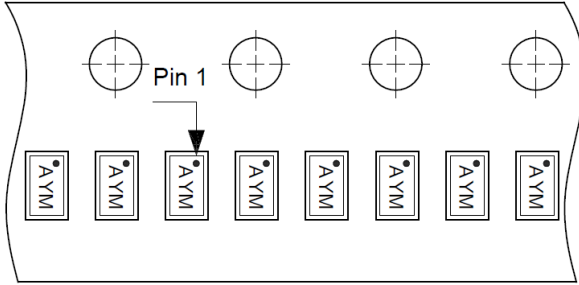


| Dimensions | Value (in mm) |
|------------|---------------|
| G | 0.40 |
| G1 | 0.20 |
| X | 0.15 |
| X1 | 0.50 |
| Y | 0.25 |
| Y1 | 0.90 |

Tape and Reel Information

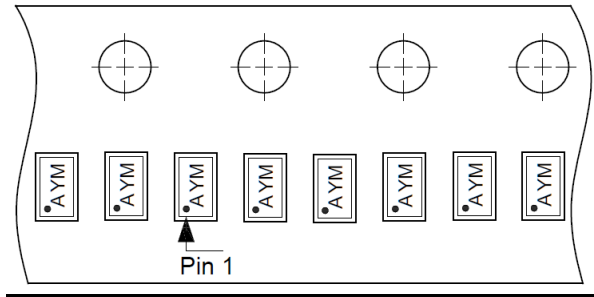
Please see <https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf> for the latest version.

DMP2077UCA3-7



DMP2077UCA3-7A

Change the PIN1 orientation in the carrier tape, rotate 180 degrees (Top side).



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