

### NOT RECOMMENDED FOR NEW DESIGN **USE AP2205**



**AP2204** 

#### WIDE INPUT VOLTAGE RANGE, 150mA ULDO REGULATOR

### **Description**

The AP2204 series is a positive voltage regulator IC fabricated by high voltage EPNP process.

The AP2204 has features of wide input voltage range, high accuracy, high ripple rejection, low dropout voltage, low noise, current limit and ultra-low guiescent current which make it ideal for use in various USB and portable devices.

The IC consists of a voltage reference, an error amplifier, a resistor network for setting output voltage, a current limit circuit for current protection, and a chip enable circuit.

The AP2204 has 1.5V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V, 5.0V fixed voltage versions and adjustable voltage version.

The AP2204 is available in space-saving SOT-23-5, SOT-89 and PSOP-8 packages.

#### **Features**

- Wide Input Voltage Range: 2.6V to 24V
- Wide Output Voltage Range: 1.24V to 22V
- Excellent Ripple Rejection: 60dB@ f = 1kHz
- Low Dropout Voltage: VDROP = 100mV@ IOUT = 100µA
- Low Ground Current
- High Output Voltage Accuracy
- Compatible with Low ESR Ceramic Capacitor
- **Excellent Line/Load Regulation**
- Thermal Shutdown Function
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

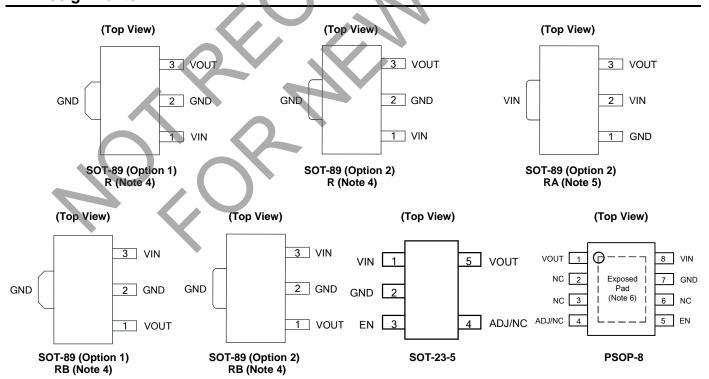
## **Applications**

- Battery-powered Equipment
- Laptop, Palmtops, Notebook Computers
- Portable Information Appliances

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

# Pin Assignments

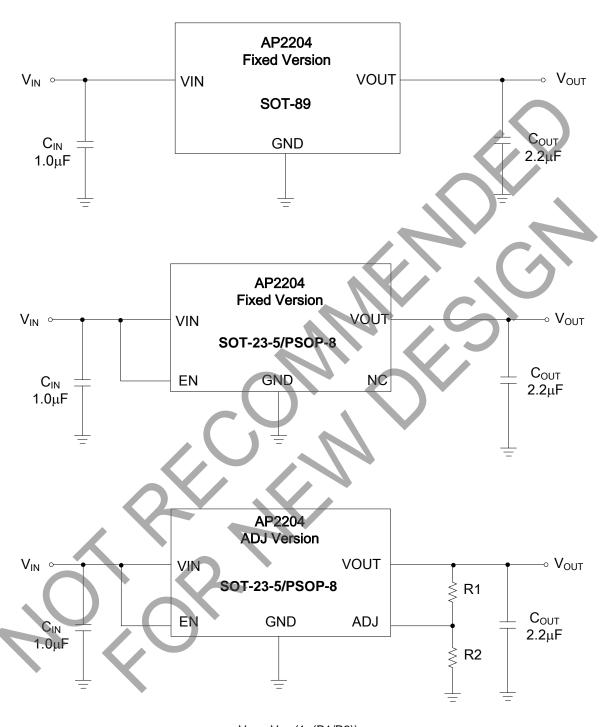


Notes:

- 4. The substrate/exposed pad should be connected to GND.
- 5. The substrate/exposed pad should be connected to VIN.
- 6. The exposed pad should be connected to GND for better dissipation



# **Typical Applications Circuit**



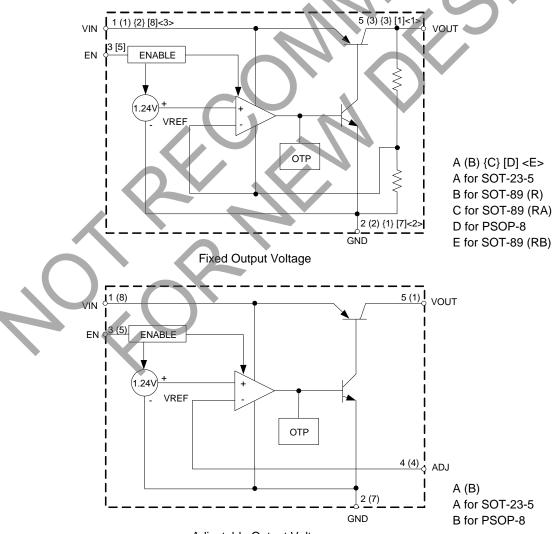
 $V_{OUT}=V_{REF}(1+(R1/R2))$ 



# **Pin Descriptions**

	Pin	Number					
COT 22 F	PSOP-8	SOT-89			Pin Name	Function	
SOT-23-5		R	RA	RB	Numo		
1	8	1	2	3	VIN	Input voltage	
2	7	2	1	2	GND	Ground	
3	5	_	_	_	EN	Enable input	
4	4	-	-	_	ADJ/NC	ADJ- Adjust output for ADJ version  NC- Not connected for fixed version, Not Connected internally.  Recommend connection to GND to maximize PCB copper for thermal dissipation.	
5	1	3	3	1	VOUT	Regulated output voltage	

# **Functional Block Diagram**





# **Absolute Maximum Ratings** (Note 7)

Symbol	Parameter	Rating	Unit
Vin	Supply Input Voltage	38	V
Vce	Enable Input Voltage	38	V
Іоит	Output Current	250	mA
T <sub>LEAD</sub>	Lead Temperature (Soldering, 10sec)	+260	ů
TJ	Operating Junction Temperature	+150	°C
θја	Thermal Resistance	SOT-23-5 250 SOT-89 165 PSOP-8 (Note 8) 51	°C/W
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
_	ESD (Machine Model)	275	V
_	ESD (Human Body Model)	2000	V

Notes:

# **Recommended Operating Conditions**

Symbol	Parameter	Min	Мах	Unit
Vin	Supply Input Voltage	2.6 (Note 9)	24	V
TJ	Operating Junction Temperature	-40	+125	°C

Note:

9. Minimum recommended input voltage is the larger of 2.6V or V<sub>OUT</sub> + 1V. Below this value the device may enter drop-out conditions and cease to regulate the output voltage correctly.

Stresses greater than those listed under "Absolute Maximum Ratings" can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods can affect device reliability.
 θ<sub>JA</sub> is measured with the component mounted on a 2-Layer FR-4 PCB board with 1.5cm\*1.5cm thermal sink pad in free air.



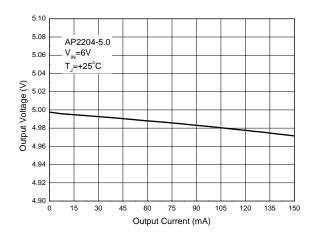
**Electrical Characteristics** (@V<sub>IN</sub> = V<sub>OUT</sub>+1V, T<sub>J</sub> = +25°C, I<sub>OUT</sub> = 100 $\mu$ A, C<sub>IN</sub> = 1.0 $\mu$ F, C<sub>OUT</sub> = 2.2 $\mu$ F, **Bold** typeface applies over -40°C  $\leq$  T<sub>J</sub>  $\leq$  +125°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
Vouт	Output Voltage	Variation from Specified Vout	Vоит ×98%	-	V <sub>О</sub> Т ×102%	V		
Vref	Reference Voltage	_	1.215	1.24	1.265	V		
VIN	Input Voltage	1	_	-	24	V		
I <sub>OUT(max)</sub>	Maximum Output Current	$V_{IN}$ - $V_{OUT}$ = 1V, $V_{OUT}$ = 98% × $V_{OUT}$	150	200		mA		
ΔVουτ/ΔVιν	Line Regulation	V <sub>OUT</sub> +1V ≤ V <sub>IN</sub> ≤ 24V	_	0.05	-	%		
ΔVουτ/Vουτ	Load Regulation	1mA ≤ I <sub>OUT</sub> ≤ 150mA		0.5		%		
		Ιουτ = 100μΑ	1-1	100	150			
V	D	I <sub>OUT</sub> = 50mA		270	350	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
VDROP	Dropout Voltage	Iout = 100mA	-	320	460	mV		
		IOUT = 150mA		360	500			
		IOUT = 0A		20	ı			
		Ιουτ = 100μΑ	7	50	-	μA		
I <sub>GND</sub>	Ground Current	I <sub>OUT</sub> = 50mA	<b>X</b> -/	0.5	_			
		Iout = 100mA	Y	1.3	_	mA		
		Iouт = 150mA	_	2.5	-			
I <sub>STD</sub>	Standby Current	V <sub>IN</sub> = V <sub>OUT</sub> +1V V <sub>EN</sub> in OFF Mode	_	0.01	1.0	μΑ		
		Ripple 0.5V <sub>P-P</sub>	_	60	_			
PSRR	Power Supply Rejection Ration	$V_{IN} = V_{OUT} + 1V$ $f = 1kHz$	_	60	_	dB		
ΔVουτ/(Vουτ×ΔΤ)	Output Voltage Temperature Coefficient	$lout = 100\mu A$ , $-40 ^{\circ}C \le T_{J} \le +125 ^{\circ}C$	_	±100	1	ppm/°C		
V <sub>NOI</sub>	RMS Output Noise	T <sub>J</sub> = +25°C, 10Hz ≤ f ≤ 100kHz	_	30	-	$\mu V_{\text{rms}}$		
I <sub>ADJ</sub>	ADJ Pin Current	Ιουτ = 100μΑ	_	0.5	_	μΑ		
len	EN Pin Current	VEN = VOUT+1V	_	1	_	μΑ		
	EN "High" Voltage	EN Input Voltage "High"	2.0	_	_	V		
-	EN "Low" Voltage	EN Input Voltage "Low"	_	_	0.4	V		
		SOT-23-5	_	43	_			
θις	Thermal Resistance (Junction to Case)	SOT-89	_	27	_	°C/W		
	(surrous)	PSOP-8	_	22	_			

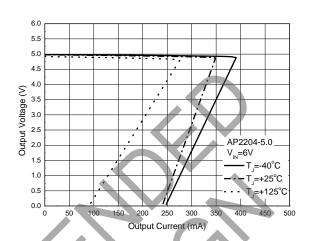


### **Performance Characteristics**

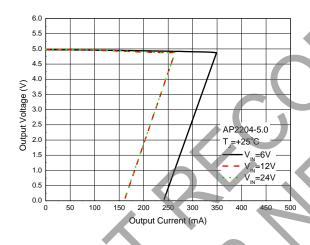
#### **Output Voltage vs. Output Current**



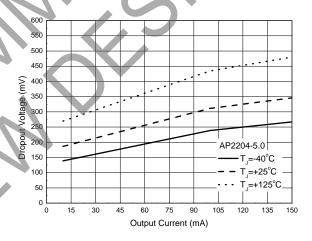
#### **Output Voltage vs. Output Current**



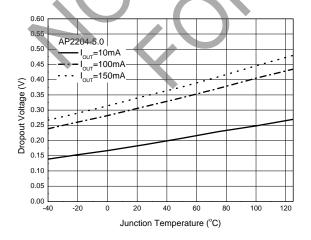
### **Output Voltage vs. Output Current**



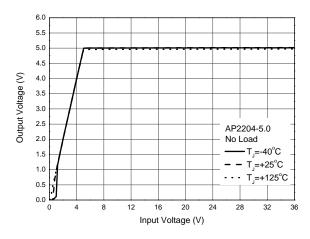
# **Dropout Voltage vs. Output Current**



### **Dropout Voltage vs. Junction Temperature**



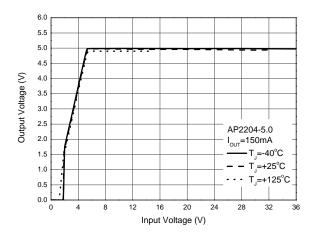
#### **Output Voltage vs. Input Voltage**



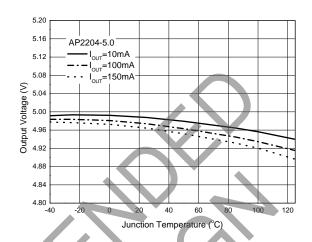


### **Performance Characteristics** (continued)

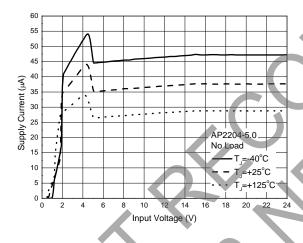
#### **Output Voltage vs. Input Voltage**



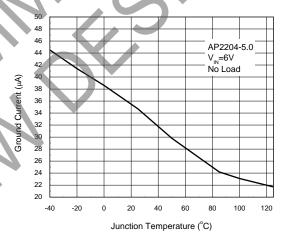
#### **Output Voltage vs. Junction Temperature**



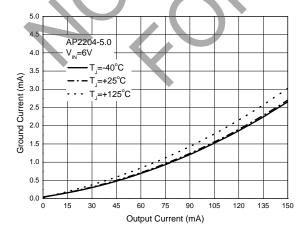
### Supply Current vs. Input Voltage



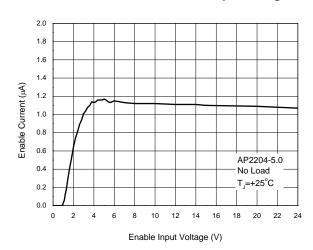
### **Ground Current vs. Junction Temperature**



#### **Ground Current vs. Output Current**



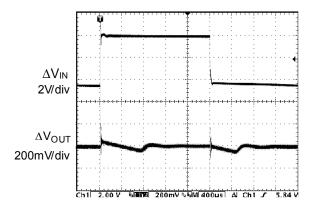
#### **Enable Current vs. Enable Input Voltage**



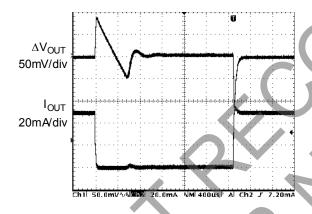


# **Performance Characteristics** (continued)

 $\label{eq:line_line} Line Transient $$ (Conditions: V_{IN}=V_{EN}=3.5V$ to 8V, $C_{IN}=1.0\mu F, $$ C_{OUT}=2.2\mu F, $I_{OUT}=1mA)$$ 

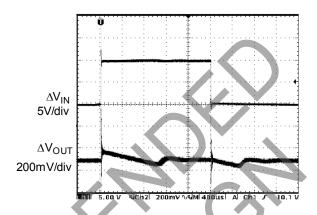


Load Transient (Conditions: V<sub>IN</sub>=5V, C<sub>IN</sub>=1.0μF, C<sub>OUT</sub>=2.2μF, I<sub>OUT</sub>=1mA to 50mA)

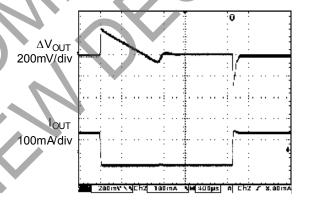


# **Enable Input Response**

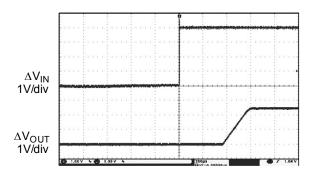




Load Transient (Conditions: V<sub>IN</sub>=5V, C<sub>IN</sub>=1.0μF, C<sub>OUT</sub>=2.2μF, Ι<sub>ΟUT</sub>=1mA to 150mA)



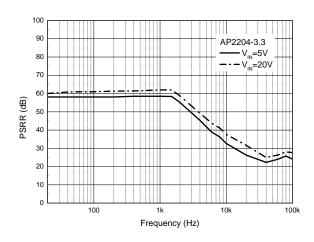
#### Start-up Response



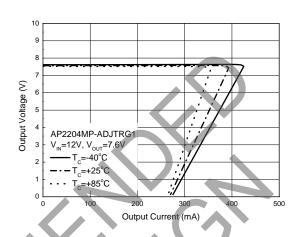


# **Performance Characteristics** (continued)

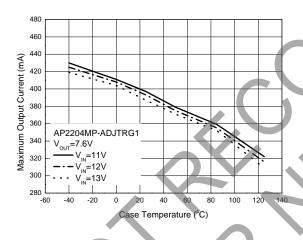
# PSRR vs. Frequency (Conditions: V<sub>PP</sub>=2V, I<sub>OUT</sub>=10mA)



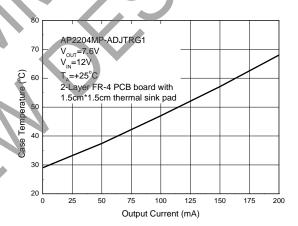
#### **Output Voltage vs. Output Current**



# Maximum Output Current vs. Case Temperature

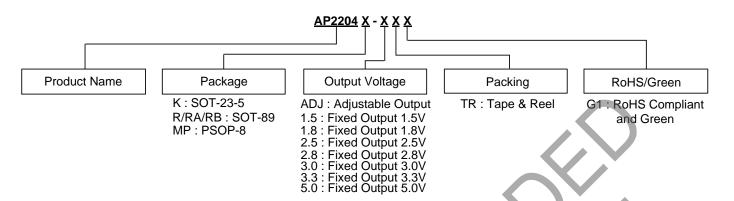


### Case Temperature vs. Output Current





# **Ordering Information**

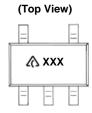


Package	Temperature Range	Output Voltage	Part Number	Marking ID	Packing
		ADJ	AP2204K-ADJTRG1	GAF	3000/7" Tape & Reel
		1.5V	AP2204K-1.5TRG1	GBH	3000/7" Tape & Reel
		1.8V	AP2204K-1.8TRG1	GAG	3000/7" Tape & Reel
COT 22 5	40.45 + 0590	2.5V	AP2204K-2.5TRG1	GAD	3000/7" Tape & Reel
SOT-23-5	-40 to +85°C	2.8V	AP2204K-2.8TRG1	GAE	3000/7" Tape & Reel
		3.0V	AP2204K-3.0TRG1	GEF	3000/7" Tape & Reel
		3.3V	AP2204K-3.3TRG1	GAH	3000/7" Tape & Reel
		5.0V	AP2204K-5.0TRG1	GAI	3000/7" Tape & Reel
	Q.	1.5V (R)	AP2204R-1.5TRG1	G22C	1000/7" Tape & Reel
		1.8V (R)	AP2204R-1.8TRG1	G31C	1000/7" Tape & Reel
		2.5V (R)	AP2204R-2.5TRG1	G22D	1000/7" Tape & Reel
SOT-89	-40 to +85°C	2.8V (R)	AP2204R-2.8TRG1	G22E	1000/7" Tape & Reel
		3.0V (R)	AP2204R-3.0TRG1	G22F	1000/7" Tape & Reel
	0	3.3V (R)	AP2204R-3.3TRG1	G31D	1000/7" Tape & Reel
		5.0V (R)	AP2204R-5.0TRG1	G31E	1000/7" Tape & Reel
SOT-89	40 +0 10500	3.3V (RA)	AP2204RA-3.3TRG1	G37O	1000/7" Tape & Reel
501-89	-40 to +85°C	5.0V (RA)	AP2204RA-5.0TRG1	G41O	1000/7" Tape & Reel
COT 00	40 to 195°C	3.3V (RB)	AP2204RB-3.3TRG1	G37R	1000/7" Tape & Reel
SOT-89	-40 to +85°C	5.0V (RB)	AP2204RB-5.0TRG1	G41R	1000/7" Tape & Reel
PSOP-8	-40 to +85°C	ADJ	AP2204MP-ADJTRG1	2204MP-ADJG1	4000/13" Tape & Reel



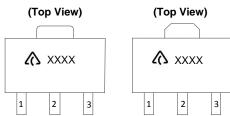
# **Marking Information**

### (1) SOT-23-5



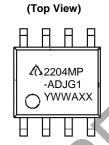
XXX: Marking ID (See Ordering Information)

### (2) SOT-89



First Line: Logo and Marking ID (See Ordering Information)

### (3) PSOP-8



First and Second Lines: Logo and Marking ID (See Ordering Information)
Third Line: Date Code

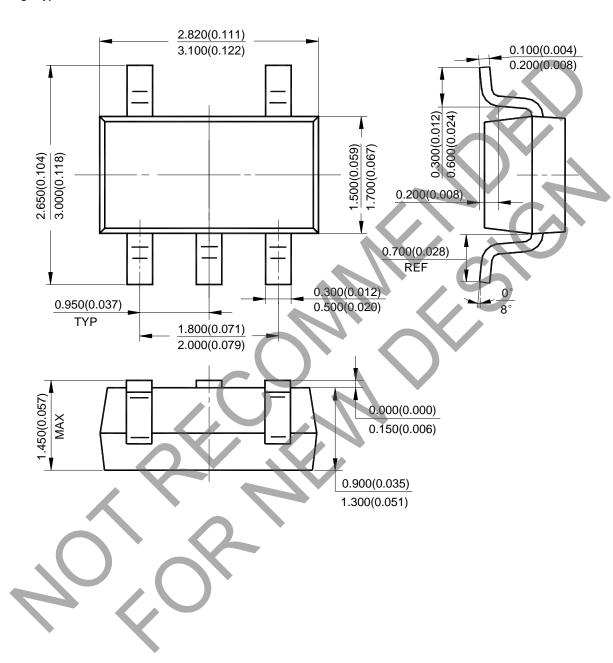
Y: Year
WW: Work Week of Molding
A: Assembly House Code
XX: 7<sup>th</sup> and 8<sup>th</sup> Digits of Batch Number



## Package Outline Dimensions (All dimensions in mm(inch).)

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### (1) Package Type: SOT-23-5

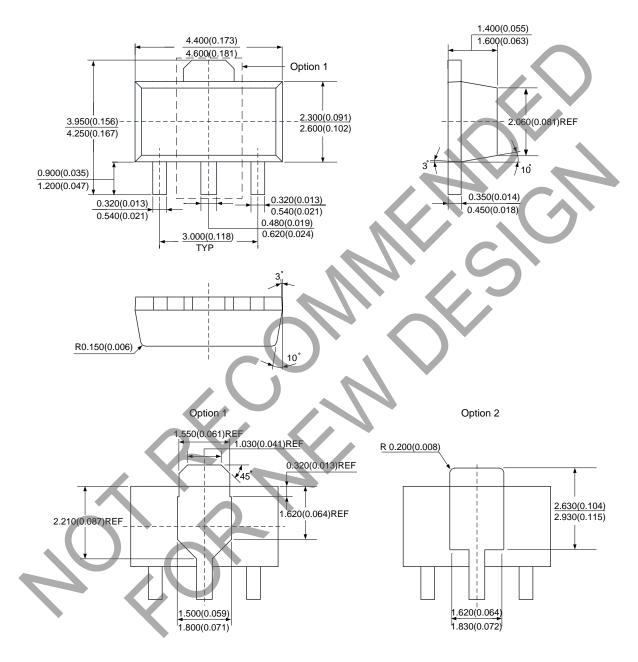




### Package Outline Dimensions (All dimensions in mm(inch).) (continued)

Please see http://www.diodes.com/package-outlines.html for the latest version.

### (2) Package Type: SOT-89

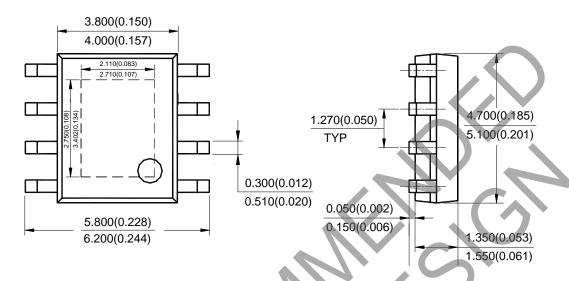


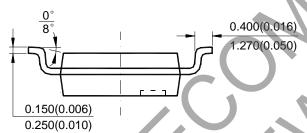


### Package Outline Dimensions (All dimensions in mm(inch).) (continued)

Please see http://www.diodes.com/package-outlines.html for the latest version.

### (3) Package Type: PSOP-8





Note: Eject hole, oriented hole and mold mark is optional.

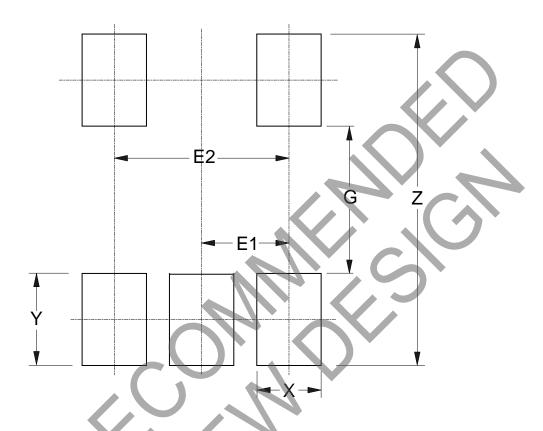
Downloaded from **Arrow.com**.



# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### (1) Package Type: SOT-23-5



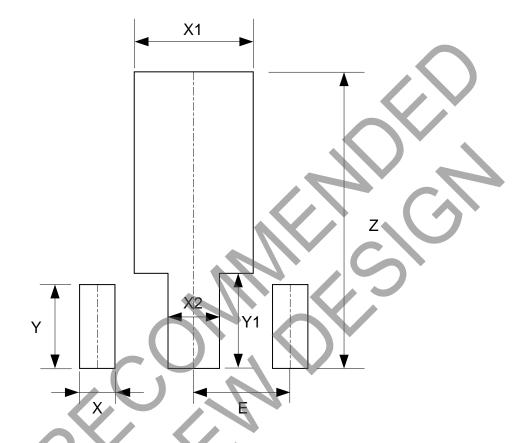
Dimensions	Z (mm)/(inch)	G (mm)/(inch)	(mm)/(inch)	Y (mm)/(inch)	E1 (mm)/(inch)	E2 (mm)/(inch)
Value	3.600/0.142	1.600/0.063	0.700/0.028	1.000/0.039	0.950/0.037	1.900/0.075



# Suggested Pad Layout (continued)

Please see http://www.diodes.com/package-outlines.html for the latest version.

### (2) Package Type: SOT-89



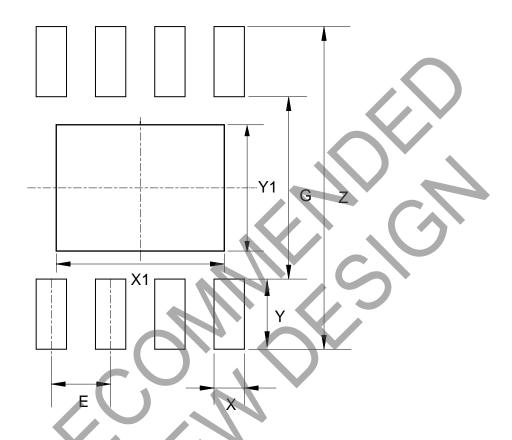
Dimensions	Z	X	X1	X2	Υ	Y1	Е
Diffictisions	(mm)/(inch)						
Value	4.600/0.181	0.550/0.022	1.850/0.073	0.800/0.031	1.300/0.051	1.475/0.058	1.500/0.059



# Suggested Pad Layout (continued)

Please see http://www.diodes.com/package-outlines.html for the latest version.

### (3) Package Type: PSOP-8



Dimensions	Z	G	X	Y	X1	Y1	E
	(mm)/(inch)						
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	3.600/0.142	2.700/0.106	1.270/0.050



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