



SPECIFICATION FOR APPROVAL

CUSTOMER	
NOMINAL FREQUENCY	32.768 KHz
PRODUCT TYPE	TYPE KK 3.2x2.5 SEAM SEALED CRYSTAL CLOCK OSCILLATOR
SPEC. NO. (P/N)	KK3270019
CUSTOMER P/N	
ISSUE DATE	March 9, 2018
VERSION	E

APPROVED	PREPARED	QA
Brenda	Pess	Dong Jong

Diodes Incorporated

No.2, Ziqiang 5th Rd., Zhongli Industrial Park, Zhongli Dist., Taoyuan City 32063, Taiwan (R.O.C.)

TEL: 886-3-451-8888 FAX: 886-3-461-3865 https://www.diodes.com

- *Pb-free
- *RoHS Compliant
- *HF-Halogen Free
- *REACH Compliant

E0-R-4-014 Rev. F

KK3270019

VER. E

9-Mar-18

VERSION HISTORY

Version No.	Version Date	Description	Notes
А	Feb.26,2009	Initial Release	
В	May.20,2010	Change Output Wave form TR/TF to TF/TR Position	
С	Aug.17,2010	Changed Logo	
D	Apr.14,2011	Added Start up time: 10ms max & Updated Output Enable / Disable Function table	
E	Mar.9,2018	Updated logo	



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ELECTRICAL SPECIFICATIONS

SRe Part Number: KK3270019

Item	Symbol	Specifications	Units	Notes
Nominal Frequency	Fo	32.768	KHz	
Frequency Stability	FT	± 50	ppm	**See note
Operating Temperature Range	TR	-20 to +70	°C	
Supply Voltage	V_{DD}	+3.3 ± 10%	V	
Logic Type	LT	LVCMOS		
Supply Current, Output Enabled	I _{DD} /OE	1	mA	Max.
Supply Current, Output Disabled	I _{DD} /OD	10	μA	Max.
Duty Cycle (Symmetry)	DC/SY	45 / 55	%	Measured 50% of Waveform
Rise / Fall Time	T _R /T _F	130	ns	Measured at 10 / 90% of Waveform
Output Voltage "0" Level	V _{OL}	10% V _{DD}	V	Max.
Output Voltage "1" Level	V _{OH}	90% V _{DD}	V	Min.
Output Load	CL	15	pF	Max.
Start Up Time		10	ms	Max.
Storage Temperature Range		-55°C to +125°C	°C	

This product doesn't include harmful substance that stipulated by SONY SS-00259 Level 1 and S-AT2-001 Level 1 standard. RoHS Compliant (Pb - Free).

Output Enable / Disable Function

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (Pin1), Output Enable	0.8			V	Or Open
Input Voltage (Pin1), Output Disable (low power standby)			0.4	V	Output is Hi-Z
Output Disable Delay			100	ns	
Output Enable Delay			10	ms	

DECES.

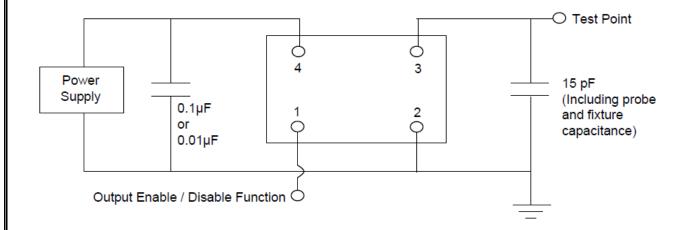
^{**}Stability includes all combinations of Operating Temperature, Load changes, rated Input (Supply) Voltage changes, Initial Calibration Tolerance (25°C), Aging (1 year at 25°C Average Effective Ambient Temperature), Shock and Vibration.

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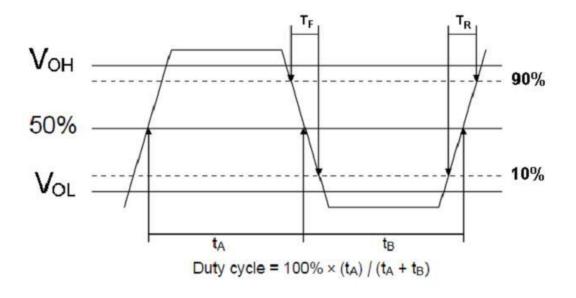
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TEST CIRCUIT



OUTPUT WAVEFORM





RELIABILITY SPECIFICATIONS

ENVIRONMENTAL:

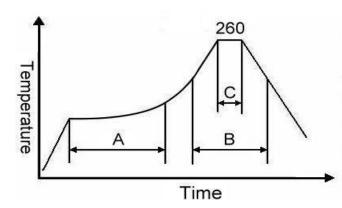
- a) THERMAL SHOCK: MIL-STD-883, Method 1011, Condition A
- b) MOISTURE RESISTANCE: MIL-STD-883, Method 1004
- c) VIBRATION: MIL-STD-883, Method 2007, Condition A
- d) RESISTANCE TO SOLDERING HEAT: J-STD-020D Table 5-2 Pb-free devices (except 2 cycles max)
- e) HAZARDOUS SUBSTANCE: Pb free and RoHS Compliant.

MECHANICAL:

- a) SHOCK: MIL-STD-883, Method 2002, Condition B
- b) SOLDERABILITY: JESD22-B102-D Method 2 (Preconditioning E)
- c) TERMINAL STRENGTH: MIL-STD-883, Method 2004, Test Condition D
- d) GROSS LEAK: MIL-STD-883, Method 1014, Condition C
- e) FINE LEAK: MIL-STD-883, Method 1014, Condition A2, R1=2x10⁻⁸ atm cc/s
- f) SOLVENT RESISTANCE: MIL-STD-202, Method 215

SUGGESTED IR REFLOW PROFILE

*As per IPC-JEDEC J-STD-020D



0.45	-	
M	ote	٠
	OLC	

	Stage	Temperature	Time
Α	Preheat	150~200°C	60~120 Sec
В	Primary Heat	217°C	60~150 Sec
С	Peak	260°C	10 Sec

INCORPORATED

E0-R-4-014 Rev. F

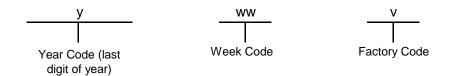
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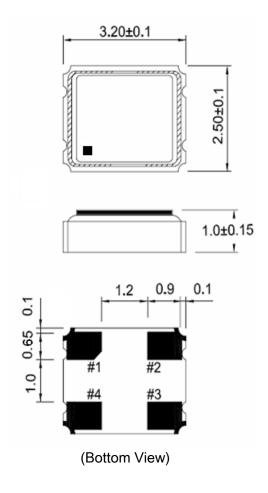
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MARKING

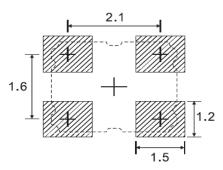




MECHANICAL DRAWINGS (Scale:None. Dimensions are in mm.)



Recommended Land Pattern*



*External high-frequency power decoupling is recommended.(see test circuit for minimum recommendation). To ensure optimal performance, do not route traces beneath the package.

Pin	Function
1	OE
2	Ground
3	Clock Output
4	V_{DD}

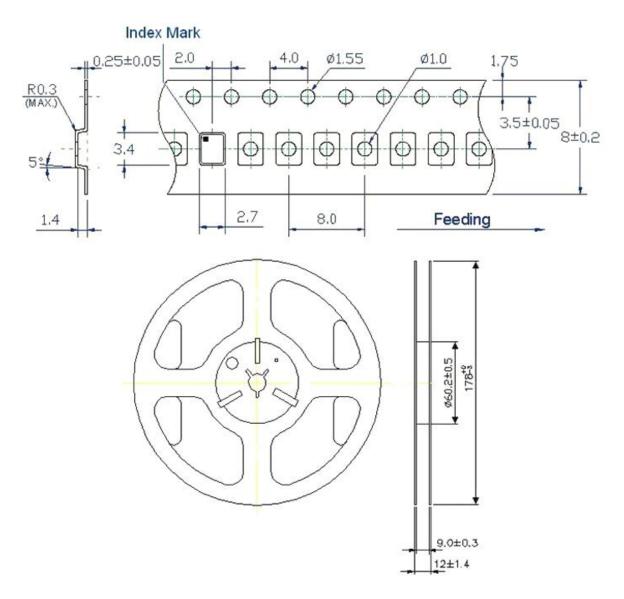


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TAPE&REEL



- 1. 230mm minimum leafer which consist of carrier and/or tape followed by a minimum of 160mm of empty carrier tape sealed with cover tape.
- 2. 160mm minimum trailer of empty carrier tape sealed with cover tape.



