

L05ESDL5V0F6-2

STAND-OFF VOLTAGE - 5.0 Volts ESD PROTECTION DEVICE POWER DISSIPATION - 50 WATTS **GENERAL DESCRIPTION** SOT-563 The L05ESDL5V0F6-2 is ultra low capacitance TVS arrays designed to protect high speed data interfaces. This series has been specifically SOT-563 designed to protect sensitive components which are connected to high-MIN. DIM. MAX. speed data and transmission lines from overvoltage caused by ESD (electrostatic discharge), CDE (Cable Discharge Events), and EFT 0.50 0.60 А (electrical fast transients). 0.15 0.3 b 0.10 0.18 с **FEATURES** D 1.50 1.70 Protects up to two I/O lines (Data lines) & power line (Vcc 1.70 pin5) Е 1.55 • Low capacitance: <0.9pF for high-speed interfaces E1 1.10 1.25 0.50 BSC Low clamping voltage е 0.10 0.30 L ● IEC 61000-4-2, level 4 (ESD), > ±15KV (air) ; > ±8KV All Dimensions in millimeter (contact). **APPLICATION** PIN ASSIGNMENT High Definition Multimedia Interface (HDMI) 1, 3 To Protected IC • Digital Visual Interface (DVI) 4,6 I/O Lines • 10/100/1000 Ethernet 5 Vcc 2 Ground • USB 2.0 High Speed Video Graphics Cards (VGA) IEEE 1394 Firewire Ports High Speed 6 1 2 Ð 5 **MECHANICAL DATA** • Case Material: "Green" molding compound UL flammability 4 2 lines Protection 3 classification 94V-0 (No Br.Sb, Cl) • Terminals: Lead Free Plating (Matte Tin Finish)

Component in accordance to RoHs 2002/95/E

MAXIMUM RATINGS (Tj= 25°C unless otherwise noticed)

Rating	Symbol	Value	Unit
Peak Pulse Power (tp = 8/20us)	Ррк	50	W
Peak Pulse Current (tp = 8/20us)	Ipp	3.0	А
Operating Junction Temperature Range	TJ	-55 to + 125	°C
Storage Temperature Range	Tstg	-55 to + 150	°C
Soldering Temperature, t max = 10s	TL	260	°C

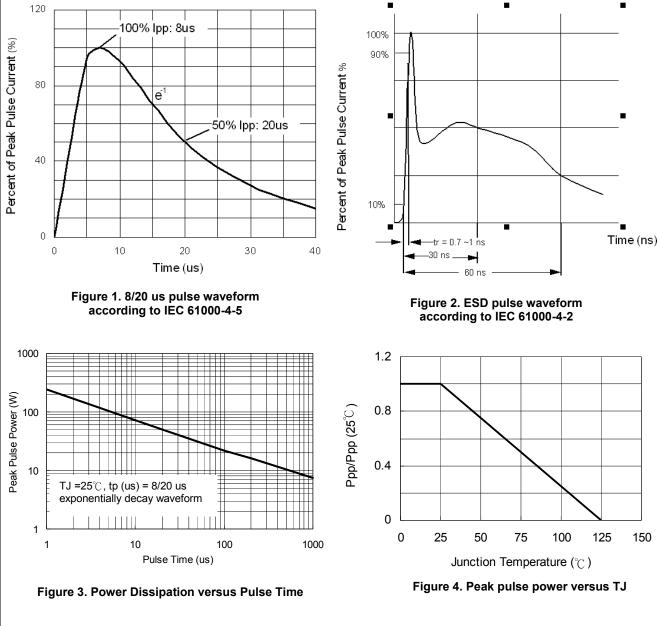
ELECTRICAL CHARACTERISTICS (Tj= 25°C unless otherwise noticed)

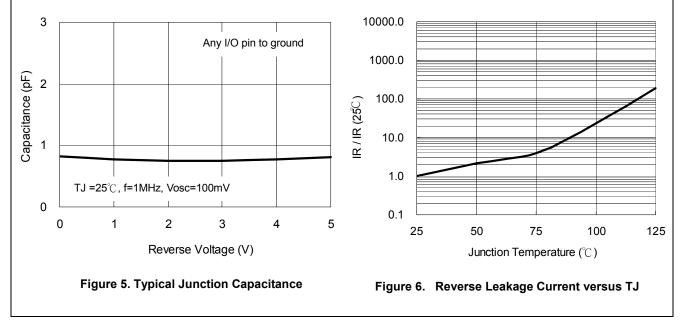
Parameter	Symbol	Conditions	Min	Тур	Мах	Unit
Reverse standoff voltage	V _{RWM}	Between I/O lines to Gnd or I/O to I/O			5.0	V
Breakdown voltage	VBR	IR = 1 mA, Between I/O lines to Gnd	6.0			V
Reverse leakage current	IRM	V_{DRM} = 5V, Between I/O lines to Gnd or I/O to I/O			1	uA
Clamping Voltage	Vc	I_{PP} = 1A, tp = 8/20 $\mu s,$ Between I/O lines to Gnd			14	V
Clamping Voltage	Vc	I_{PP} = 3A, tp = 8/20 $\mu s,$ Between I/O to Gnd			16	V
Junction capacitance	CJ	V_R = 0V, f = 1MHz, Between I/O to I/O		0.3	0.7	pF
Junction capacitance	CJ	V_R = 0V, f = 1MHz, Between I/O to Gnd		0.7	0.9	pF
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RATING AND CHARACTERISTIC CURVES L05ESDL5V0F6-2

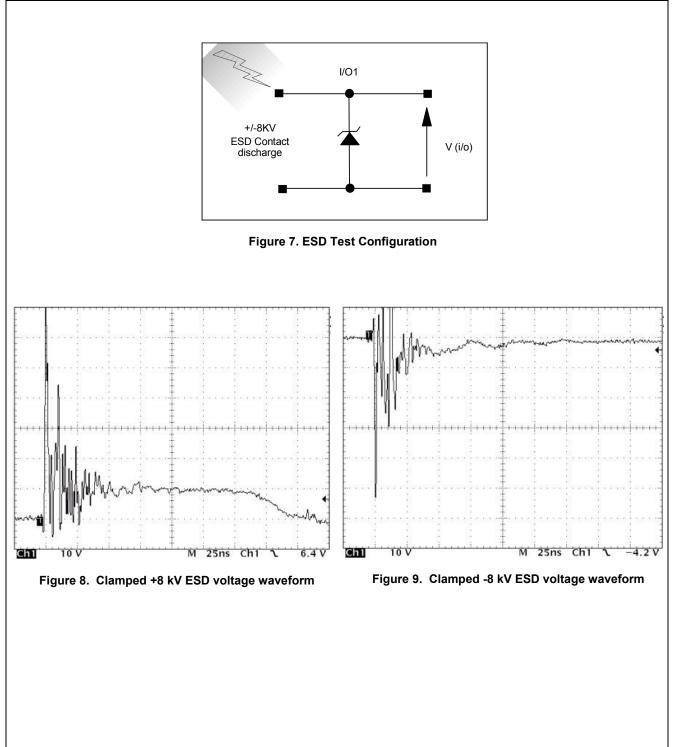




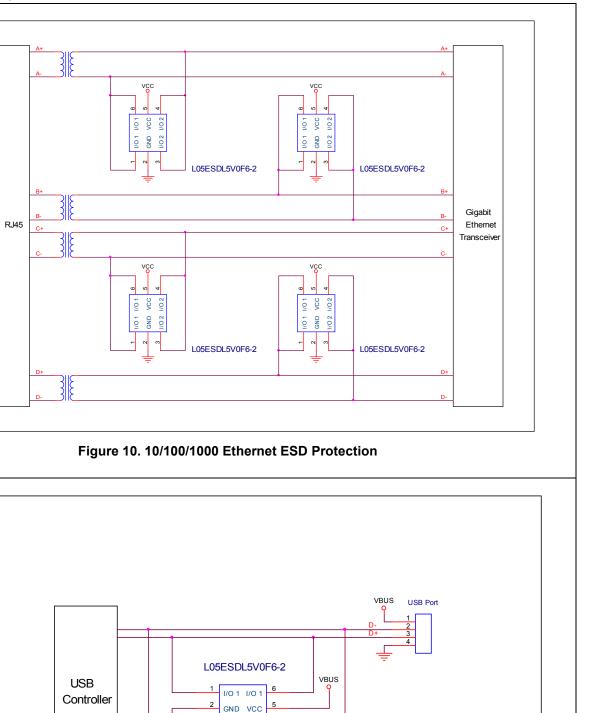


RATING AND CHARACTERISTIC CURVES L05ESDL5V0F6-2





Application Information L05ESDL5V0F6-2



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3 1/0 2 1/0 2

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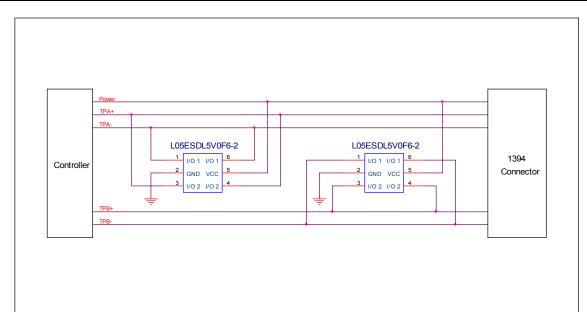
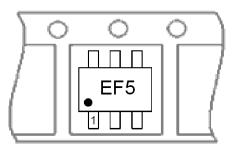


Figure 12. IEEE1394 Interface ESD Protection



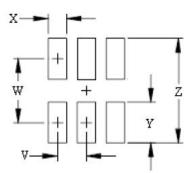
Marking & Orientation



Packaging Information

DEVICE	Q'TY/REEL	REEL DIA.	Q'TY/BOX	Q'TY/CARTON
	(PCS)	(INCH)	(PCS)	(PCS)
L05ESDL5V0F6-2	3000	7	45000	90K/180K

SOT-563 Soldering Pad Layout



Dim.	Millimeters	Inches
Ζ	2.30	0.090
Х	0.30	0.011
W	1.45	0.057
Y	0.85	0.033
V	0.50	0.019



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