(N)

Lead-free Green

## Features

$\rightarrow$ Near-Zero propagation delay
$\rightarrow$ 5-ohm switches connect inputs to outputs
$\rightarrow$ High signal passing bandwidth ( 500 MHz )
$\rightarrow$ Beyond Rail-to-Rail switching
-0 to 5 V switching with 3.3 V power supply

- 0 to 3.3 V switching with 2.5 V power supply
$\rightarrow 5 \mathrm{~V}$ I/O tolerant with supply in OFF and ON state
$\rightarrow \quad 1.8 \mathrm{~V}, 2.5 \mathrm{~V}$ and 3.3 V supply voltage operation
$\rightarrow$ Hot Insertion Capable
$\rightarrow$ Industrial Operating Temperature: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
$\rightarrow 8 \mathrm{kV}$ ESD Protection (human body model)
$\rightarrow$ Latch-up Performance: >200mA per JESD17
$\rightarrow$ Totally Lead-Free \& Fully RoHS Compliant (Notes 1 \& 2)
$\rightarrow$ Halogen and Antimony Free. "Green" Device (Note 3)
$\rightarrow \quad$ Packaging ( Pb -free \& Green available): -20-pin 173-mil wide plastic TSSOP (L) -20-pin TQFN(ZH)


## Block Diagram



## Description

The PI3CH3244 is a Low-voltage, 8-channel switch designed with fast enables. The switch creates no additional ground bounce noise or propagation delay.

The PI3CH3244 device has two active LOW enables. It is very useful in switching signals that have high bandwidth (500 MHz ).

## Applications

$\rightarrow$ High Bandwidth Data Switching
$\rightarrow$ Hot-Docking
$\rightarrow$ Analog Signal Switching
$\rightarrow$ Differential Signal Switching

## Truth Table ${ }^{(1)}$

| 1B1-1B4 | 2B1-2B4 | $\overline{\text { EN1 }}$ | $\overline{\text { EN2 }}$ | Function |
| :---: | :---: | :---: | :---: | :--- |
| Hi-Z | Hi-Z | H | H | Disconnect |
| 1A1-1A4 | Hi-Z | L | H | Connect |
| Hi-Z | AA1-2A4 | H | L | Connect |
| 1A1-1A4 | 2A1-2A4 | L | L | Connect |

Note:

1. $\mathrm{H}=$ High Voltage Level; L=Low Voltage Level $\mathrm{Hi}-\mathrm{Z}=$ High Impedance.

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) \& 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and $<1000 \mathrm{ppm}$ antimony compounds.

Pin Configuration


## Pin Description

| Pin No | Pin Name | Description |
| :---: | :---: | :---: |
| 1,19 | $\overline{\mathrm{EN}} \mathrm{A}$ | Switch Enable |
| $2,4,6,8$ | $1 \mathrm{~A} 1-1 \mathrm{~A} 4$ | A Ports |
| $3,5,7,9$ | $2 \mathrm{~B} 1-2 \mathrm{~B} 4$ | B Ports |
| 10 | GND | Ground |
| $12,14,16,18$ | $1 \mathrm{~B} 4-1 \mathrm{~B} 1$ | B Ports |
| $13,15,17,19$ | $2 \mathrm{~A} 4-2 \mathrm{~A} 1$ | A Ports |
| 20 | $\mathrm{~V}_{\mathrm{CC}}$ | Power |

## Maximum Ratings

| Storage Temperature ............................................................ $6 . .5^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |  |
| :---: | :---: |
| Ambient Temperature with Power Applied............................. $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  |
| Supply Voltage to Ground Potential ..............................................-0.5V to + + 4.6 V |  |
| DC Input Voltage ..................................................................0.5V to + + 6.0 .0 V |  |
| DC Output Current....................................................................... 120mA |  |
|  |  |


#### Abstract

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.


## DC Electrical Characteristics

3.3V supply (Over operating range, $\mathrm{T}_{\mathrm{A}}=-40 \sim+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V} \pm 10 \%$, unless otherwise noted)

| Symbol | Description | Test Conditions ${ }^{(1)}$ | Min | Typ ${ }^{(2)}$ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{IH}}$ | Control Input HIGH Voltage | Guaranteed Logic HIGH Level | 2.0 | - | - | V |
| $\mathrm{V}_{\text {IL }}$ | Control Input LOW Voltage | Guaranteed Logic LOW Level | -0.5 | - | 0.8 | V |
| $\mathrm{V}_{\text {IK }}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{CC}}=$ Min., $\mathrm{I}_{\mathrm{IN}}=-18 \mathrm{~mA}$ | - | -1.3 | -1.8 | V |
| $\mathrm{I}_{\mathrm{H}}$ | Input HIGH Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\mathrm{CC}}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\mathrm{IL}}$ | Input Low Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{GND}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {OZH }}$ | High-Impedance Current | $0 \leq \mathrm{A}, \mathrm{B} \leq \mathrm{V}_{\text {CC }}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{R}_{\text {ON }}$ | Switch On-Resistance ${ }^{(3)}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IV}}=0.0 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{ON}}=-48 \mathrm{~mA} \text { or }-64 \mathrm{~mA} \\ & \hline \mathrm{~V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IN}}=3.6 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{ON}}=-15 \mathrm{~mA} \end{aligned}$ | - | 4 5 | 6 8 | $\Omega$ |

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type
2. Typical values are at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{TA}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.
2.5 V supply (Over operating range, $\mathrm{T}_{\mathrm{A}}=-40 \sim+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=2.5 \mathrm{~V} \pm 10 \%$, unless otherwise noted)

| Symbol | Description | Test Conditions ${ }^{(1)}$ | Min | Typ ${ }^{(2)}$ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{IH}}$ | Control Input HIGH Voltage | Guaranteed Logic HIGH Level | 1.8 | - | $\mathrm{V}_{\mathrm{CC}}+0.3$ | V |
| $\mathrm{V}_{\text {IL }}$ | Control Input LOW Voltage | Guaranteed Logic LOW Level | -0.3 | - | 0.8 | V |
| $\mathrm{V}_{\text {IK }}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{I}_{\text {IN }}=-6 \mathrm{~mA}$ | - | -0.7 | -1.8 | V |
| $\mathrm{I}_{\mathrm{IH}}$ | Input HIGH Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {CC }}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\mathrm{IL}}$ | Input Low Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{GND}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {OZH }}$ | High-Impedance Current | $0 \leq \mathrm{A}, \mathrm{B} \leq \mathrm{V}_{\mathrm{CC}}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{R}_{\text {ON }}$ | Switch On-Resistance ${ }^{(3)}$ | $\begin{aligned} & \hline \mathrm{V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IN}}=0.0 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{ON}}=-48 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IN}}=2.25 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{ON}}=-15 \mathrm{~mA} \end{aligned}$ | - | 4 7 | 8 14 | $\Omega$ |

## Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $\mathrm{V}_{\mathrm{CC}}=2.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
3. Measured by the voltage drop between $A$ and $B$ pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.

A Product Line of Diodes Incorporated
1.8 V supply (Over operating range, $\mathrm{T}_{\mathrm{A}}=-40 \sim+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=1.8 \mathrm{~V} \pm 10 \%$, unless otherwise noted)

| Symbol | Description | Test Conditions ${ }^{(1)}$ | Min | Typ ${ }^{(2)}$ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{IH}}$ | Control Input HIGH Voltage | Guaranteed Logic HIGH Level | 1.2 | - | $\mathrm{V}_{\mathrm{CC}}+0.3$ | V |
| $\mathrm{V}_{\text {IL }}$ | Control Input LOW Voltage | Guaranteed Logic LOW Level | -0.3 | - | 0.6 | V |
| $\mathrm{V}_{\text {IK }}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{CC}}=$ Min., $\mathrm{I}_{\mathrm{IN}}=-18 \mathrm{~mA}$ | - | -0.7 | -1.8 | V |
| $\mathrm{I}_{\mathrm{IH}}$ | Input HIGH Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {CC }}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {IL }}$ | Input Low Current | $\mathrm{V}_{\text {CC }}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{GND}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {OZH }}$ | High-Impedance Current | $0 \leq \mathrm{A}, \mathrm{B} \leq \mathrm{V}_{\text {CC }}$ | - | - | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{R}_{\text {ON }}$ | Switch On-Resistance ${ }^{(3)}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IN}}=0.0 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{ON}}=-48 \mathrm{~mA} \end{aligned}$ | - | 4 | 8 | $\Omega$ |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IV}}=1.6 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{ON}}=-15 \mathrm{~mA} \end{aligned}$ | - | 10 | 25 |  |

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $\mathrm{V}_{\mathrm{CC}}=1.8 \mathrm{~V}, \mathrm{TA}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A, B) pins.

Capacitance $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{f}=1 \mathrm{MHz}\right)$

| Symbol ${ }^{(1)}$ | Description | Test Conditions | Typ ${ }^{(2)}$ | Unit |
| :---: | :---: | :---: | :---: | :---: |
| $\mathrm{C}_{\text {IN }}$ | Input Capacitance | $\mathrm{V}_{\text {IN }}=0 \mathrm{~V}$ | 2.0 | pF |
| $\mathrm{C}_{\text {OFF }}$ | A/B Capacitance, Switch Off |  | 3.5 |  |
| $\mathrm{C}_{\text {ON }}$ | A/B Capacitance, Switch On |  | 7.0 |  |

## Power Supply Characteristics

| Symbol | Description | Test Conditions ${ }^{(\mathbf{1 )}}$ | Min | Typ $^{(2)}$ | Max | Unit |
| :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\mathrm{CC}}$ | Quiescent Power Supply Current | $\mathrm{V}_{\mathrm{CC}}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}=\mathrm{GND}$ or <br> $\mathrm{V}_{\mathrm{CC}}$ | - | 0.2 | 0.5 | mA |

Note

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at $+25^{\circ} \mathrm{C}$ ambient.

## Dynamic Electrical Characteristics

(Over Operating Range, $\mathrm{T}_{\mathrm{A}}=-40 \sim+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V} \pm 10 \%$ )

| Symbol | Description | Test Conditions | Min | Typ | Max | Unit |
| :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{X}_{\text {TALK }}$ | Crosstalk | 10 MHz | - | -60 | - | dB |
| $\mathrm{O}_{\text {IRR }}$ | Off-Isolation | 10 MHz | - | -60 | - |  |
| BW | $-3 d B$ Bandwidth | See test Diagram | - | 500 | - | MHz |

A Product Line of Diodes Incorporated

## Switch Characteristics

Over 3.3V Operating Range

| Symbol | Description | Test Conditions ${ }^{(\mathbf{1 )}}$ | Min | Typ | Max | Unit |
| :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{t}_{\text {PLH, }} \mathrm{t}_{\text {PHL }}$ | Propagation Delay ${ }^{(2,3)}$ Ax to Bx, Bx to Ax | See test Diagram | - | - | 0.3 |  |
| $\mathrm{t}_{\text {PZH, }} \mathrm{t}_{\text {PZL }}$ | Enable Time $\overline{\mathrm{EN}}$ to Ax or Bx | See test Diagram | 1.5 | - | 9.0 | ns |
| $\mathrm{t}_{\text {PHZ, }} \mathrm{t}_{\text {PLZ }}$ | Disable Time $\overline{\mathrm{ENx}}$ to Ax or Bx | See test Diagram | 1.5 | - | 9.0 |  |

Note:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30 ns for 10 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Over 2.5V Operating Range

| Symbol | Description | Test Conditions ${ }^{(\mathbf{1 )}}$ | Min | Typ | Max | Unit |
| :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{t}_{\text {PLH, }}, \mathrm{t}_{\text {PHL }}$ | Propagation Delay ${ }^{(2,3)}$ Ax to Bx, Bx to Ax | See test Diagram | - | - | 0.3 |  |
| $\mathrm{t}_{\text {PZH, }} \mathrm{t}_{\text {PZL }}$ | Enable Time $\overline{\mathrm{ENX}}$ to Ax or Bx | See test Diagram | 1.5 | - | 15.0 | n |
| $\mathrm{t}_{\text {PHZ }}, \mathrm{t}_{\text {PLZ }}$ | Disable Time $\overline{\mathrm{ENx}}$ to Ax or Bx | See test Diagram | 1.5 | - | 12.0 |  |

Note:

1. See test circuit and waveforms
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30 ns for 10 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Over 1.8V Operating Range

| Symbol | Description | Test Conditions ${ }^{(\mathbf{1 )}}$ | Min | Typ | Max | Unit |
| :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{t}_{\text {PLH, }} \mathrm{t}_{\text {PHL }}$ | Propagation Delay ${ }^{(2,3)}$ Ax to Bx, Bx to Ax | See test Diagram | - | - | 0.3 |  |
| t $_{\text {PZH, }} \mathrm{t}_{\text {PZL }}$ | Enable Time $\overline{\mathrm{ENx}}$ to Ax or Bx | See test Diagram | 1.5 | - | 25.0 | n ns |
| $\mathrm{t}_{\text {PHZ, }} \mathrm{t}_{\text {PLZ }}$ | Disable Time $\overline{\mathrm{ENx}}$ to Ax or Bx | See test Diagram | 1.5 | - | 12.0 |  |

Note:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30 ns for 10 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## Test Circuit for Electrical Characteristics



Notes:

1. $\mathrm{CL}=$ Load capacitance: includes jig and probe capacitance.
2. RT $=$ Termination resistance: should be equal to ZOUT of the Pulse Generator
3. All input impulses are supplied by generators having the following characteristics: $\mathrm{PRR} \leq 10 \mathrm{MHz}, \mathrm{ZO}=50$ - ohm, $\mathrm{tR} \leq 2.5 \mathrm{~ns}, \mathrm{tF} \leq 2.5 \mathrm{~ns}$.
4.The outputs are measured one at a time with one transition per measurement.

## Switch Positions

| Test | Switch |
| :---: | :---: |
| telZ, $^{\text {tPZL }}$ | 6.0 V |
| t $_{\text {PHZ }}$ tPZH | GND |
| Prop Delay | Open |

## Test Circuit for Dynamic Electrical Characteristics



## Switching Waveforms



## Applications Information

## Logic Inputs

The logic control inputs can be driven up to 3.6 V regardless of the supply voltage. For example, given a +3.3 V supply, $\overline{\mathrm{EN}}$ may be driven LOW to 0 V and HIGH to 3.6 V . Driving EN Rail-to-Rail minimizes power consumption.

## Hot Insertion

For Datacom and Telecom applications that have ten or more volts passing through the backplane, a high voltage from the power supply may be seen at the device input pins during hot insertion. The PI3CH3244 devices have maximum limits of 6 V and 120 mA for 20 ns . If the power is higher or applied for a longer time or repeatedly reaches the maximum limits, the devices can be damaged.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

## Mechanical Packaging

```
20-TSSOP (L)
```



## 20-TQFN (ZH)



For latest package info.
please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

## Ordering Information

| Part No. | Package Code | Package Description |
| :--- | :---: | :--- |
| PI3CH3244LEX | L | 20-Pin, 173mil Wide (TSSOP) |
| PI3CH3244ZHEX | ZH | 20-contact, Very Thin Quad Flat No-Lead (TQFN) |

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) \& 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain $<900 \mathrm{ppm}$ bromine, $<900 \mathrm{ppm}$ chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl})$ and <1000ppm antimony compounds.
4. $\mathrm{E}=\mathrm{Pb}$-free and Green
5. X suffix = Tape/Reel

A Product Line of Diodes Incorporated

## IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION)
Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.
Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

## LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:
A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated
www.diodes.com

