

AP1115

0.6A LOW DROPOUT POSITIVE ADJUSTABLE OR FIXED-MODE REGULATOR

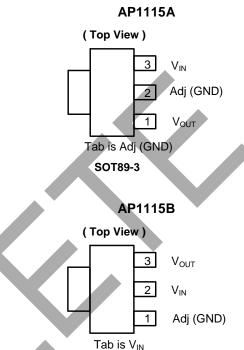
Description

The AP1115 is a low dropout positive adjustable or fixed mode regulator with 0.6A output current capability. The product is specifically designed to provide a well-regulated supply for low-voltage IC applications such as high-speed bus termination and low-current 3.3V logic supply. AP1115 is also well suited for other applications such as VGA cards. AP1115 is guaranteed to have <1.3V dropout at full load current, making it ideal to provide well regulated outputs of 1.25V to 5V with up to 18V input supply.

Features

- 1.3V Maximum Dropout at Full Load Current
- Fast Transient Response
- **Output Current Limiting**
- Built-in Thermal Shutdown
- Good Noise Rejection
- 3-Terminal Adjustable or Fixed 1.5V / 1.8V / 2.5V / 2.8V/ 3.0V / 3.3V / 3.5V / 5.0V
- Lead Free Package: SOT89-3L
 - Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- SOT89-3L: Available in "Green" Molding Compound (No Br, Sb)
 - Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



SOT89-3

Applications

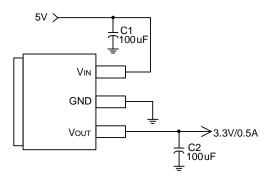
- PC Peripheral
- Communication

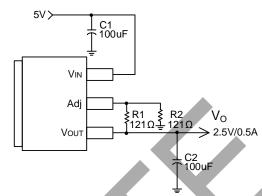
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



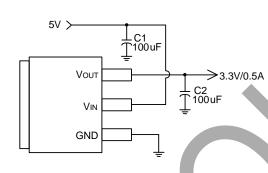
Typical Applications Circuit

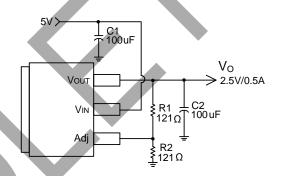




AP1115A (5V/3.3V Fixed Output)

AP1115 A (5V/2.5V Adj Output)





AP1115B (5V/3.3V Fixed Output)

AP1115B (5V/2.5V Adj Output)

Note:
$$V_0 = V_{REF} \times (1 + \frac{R_2}{R_1})$$

Pin Descriptions

Pin Name	1/0	Function
Adj (GND)	-	A resistor divider from this pin to the V_{OUT} pin and ground sets the output voltage (Ground only for Fixed-Mode).
V _{OUT}	0	The output of the regulator. A minimum of 10uF capacitor $(0.15\Omega \le ESR \le 20\Omega)$ must be connected from this pin to ground to insure stability.
V _{IN} I to ground to insu		The input pin of regulator. Typically a large storage capacitor $(0.15\Omega \le \text{ESR} \le 20\Omega)$ is connected from this pin to ground to insure that the input voltage does not sag below the minimum dropout voltage during the load transient response. This pin must always be 1.3V higher than V_{OUT} in order for the device to regulate properly.



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V _{IN}	DC Supply Voltage	-0.3 to +18	V
P _D	Power Dissipation	Internally Limited	mW
T _{ST}	Storage Temperature	-65 to +150	°C
T _{MJ}	Maximum Junction Temperature	+150	°C

Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
T _{OP}	Operating Junction Temperature Range	0	+125	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Parameter		Conditions	Min	Тур	Max	Unit
Reference Voltage	AP1115-ADJ	$T_A = +25$ °C, $(V_{IN}{OUT}) = 1.5$ V, $I_O = 10$ mA	1.225	1.250	1.275	V
	AP1115-1.5	$I_{OUT} = 10$ mA, $T_A = +25$ °C, 3 V $\leq V_{IN} \leq 12$ V	1.470	1.500	1.530	V
	AP1115-1.8	$I_{OUT} = 10$ mA, $T_A = +25$ °C, 3.3 V $\leq V_{IN} \leq 12$ V	1.764	1.800	1.836	V
	AP1115-2.5	$I_{OUT} = 10 \text{mA}, T_A = +25^{\circ}\text{C}, 4V \le V_{IN} \le 12V$	2.450	2.500	2.550	V
0	AP1115-2.8	$I_{OUT} = 10$ mA, $T_A = +25$ °C, 4.3 V $\leq V_{IN} \leq 12$ V	2.744	2.800	2.856	V
Output Voltage	AP1115-3.0	$I_{OUT} = 10$ mA, $T_A = +25$ °C, 4.5 V $\leq V_{IN} \leq 12$ V	2.940	3.000	3.060	V
	AP1115-3.3	$I_{OUT} = 10$ mA, $T_A = +25$ °C, 4.8 V $\leq V_{IN} \leq 12$ V	3.235	3.300	3.365	V
	AP1115-3.5	$I_{OUT} = 10 \text{mA}, T_A = +25 ^{\circ}\text{C}, 5 \text{V} \le V_{IN} \le 12 \text{V}$	3.430	3.500	3.570	V
	AP1115-5.0	$I_{OUT} = 10 \text{mA}, T_A = +25 ^{\circ}\text{C}, 6.5 \text{V} \le V_{IN} \le 12 \text{V}$	4.900	5.000	5.100	V
Line Regulation	AP1115-XXX	$I_0 = 10$ mA, $V_{OUT} + 1.5$ V $< V_{IN} < 15$ V, $T_A = +25$ °C	_	_	0.2	%
	AP1115-ADJ	$V_{IN} = 3.3V$, $V_{ADJ} = 0.0$ mA < lo < 0.6A, $T_A = +25$ °C (Notes 4 & 5)	_	_	1	%
	AP1115-1.5	$V_{IN} = 3V$, 0mA < Io < 0.6A, $T_A = +25$ °C (Notes 4 & 5)	_	12	15	mV
	AP1115-1.8	V _{IN} = 3.3V, 0mA < lo < 0.6A, T _A = +25°C (Notes 4 & 5)	_	15	18	mV
	AP1115-2.5	V _{IN} = 4V, 0mA < Io < 0.6A, T _A = +25°C (Notes 4 & 5)	_	20	25	mV
Load Regulation	AP1115-2.8	V _{IN} = 4.3V, 0mA < Io < 0.6A, T _A = +25°C (Notes 4 & 5)	_	22	28	mV
	AP1115-3.0	V_{IN} = 5V, 0 \leq I _{OUT} \leq 0.6A, T _A = +25°C (Notes 4 & 5)	_	23	30	mV
	AP1115-3.3	V_{IN} = 5V, 0 \leq I _{OUT} \leq 0.6A, T _A = +25°C (Notes 4 & 5)	_	26	33	mV
	AP1115-3.5	$V_{IN} = 5V$, $0 \le I_{OU}T \le 0.6A$, $T_A = +25$ °C (Notes 4 & 5)	_	28	35	mV
	AP1115-5.0	$V_{IN} = 8V$, $0 \le I_{OU}T \le 0.6A$, $T_A = +25^{\circ}C$ (Notes 4 & 5)	_	40	50	mV

Notes:
4. See thermal regulation specifications for changes in output voltage due to heating effects. Line and load regulation are measured at a constant junction temperature by low duty cycle pulse testing. Load regulation is measured at the output lead = 1/18" from the package.

^{5.} Line and load regulation are guaranteed up to the maximum power dissipation of 5W. Power dissipation is determined by the input/output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.

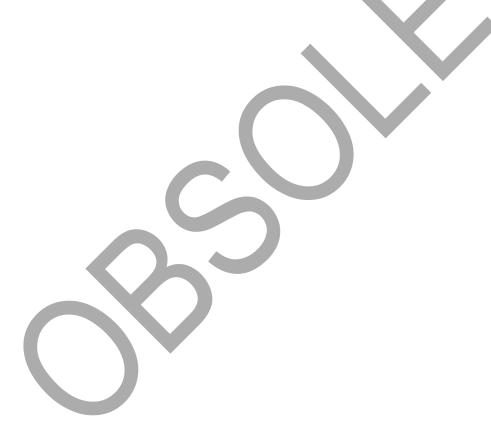


Electrical Characteristics (cont.) (@T_A = +25°C, unless otherwise specified.)

Parameter	Conditions		Min	Тур	Max	Unit
Dropout Voltage (V _{IN} -V _{ΟUΤ})	AP1115-ADJ/1.5/1.8 2.5/2.8/3.0/3.3/3.5/5.0	$I_{OUT} = 0.6A$, $\Delta V_{OUT} = 1\%V_{OUT}$	_	1.1	1.3	V
Current Limit	AP1115-ADJ/1.5/1.8 2.5/2.8/3.0/3.3/3.5/5.0	$(V_{IN}-V_{OUT})=5V$	0.7	_	_	А
Minimum Load Current (Note 6)	AP1115-XXX	$0^{\circ}C \le T_{J} \le +125^{\circ}C$	_	5	10	mA
Thermal Regulation	T _A = +25°C, 30ms pulse		_	0.008	0.04	%/W
Disale Delegation	f = 120Hz,C _{OUT} = 25μF Tantalum, I _{OUT} = 0.6A					
Ripple Rejection	AP1115-XXX	$V_{IN} = V_{OUT} + 3V$	_	60	70	dB
Temperature Stability	I _O = 10mA		-	0.6	7-	%
θ_{JA}	Thermal Resistance Junction-to-Ambient (No heat sink; No air flow) (Note 7)			164	_	°C/W
θ_{JC}	Thermal Resistance Junction-to-Case Control Circuitry/Power Transistor (Note 7)			35	-	°C/W

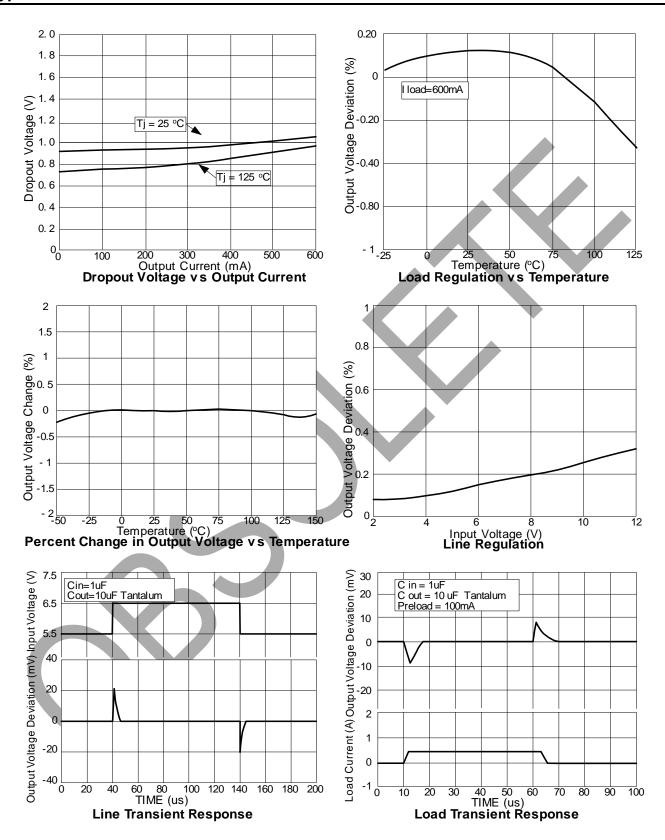
Notes:

- 6. Quiescent current is defined as the minimum output current required to maintain regulation. At 12V input/output differential the device is guaranteed to regulate if the output current is greater than 10mA.7. Test conditions for SOT89-3L: Device mounted on FR-4 substrate, 2oz copper, with minimum recommended pad layout.





Typical Performance Characteristics





Ordering Information

AP1115 X Y XX X - 13X Pin Version Package Vout Lead Free Packing L: Lead Free A: Pin1: Vout Y: SOT89-3 Blank: ADJ 13: Tape & Reel G: Green 15:1.5V 2: Adj(GND) 13R: Tape & Reel 18:1.8V 3 : VIN 25: 2.5V B: Pin1: Adj(GND) 28: 2.8V 2 : VIN 3 : Vout 30:3.0V 33:3.3V 35:3.5V 50:5.0V

	Device	vice Package Code		13" Tape and Reel (Note 9)		
	Device	1 ackage code	(Note 8)	Quantity	Part Number Suffix	
Pub Lead-Free	AP1115XYXXL-13	Υ	SOT89-3	2500/Tape & Reel	-13	
Lead free Green	AP1115XYXXG-13	Υ	SOT89-3	2500/Tape & Reel	-13	
Pub Lead-Free	AP1115XYXXL-13R	Υ	SOT89-3	4000/Tape & Reel	-13R	
Lead free Green	AP1115XYXXG-13R	Υ	SOT89-3	4000/Tape & Reel	-13R	

Notes:

- 8. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 9. Find Surface Mount (SMD) Packaging and Reel and Carrier Tape specification in document AP02007.pdf http://www.diodes.com/datasheets/ap02007.pdf

Marking Information

SOT89-3

(Top View)



XX: Identification code

<u>Y</u>: Year: 0~9

<u>W</u>: Week: A~Z: 1~26 week; a~z: 27~52 week; z represents 52 and 53 week X: Internal code a~z: Lead Free

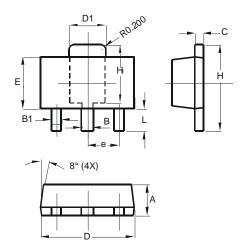
A~Z: Green

Output Version	Identification Code		
Output version	AP1115A	AP1115B	
ADJ	JO	JU	
1.5V	JP	JV	
1.8V	JQ	JW	
2.5V	JR	JX	
2.8V	JC	JD	
3.0V	JM	JN	
3.3V	JS	JY	
3.5V	JK	JL	
5.0V	JT	JZ	



Package Outline Dimensions (All dimensions in mm.)

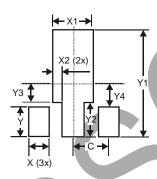
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT89-3			
Dim	Min	Max		
Α	1.40	1.60		
В	0.44	0.62		
B1	0.35	0.54		
С	0.35	0.44		
D	4.40	4.60		
D1	1.62	1.83		
Е	2.29	2.60		
е	1.50	Тур		
Н	3.94	4.25		
H1	2.63	2.93		
L	0.89	1.20		
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	value (in mm)
Х	0.900
X1	1.733
X2	0.416
Υ	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
С	1.500

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