A Product Line of
Diodes Incorporated

SPECIFICATION FOR APPROVAL

CUSTOMER	
NOMINAL FREQUENCY	312.500000 MHz
PRODUCT TYPE	TYPE NX 5.0x3.2 SEAM SEALED CRYSTAL CLOCK OSCILLATOR
SPEC. NO. (P/N)	NX53V25001
CUSTOMER P/N	
ISSUE DATE	June 6, 2018
VERSION	B

APPROVED	PREPARED	QA
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*Pb-free
*RoHS Compliant
*HF-Halogen Free
*REACH Compliant

VERSION HISTORY

Version No.	Version Date	Description	Notes
A	Oct.7,2016	Initial Release	
B	Jun.6,2018	Updated Logo	

TYPE NX 5.0x3.2 SEAM SEALED CRYSTAL CLOCK OSCILLATOR

NX53V25001

VER. B 6-Jun-18

ELECTRICAL SPECIFICATIONS

SRe Part Number : NX53V25001

Item	Symbol	Specifications	Units	Notes
Nominal Frequency	Fo	312.500000	MHz	
Frequency Stability	FT	± 50	ppm	**See note
Operating Temperature Range	TR	-40 to +85	℃	
Supply Voltage	V_{CC}	+3.3 ± 5.0%	V	
Logic Type	LT	LVDS		
Supply Current, Output Enabled	I_{CC}/OE	70	mA	Max.
Supply Current, Output Disabled	I_{CC}/OD	40	mA	Max.
Duty Cycle (Symmetry)	DC/SY	45 / 55	%	Measured 50% of Waveform
Rise / Fall Time	T_R/T_F	400	ps	Max. measured 20/80% of Waveform
Output Voltage "0" Level	V_{OL}	1.10 / 0.9	V	Typ. / Min.
Output Voltage "1" Level	V_{OH}	1.43 / 1.6	V	Typ. / Max.
Output Load		100Ω connected between outputs		Output requires termination
Differential Output Voltage	V_{OD}	247 / 454	mV	Min. / Max.
Jitter, Phase	RMS	0.5	ps	Max. 12KHz ~ 20MHz Frequency Band
Jitter, Accumulated	RMS(1-σ)	6	ps	Max. 20,000 Consecutive Periods
Jitter, Peak to Peak	Pk-Pk	40	ps	Max. 100,000 Random Periods
Storage Temperature Range		-55 to +125	℃	

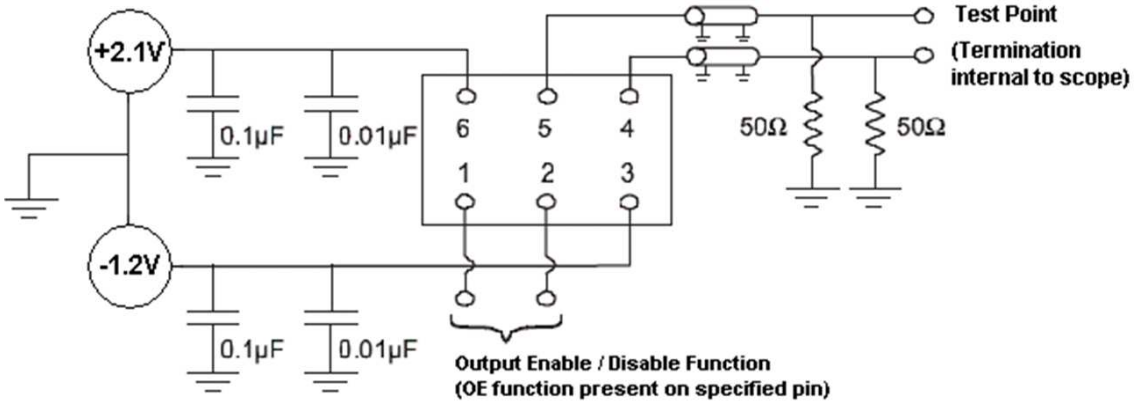
※ This product doesn't include harmful substance that stipulated by SONY SS-00259 Level 1 and S-AT2-001 Level 1 standard. RoHS Compliant (Pb - Free).

****Stability includes all combinations of Operating Temperature, Load changes, rated Input (Supply) Voltage changes, Initial Calibration Tolerance (25℃), Aging (1 year at 25℃ Average Effective Ambient Temperature), Shock and Vibration.**

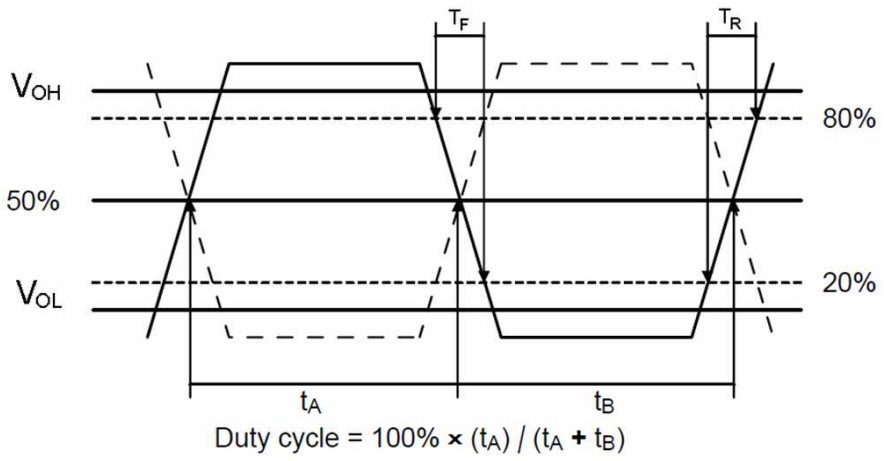
Output Enable / Disable Function

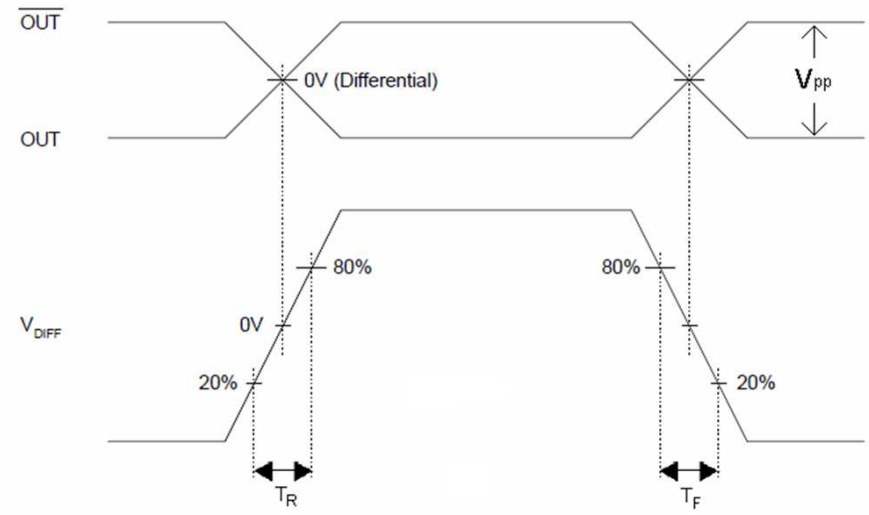
Parameter	Min.	Typ.	Max.	Units	Notes
Input Voltage (Pin1), Output Enable	$0.7V_{CC}$			V	Or Open
Input Voltage (Pin1), Output Disable (low power standby)			$0.3V_{CC}$	V	Output is Hi-Z
Output Disable Delay			100	ns	
Output Enable Delay			100	ns	
Start Up Time			10	ms	

TEST CIRCUIT



OUTPUT WAVEFORM





RELIABILITY SPECIFICATIONS

ENVIRONMENTAL:

a) THERMAL SHOCK: MIL-STD-883, Method 1011, Condition A

b) MOISTURE RESISTANCE: MIL-STD-883, Method 1004

c) VIBRATION: MIL-STD-883, Method 2007, Condition A

d) RESISTANCE TO SOLDERING HEAT: J-STD-020D Table 5-2 Pb-free devices (except 2 cycles max)

e) HAZARDOUS SUBSTANCE: Pb - free and RoHS/ Green Compliant.

MECHANICAL:

a) SHOCK: MIL-STD-883, Method 2002, Condition B

b) SOLDERABILITY: JESD22-B102-D Method 2 (Preconditioning E)

c) TERMINAL STRENGTH: MIL-STD-883, Method 2004, Test Condition D

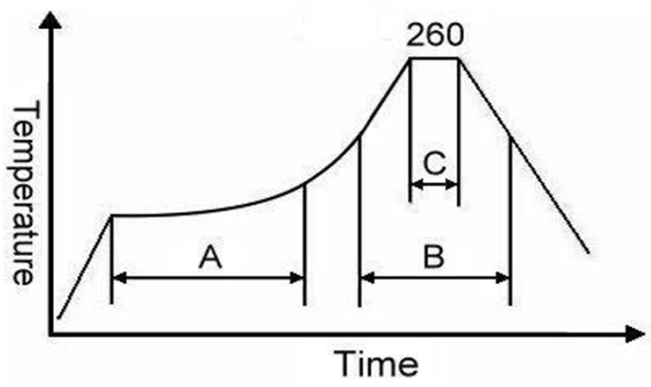
d) GROSS LEAK: MIL-STD-883, Method 1014, Condition C

e) FINE LEAK: MIL-STD-883, Method 1014, Condition A2, R1=2x10^{-8} atm cc/s

f) SOLVENT RESISTANCE: MIL-STD-202, Method 215

SUGGESTED IR REFLOW PROFILE

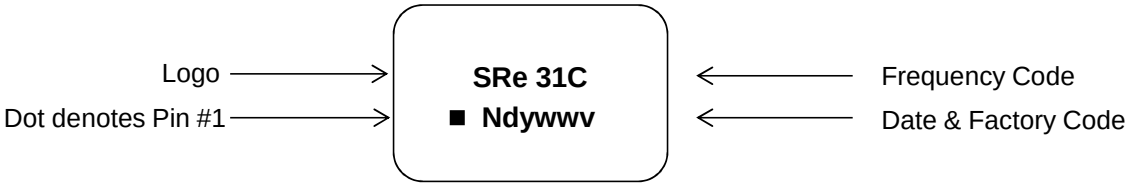
*As per IPC-JEDEC J-STD-020D

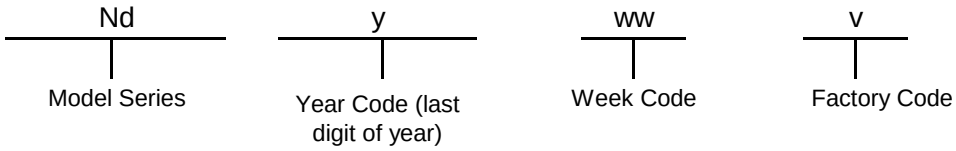


Note:

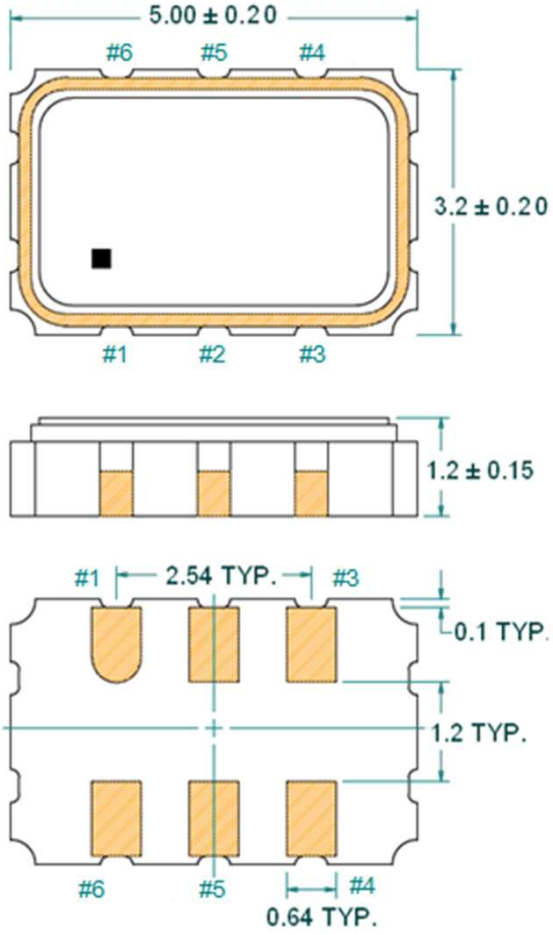
	Stage	Temperature	Time
A	Preheat	150~200°C	60~120 Sec
B	Primary Heat	217°C	60~150 Sec
C	Peak	260°C	10 Sec

MARKING



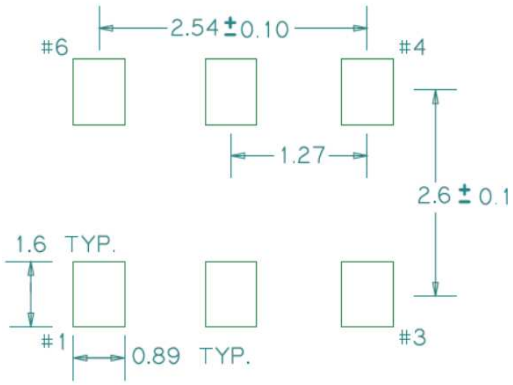


MECHANICAL DRAWINGS (Scale: None. Dimensions are in mm.)



Bottom View

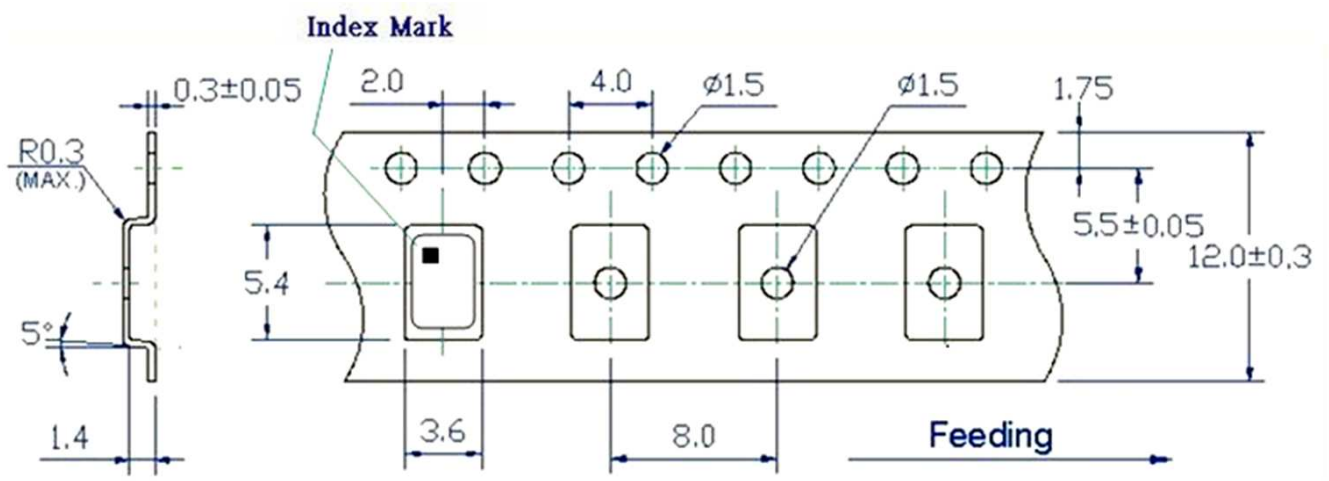
Recommended Land Pattern*

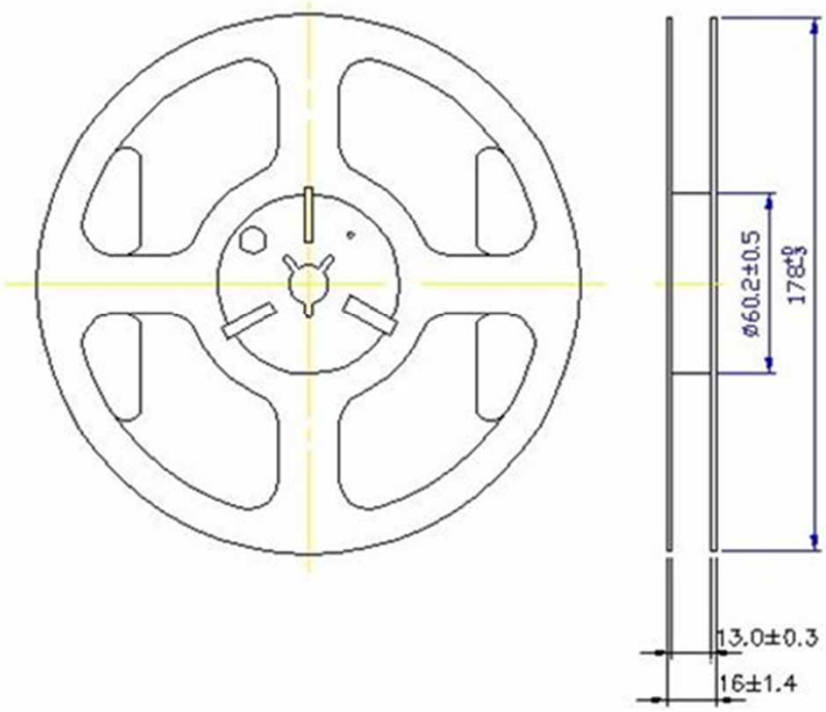


*External high-frequency power decoupling is recommended.(see test circuit for minimum recommendation). To ensure optimal performance, do not route traces beneath the package.

Pin	Function
1	OE
2	NC
3	Ground
4	Q
5	$\overline{Q}$
6	V_{CC}

TAPE & REEL





1. 230mm minimum leafer which consist of carrier and/or tape followed by a minimum of 160mm of empty carrier tape sealed with cover tape.

2. 160mm minimum trailer of empty carrier tape sealed with cover tape.

PACKING