



3:1 Mux/DeMux Bus Switch

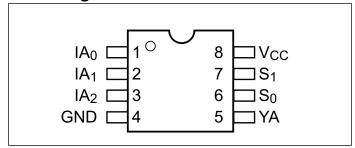
Features

- → Near-Zero propagation delay
- → 5-ohm switches connect inputs to outputs
- → Direct bus connection when switches are ON
- → Ultra Low Quiescent Power (0.2µA typical) Ideally suited for notebook applications
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- → For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

- → Packaging (Pb-free & Green available):
 - 8 pin 118-mil wide plastic MSOP (U)

Pin Configuration



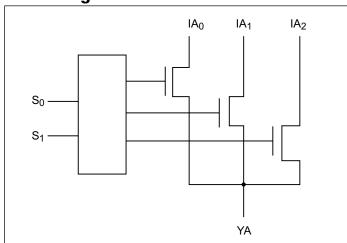
Pin Description

Pin Name	Description
IAn	Data Inputs
S0-1	Select Inputs
YA	Data Output
GND	Ground
V_{CC}	Power

Description

Diodes' PI5C3309 is a 3:1 Multiplexer/Demultiplexer with 3-State outputs. Inputs can be connected to outputs with low On-Resistance (5-ohm) with no additional ground bounce noise or propagation delay.

Block Diagram



Truth Table (1)

Select			
S1	S0	YA	Function
L	L	IA0	S1-0 = 0
L	Н	IA1	S1-0 = 1
Н	L	IA2	S1-0=2
Н	Н	Hi-Z	S1-0=3

Note:

- 1. H = High Voltage Level
 - L = Low Voltage Level

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature—65°C to +150°C
Ambient Temperature with Power Applied –40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V_{CC} only) $-0.5V$ to $+7.0V$
Supply Voltage to Ground Potential (Outputs & D/O only) -0.5V to +7.0V
DC Input Voltage0.5V to +7.0V
DC Output Current
Power Dissipation

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 5V \pm 5\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed logic HIGH level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed logic LOW level	-0.5		0.8	v
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
I_{IL}	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I _{OZH}	High Impedance Output Current	$0 \le I_N, Y_N \le V_{CC}$			±1	
V_{IK}	Clamp Diode Voltage	$V_{CC} = Min, I_{IN} = -18mA$	-0.7		1.2	V
I _{OS}	Short Circuit current	I(Y)=0V, Y(I)=V _{CC}			100	mA
V_{H}	Input Hysteresis at Control Pins			150		mV
D	Switch on Resistance (4)	$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA$		5	7	Ω
R _{ON}	Switch on Resistance (7	$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		10	15	52

Notes:

- 1. For Max, or Min, conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 5.0V$, $T_A = 25^{\circ}C$ ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I, Y) pins.

Capacitance $(T_A = 25^{\circ}C, f = 1 \text{ MHz})$

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Units
C_{IN}	Input Capacitance		3	
C _{OFF}	IA Capacitance, Switch Off	$V_{IN} = 0V$	4	pF
C _{ON}	I _A Capacitance, Switch On		16	

Notes:

1. This parameter is determined by device characterization but is not production tested.





Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	V Mov	$V_{IN} = GND \text{ or } V_{CC}$		0.1	3.0	μΑ
ΔI_{CC}	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.$	$V_{IN} = 3.4V^{(3)}$			2.5	mA
I_{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., I and Y pins open Control Input Tog- gling 50% Duty Cycle				0.25	mA/ MHz

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 5.0V$, +25°C ambient.
- 3. Per TTL driven input ($V_{IN} = 3.4V$, control inputs only); I and Y pins do not contribute to I_{CC}.
- 4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. The parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

	Description		PI5C3309		Units
Parameters		Conditions	Com		
			Min.	Max.	Units
t_{IY}	Propogation Delay ^(1,2) I _N to Y _N			0.25	
t_{SY}	Bus Select Time S _N to Y _N		0.5	6.6	
t _{PZH} t _{PZL}	Bus Enable Time S _N to Y _N	$C_{L} = 50 \text{pF}$ $R_{L} = 500 \Omega$	0.5	6.0	ns
t _{PHZ} t _{PLZ}	Bus Disable Time S _N to Y _N	NL JOUSE	0.5	6.0	

Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Part Marking

U Package



Y: Date Code (Year)

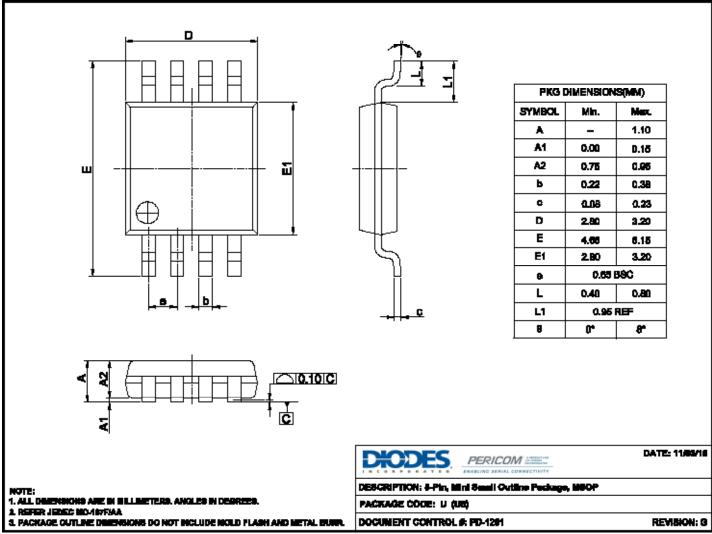
W: Date Code (Workweek)
1st X: Assembly Site Code
2nd X: Wafer Fab Site Code

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Packaging Mechanical: 8-MSOP(U)



16-**CX**

For latest package info.

 $please\ check:\ http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/packaging-mechanical-and-thermal-characteristics/packaging-mech$

Ordering Information

Ordering Code	Packaging Code	Package Description
PI5C3309UEX	U	8-pin, Mini Small Outline Package (MSOP)

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- $2. \ See \ https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.$
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





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