

SIGC06T120CS

IGBT Chip in NPT-technology

FEATURES:

- 1200V NPT technology
- 180µm chip
- short circuit prove
- positive temperature coefficient
- easy paralleling

This chip is used for:

SGP02N120

Applications:

drives, SMPS, resonant applications

Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code	
SIGC06T120CS	1200V	2A	2.45 x 2.25 mm ²	sawn on foil	Q67050-A4115-	
0.00001.12000	1200 0 27	2/1	2.43 X 2.23 IIIII	3awii on ioii	A001	

MECHANICAL PARAMETER:

Raster size	2.45 x 2.25				
Area total / active	5.512 / 2.5				
Emitter pad size	1.03x1.29				
Gate pad size	0.42x0.56				
Thickness	180	μm			
Wafer size	150	mm			
Flat position	0	deg			
Max.possible chips per wafer	2794 pcs				
Passivation frontside	Photoimide				
Emitter metalization	3200 nm Al Si 1%				
Collector metalization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500μm				
Reject Ink Dot Size	Ø 0.65mm; max 1.2mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



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MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T _j =25 °C	V _{CE}	1200	V
DC collector current, limited by T _{jmax}	I _C	1)	А
Pulsed collector current, t _p limited by T _{jmax}	I _{cpuls}	6	Α
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	T_j , T_{stg}	-55 + 150	°C

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_i =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
Taranicter		Conditions	min.	typ.	max.]
Collector-emitter breakdown voltage	V _{(BR)CES}	V_{GE} =0 V , I_{C} =300 μ A	1200			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =2A	2.5	3.1	3.6	V
Gate-emitter threshold voltage	V _{GE(th)}	$I_C=90\mu A$, $V_{GE}=V_{CE}$	3.0	4.0	5.0	
Zero gate voltage collector current	I _{CES}	V _{CE} =1200V , V _{GE} =0V			0.2	μA
Gate-emitter leakage current	I _{GES}	V _{CE} =0V , V _{GE} =20V			120	nA

DYNAMIC CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
raiametei			min.	typ.	max.]
Input capacitance	Ciss	V _{CE} =25V,	-	205	250	pF
Output capacitance	Coss	$V_{GE}=0V$,	-	28	34	
Reverse transfer capacitance	Crss	f=1MHz	-	17	21	

SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

Parameter	Symbol	Conditions*	Value			Unit
			min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	$T_{\rm j}$ =25 ° C $V_{\rm CC}$ =800 V, $I_{\rm C}$ =2 A	1	23	30	ns
Rise time	t_{r}	$I_{\rm C}$ =2A	ı	16	21	
Turn-off delay time	$t_{d(off)}$	V_{GE} =+15/0V, R_{G} =91 Ω	1	260	340	
Fall time	t_{f}	7.6-0.12	ı	61	80	

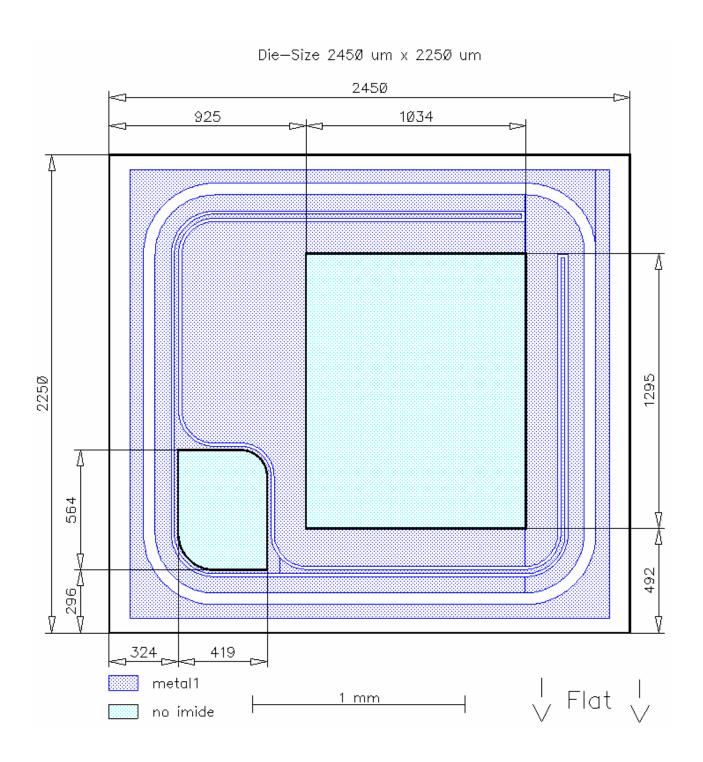
^{*} switching conditions different to LowLoss, Standard, IGBT3; under comparable switching conditions 40% faster than Standard. Values also influenced by parasitic L- and C- in measurement and package.

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CHIP DRAWING:





SIGC06T120CS

FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet SGP02N120 Package : TO220

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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