

Low Loss DuoPack : IGBT in TRENCHSTOP™ and Fieldstop technology with soft, fast recovery anti-parallel Emitter Controlled HE diode









Features:

- Very low V_{CE(sat)} 1.5V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time 5μs
- Designed for :
 - Frequency Converters
 - Uninterruptible Power Supply
- TRENCHSTOP™ and Fieldstop technology for 600V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behavior
 - very high switching speed
 - low V_{CE(sat)}
- Positive temperature coefficient in V_{CE(sat)}
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel Emitter Controlled HE diode
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : http://www.infineon.com/igbt/

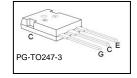
Туре	V _{CE}	<i>I</i> _C	V _{CE(sat),Tj=25°C}	$T_{j,max}$	Marking	Package
IKW30N60T	600V	30A	1.5V	175°C	K30T60	PG-TO247-3

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, <i>T</i> _j ≥ 25°C	V _{CE}	600	V
DC collector current, limited by T_{jmax}			
$T_{\rm C}$ = 25°C, value limited by bondwire	Ic	45	
$T_{\rm C} = 100^{\circ}{\rm C}$		39	
Pulsed collector current, t_p limited by T_{jmax}	I _{Cpuls}	90	
Turn off safe operating area, $V_{CE} = 600\text{V}$, $T_j = 175^{\circ}\text{C}$, $t_p = 1 \mu\text{s}$	-	90	A
Diode forward current, limited by $T_{\rm jmax}$			
$T_{\rm C}$ = 25°C, value limited by bondwire	I _F	45	
$T_{\rm C} = 100^{\circ}{\rm C}$		39	
Diode pulsed current, t_p limited by T_{jmax}	I _{Fpuls}	90	
Gate-emitter voltage	V_{GE}	±20	V
Short circuit withstand time ²⁾	,		
$V_{\text{GE}} = 15\text{V}, \ V_{\text{CC}} \le 400\text{V}, \ T_{j} \le 150^{\circ}\text{C}$	tsc	5	μS
Power dissipation $T_C = 25^{\circ}C$	P _{tot}	187	W
Operating junction temperature	T _j	-40+175	
Storage temperature	T _{stg}	-55+150	°C
Soldering temperature, 1.6mm (0.063 in.) from case for 10s	-	260	

¹ J-STD-020 and JESD-022





²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.



Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic		-		1
IGBT thermal resistance,	R _{thJC}		0.80	K/W
junction – case				
Diode thermal resistance,	R _{thJCD}		1.05	
junction – case				
Thermal resistance,	R_{thJA}		40	
junction – ambient				

Electrical Characteristic, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Conditions		Value		Unit
raiailletei	Symbol	Conditions	min.	typ.	max.	Ollit
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{\rm GE} = 0 \text{V}, I_{\rm C} = 0.2 \text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{\rm GE} = 15 \rm V, \ I_{\rm C} = 30 \rm A$				
		<i>T</i> _j =25°C	-	1.5	2.05	
		<i>T</i> _j =175°C	-	1.9	-	
Diode forward voltage	V_{F}	$V_{GE} = 0V, I_{F} = 30A$				
		<i>T</i> _j =25°C	-	1.65	2.05	
		<i>T</i> _j =175°C	-	1.6	-	
Gate-emitter threshold voltage	V _{GE(th)}	$I_{C}=0.43$ mA, $V_{CE}=V_{GE}$	4.1	4.9	5.7	
Zero gate voltage collector current	I _{CES}	V _{CE} =600V, V _{GE} =0V				μΑ
		<i>T</i> _j =25°C	-	-	40	
		<i>T</i> _j =175°C	-	-	2000	
Gate-emitter leakage current	I _{GES}	$V_{\text{CE}}=0\text{V}, V_{\text{GE}}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{\rm CE} = 20 \rm V, \ I_{\rm C} = 30 \rm A$	-	16.7	-	S
Integrated gate resistor	R _{Gint}			-		Ω

Dynamic Characteristic

Input capacitance	Ciss	V _{CE} =25V,	-	1630	-	pF
Output capacitance	Coss	$V_{GE}=0V$,	-	108	-	
Reverse transfer capacitance	Crss	f=1MHz	-	50	-	
Gate charge	Q _{Gate}	$V_{\rm CC} = 480 \text{V}, I_{\rm C} = 30 \text{A}$	-	167	-	nC
		V _{GE} =15V				
Internal emitter inductance	LE		-	13	-	nΗ
measured 5mm (0.197 in.) from case						
Short circuit collector current ¹⁾	$I_{C(SC)}$	$V_{\text{GE}} = 15 \text{V}, t_{\text{SC}} \le 5 \mu \text{s}$ $V_{\text{CC}} = 400 \text{V},$ $T_{\text{j}} = 150 ^{\circ} \text{C}$	-	275	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.





Switching Characteristic, Inductive Load, at T_j =25 °C

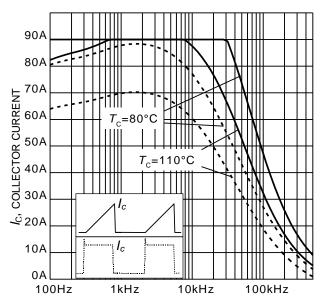
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Parameter	Symbol	Conditions	min.	Тур.	max.	Unit
IGBT Characteristic	•					
Turn-on delay time	$t_{d(on)}$	T _j =25°C,	-	23	-	ns
Rise time	$t_{\rm r}$	$V_{CC}=400V, I_{C}=30A, V_{GE}=0/15V,$	-	21	-	1
Turn-off delay time	$t_{d(off)}$	$r_{\rm G}$ =10.6 Ω ,	-	254	-	
Fall time	t_{f}	L_{σ} =136nH, C_{σ} =39pF L_{σ} , C_{σ} from Fig. E	-	46	-	
Turn-on energy	Eon	Energy losses include	-	0.69	-	mJ
Turn-off energy	E _{off}	"tail" and diode reverse recovery.	-	0.77	-	
Total switching energy	E _{ts}	100010.,.	-	1.46	-	
Anti-Parallel Diode Characteristic	1	-				<u> </u>
Diode reverse recovery time	t_{rr}	T _i =25°C,	-	143	-	ns
Diode reverse recovery charge	Q_{rr}	V_{R} =400V, I_{F} =30A,	-	0.92	-	μC
Diode peak reverse recovery current	I _{rrm}	$di_F/dt=910A/\mu s$	-	16.3	-	Α
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di _{rr} /dt		-	603	-	A/μs

Switching Characteristic, Inductive Load, at T_i =175 °C

Develope	Symbol Conditions		Value			I I m i t
Parameter	Symbol	Conditions	min.	Тур.	max.	Unit
IGBT Characteristic	•					
Turn-on delay time	$t_{d(on)}$	$T_{\rm j} = 175^{\circ} \text{C},$	-	24	-	ns
Rise time	t_{r}	$V_{CC} = 400 \text{V}, I_{C} = 30 \text{A}, V_{GE} = 0/15 \text{V},$	-	26	-	
Turn-off delay time	$t_{d(off)}$	$r_{\rm G}$ =10.6 Ω ,	-	292	-	
Fall time	t_{f}	L_{σ} =136nH, C_{σ} =39pF L_{σ} , C_{σ} from Fig. E	-	90	-	
Turn-on energy	Eon	Energy losses include	-	1.0	-	mJ
Turn-off energy	E_{off}	"tail" and diode reverse recovery.	-	1.1	-	
Total switching energy	E _{ts}	100010.7.	-	2.1	-	
Anti-Parallel Diode Characteristic	1					
Diode reverse recovery time	t_{rr}	<i>T</i> _j =175°C	-	225	-	ns
Diode reverse recovery charge	Q _{rr}	V_{R} =400V, I_{F} =30A,	-	2.39	-	μC
Diode peak reverse recovery current	I _{rrm}	$di_{\rm F}/dt$ =910A/ μ s	-	22.3	-	Α
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di _{rr} /dt		-	310	-	A/μs

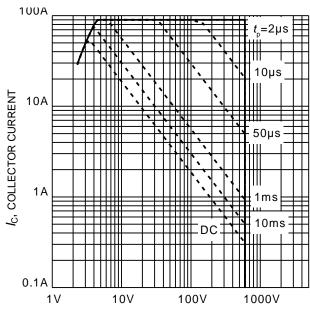






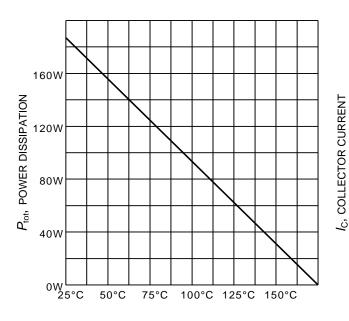
f, SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency $(T_j \le 175^{\circ}\text{C}, D = 0.5, V_{\text{CE}} = 400\text{V}, V_{\text{GE}} = 0/15\text{V}, r_{\text{G}} = 10\Omega)$



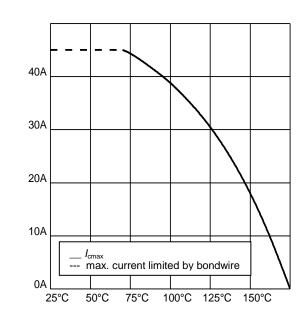
 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area $(D=0, T_C=25^{\circ}\text{C}, T_j \leq 175^{\circ}\text{C}; V_{GE}=0/15\text{V})$



 $T_{\rm C}$, CASE TEMPERATURE Figure 3. Power dissipation as a function of case temperature

 $(T_{j} \le 175^{\circ}C)$



 $T_{\rm C}$, CASE TEMPERATURE

Figure 4. Collector current as a function of case temperature

 $(V_{GE} \ge 15V, T_j \le 175^{\circ}C)$





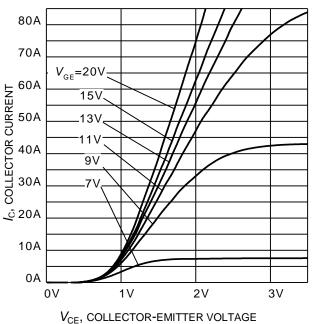


Figure 5. Typical output characteristic $(T_i = 25^{\circ}C)$

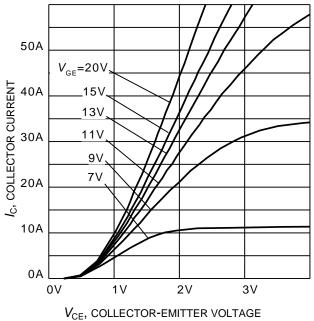
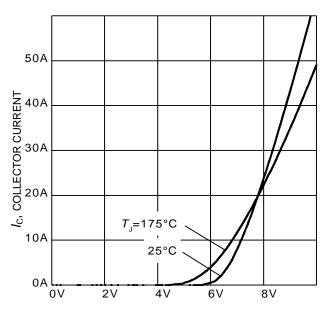
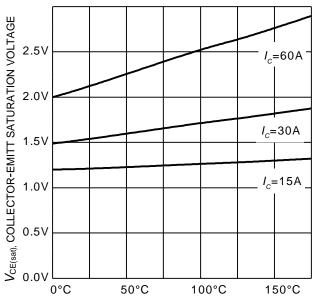


Figure 6. Typical output characteristic $(T_i = 175^{\circ}C)$



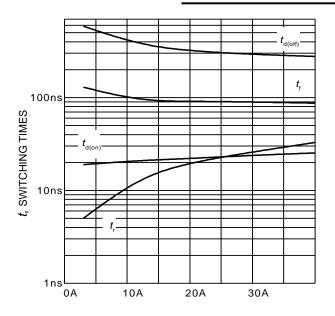
 $V_{\text{GE}}, \, \text{GATE-EMITTER VOLTAGE} \\ \textbf{Figure 7.} \quad \textbf{Typical transfer characteristic} \\ (V_{\text{CE}} = 10 \text{V}) \\ \end{cases}$

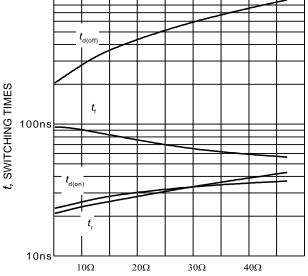


 $T_{\rm J}$, JUNCTION TEMPERATURE Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature ($V_{\rm GE}=15\rm V$)







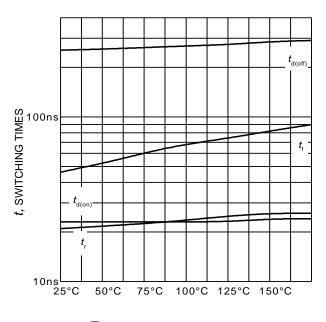


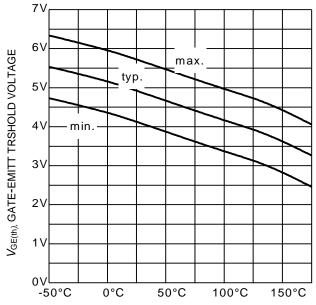
 I_{C} , COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current (inductive load, T_J =175°C, V_{CE} = 400V, V_{GE} = 0/15V, r_G = 10 Ω , Dynamic test circuit in Figure E)

 $R_{\rm G}$, gate resistor

Figure 10. Typical switching times as a function of gate resistor (inductive load, $T_J = 175$ °C, $V_{CE} = 400$ V, $V_{GE} = 0/15$ V, $I_C = 30$ A, Dynamic test circuit in Figure E)





 $T_{\rm J}$, JUNCTION TEMPERATURE

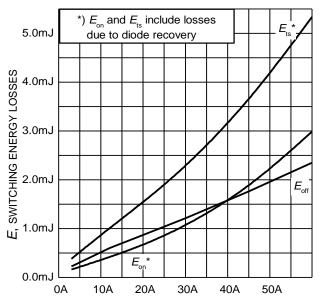
Figure 11. Typical switching times as a function of junction temperature (inductive load, V_{CE} = 400V, V_{GE} = 0/15V, I_{C} = 30A, r_{G} =10 Ω , Dynamic test circuit in Figure E)

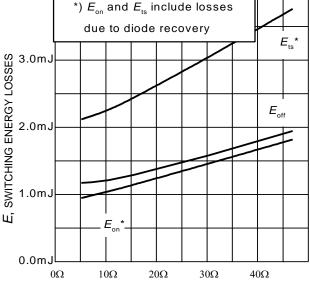
 $T_{\rm J}$, JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature $(I_C = 0.43 \text{mA})$









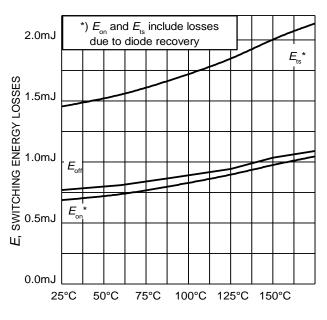
 $I_{\rm C}$, COLLECTOR CURRENT

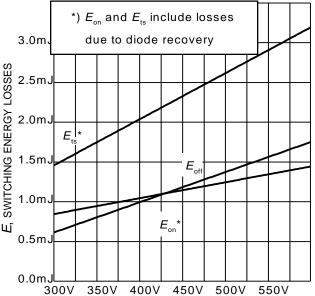
Dynamic test circuit in Figure E)

Figure 13. Typical switching energy losses as a function of collector current (inductive load, T_J = 175°C, V_{CE} = 400V, V_{GE} = 0/15V, r_G = 10 Ω ,

 $R_{\rm G}$, gate resistor

Figure 14. Typical switching energy losses as a function of gate resistor (inductive load, $T_J = 175$ °C, $V_{CE} = 400$ V, $V_{GE} = 0/15$ V, $I_C = 30$ A, Dynamic test circuit in Figure E)





 $T_{
m J}$, JUNCTION TEMPERATURE

Figure 15. Typical switching energy losses as a function of junction temperature

(inductive load, $V_{\rm CE}$ = 400V, $V_{\rm GE}$ = 0/15V, $I_{\rm C}$ = 30A, $r_{\rm G}$ = 10 Ω , Dynamic test circuit in Figure E)

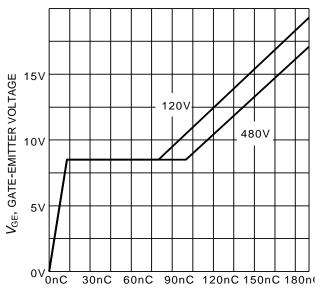
 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 16. Typical switching energy losses as a function of collector emitter voltage

(inductive load, T_J = 175°C, V_{GE} = 0/15V, I_C = 30A, r_G = 10 Ω , Dynamic test circuit in Figure E)





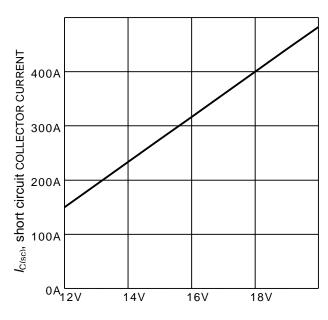


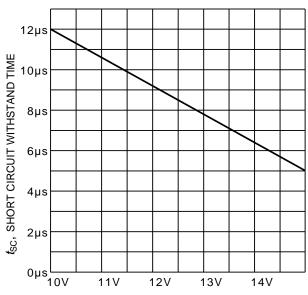
 Q_{GE} , GATE CHARGE

Figure 17. Typical gate charge $(I_c=30 \text{ A})$

 $V_{\rm CE}$, COLLECTOR-EMITTER VOLTAGE

Figure 18. Typical capacitance as a function of collector-emitter voltage $(V_{GE}=0V, f=1 \text{ MHz})$





 $V_{\rm GE}$, gate-emittetr voltage

Figure 19. Typical short circuit collector current as a function of gate-emitter voltage $(V_{CE} \le 400 \text{V}, T_i \le 150 ^{\circ}\text{C})$

 $V_{\rm GE}$, gate-emitetr voltage

Figure 20. Short circuit withstand time as a function of gate-emitter voltage (V_{CE} =400V, start at T_{J} =25°C, T_{Jmax} <150°C)





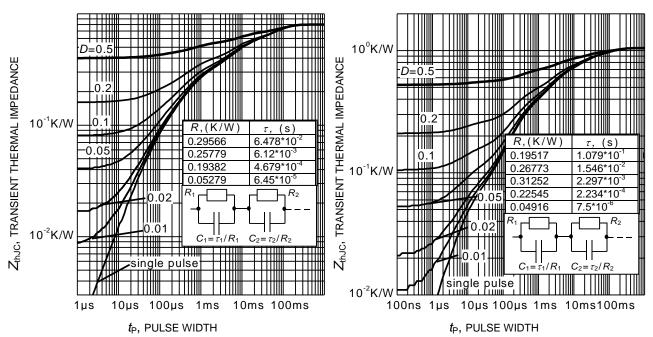


Figure 21. IGBT transient thermal impedance $(D = t_p / T)$

Figure 22. Diode transient thermal impedance as a function of pulse width $(D=t_P/T)$

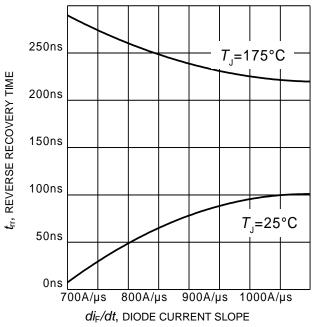
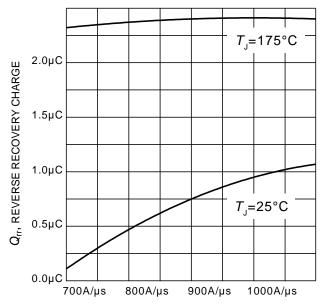


Figure 23. Typical reverse recovery time as a function of diode current slope (V_R =400V, I_F =30A, Dynamic test circuit in Figure E)



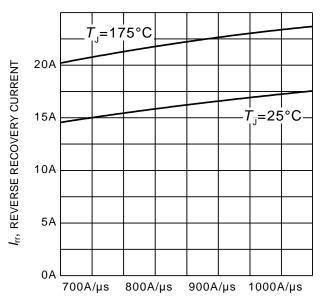
di_F/dt, DIODE CURRENT SLOPE

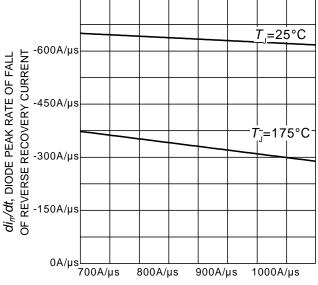
Figure 24. Typical reverse recovery charge as a function of diode current slope

 $(V_R = 400V, I_F = 30A,$ Dynamic test circuit in Figure E)







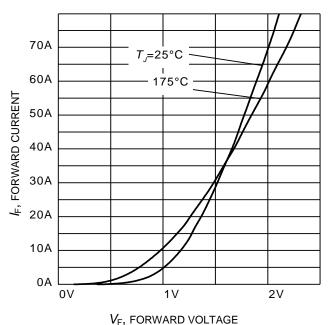


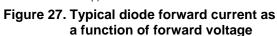
di_F/dt, DIODE CURRENT SLOPE

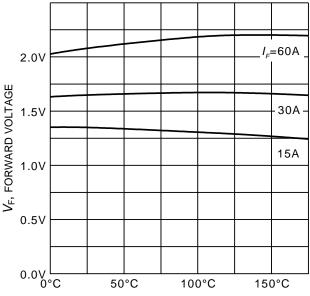
Figure 25. Typical reverse recovery current as a function of diode current slope

 $(V_R = 400V, I_F = 30A,$ Dynamic test circuit in Figure E) di_F/dt, DIODE CURRENT SLOPE

Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope (V_R=400V, I_F=30A, Dynamic test circuit in Figure E)



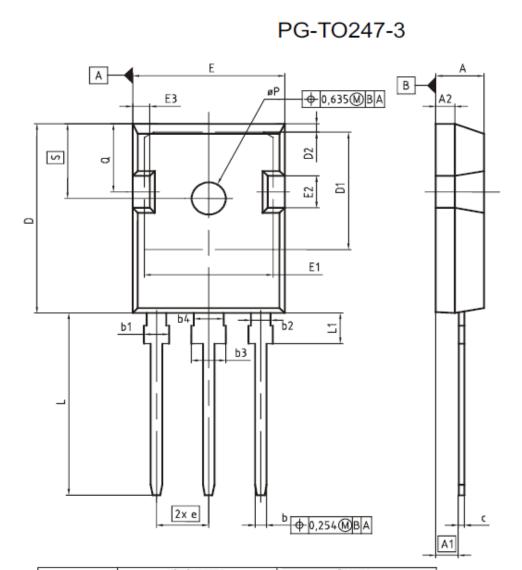




 $T_{\rm J}$, JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature



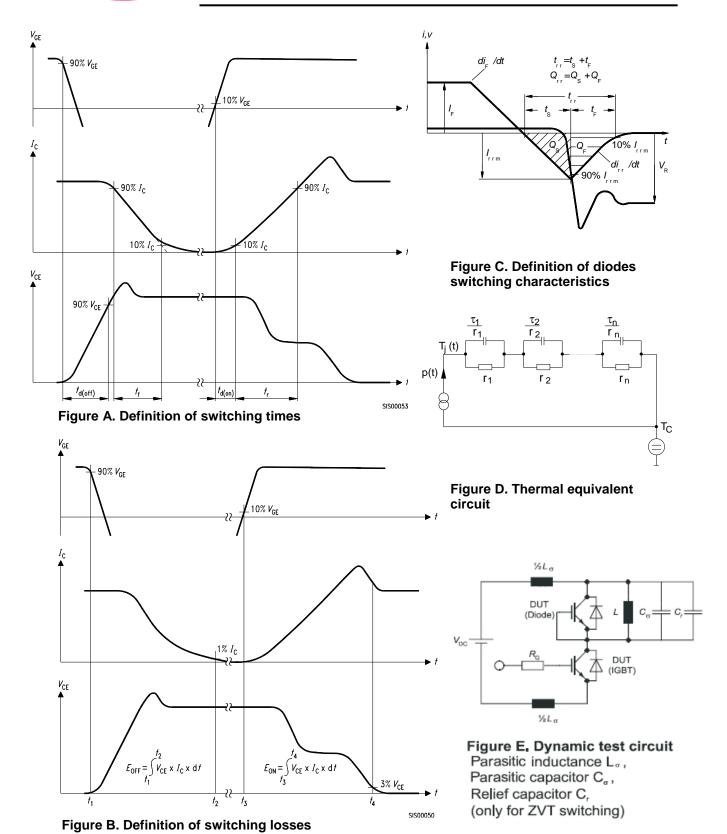


DIM	MILLIN	METERS	NCHES		
Dem	MIN	MAX	MIN	MAX	
A	4.83	5.21	0.190	0.205	
A1	2,27	2,54	0.089	0.100	
A2	1.85	2.16	0.073	0.085	
ь	1.07	1.33	0.042	0.052	
b1	1.90	2,41	0.075	0.095	
b2	1.90	2.16	0.075	0.085	
b3	2,87	3.38	0.113	0.133	
b4	2,87	3.13	0.113	0.123	
c	0.55	0.68	0.022	0.027	
D	20.80	21,10	0.819	0.831	
D1	16.25	17.65	0.640	0.695	
D2	0.95	1.35	0.037	0.053	
E	15.70	16.13	0.618	0.635	
E1	13.10	14.15	0.516	0.557	
E2	3,68	5.10	0.145	0.201	
E3	1.00	2,60	0.039	0.102	
e	5	.44 (BSC)	0.214 (BSC)		
N		3	3		
L	19,80	20,32	0.780	0.800	
L1	4.10	4.47	0.161	0.176	
øΡ	3,50	3,70	0.138	0.146	
Q	5.49	6.00	0.216	0.236	
S	6.04	6.30	0.238	0.248	

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