



Low Voltage Dual SPDT 0.8Ω Analog Switch

Features

- CMOS Technology for Bus and Analog Applications
- Low On-Resistance: 0.8Ω at 3.0V
- Wide V_{CC} Range: 1.65V to 5.5V
- Rail-to-Rail Signal Range
- Control Input Overvoltage Tolerance: 5.5V(Min)
- Fast Transition Speed: 12ns at 5.0V
- High Bandwidth: 150 MHz
- Extended Industrial Temperature Range: -40°C to 85°C
- Packaging (Pb-free & Green): 10-Pin MSOP

Applications

- Cell Phones
- PDAs
- Portable Instrumentation
- Battery powered Communications
- Computer Peripherals

Pin Description

Pin No	Name	Description
2, 4	$_{1}B_{x}$	Data Port (Normally open)
3	GND	Ground
7, 9	$_{0}\mathrm{B}_{\mathrm{x}}$	Data Port (Normally closed)
6, 10	A_X	Common Output / Data Port
8	V_{CC}	Positive Power Supply
1, 5	S_X	Logic Control

Logic Function Table

Logic Input (S _X)	Function				
0	₀ B _x Connected to A _X				
1	₁ B _x Connected to A _X				

Note: X = 0 or 1

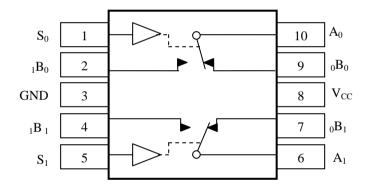
Description

The PI5A23159 is a dual high-bandwidth, fast single-pole double-throw (SPDT) CMOS switch. It can be used as an analog switch or as a low-delay bus switch. The device features ultra low RON of 0.8Ω typical at 3.0V VCC and will operate over the wide VCC range of 1.65V to 5.5V.

The PI5A23159 features very low quiescent current even when the control voltage is lower than the VCC supply. This feature services the mobile handset applications very well by allowing direct interface with baseband processor general purpose I/Os. Break-before-make switching prevents both switches being enabled simultaneously. This eliminates signal disruption during switching.

The control input, S, is independent of supply voltage.

Pin Assignment







Maximum Ratings

Storage Temperature	
Ambient Temperature with Power Applied	40°C to +85°C
Supply Voltage V _{CC}	0.5V to +7.0V
DC Switch Voltage V _S	0.5V to +7.0V
DC Input Voltage V _{IN}	0.5V to +7.0V
DC Output Current V _{OUT}	128mA
DC V_{CC} or Ground Current I_{CC}/I_{GND}	$$ $\pm 100mA$
Junction Temperature under Bias (TJ)	150°C
Junction Lead Temperature (TL)	
(Soldering, 10 seconds)	260°C
ESD (HBM)	5KV
Power Dissipation (PD) @ +85°C	MSOP10 350mW

Note:

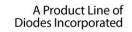
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V_{CC}	Operating Voltage	-	1.65	-	5.5	V
V _{IN}	Control Input Voltage	-	0	-	V_{CC}	V
V _S	Switch Input Voltage	-	0	-	5.5	V
V _{OUT}	Output Voltage	-	0	-	V_{CC}	V
T_A	Operating Temperature	-	-40	25	85	$^{\circ}\mathrm{C}$
t_r, t_f Input Rise and Fall	Input Disc and Fall Time	Control Input $VCC = 2.7V$ to $3.6V$	0	-	10	ns/V
	input Kise and Fail Time	Control Input $VCC = 4.5V$ to $5.5V$	0	-	5	ns/V

Note: Control input must be held HIGH or LOW; it must not float.







DC Electrical Characteristics

 $(T_A = -40$ °C to 85 °C, unless otherwise noted.)

Symbol	Parameter	Test Conditions	Supply Voltage	Min.	Тур.	Max.	Units	
V_{IAR}	Analog Input Signal Range	-	V_{CC}	0	-	V_{CC}	V	
		$I_{Ax}=100$ mA, $V_{nBx}=0$ V		-	0.7	1.1	Ω	
		$I_{Ax} = 100 \text{mA}, V_{nBx} = 2.4 \text{V}$	$V_{CC}=4.5V$	-	0.6	1.0		
		$I_{Ax} = 100 \text{mA}, V_{nBx} = 4.5 \text{V}$		-	0.8	1.2		
		$I_{Ax}=100mA$, $V_{nBx}=0V$	V _{CC} =3.0V	-	0.8	1.3	Ω	
R_{ON}	ON Resistance (1)	$I_{Ax} = 100 \text{mA}, V_{nBx} = 3.0 \text{V}$	V CC=3.0 V	-	0.9	1.9	32	
		$I_{Ax}=100mA$, $V_{nBx}=0V$	V _{CC} =2.3V	-	1.0	1.5	Ω	
		$I_{Ax} = 100 \text{mA}, V_{nBx} = 2.3 \text{V}$	▼ CC=2.5 ▼	-	1.2	1.8	32	
		$I_{Ax}=100\text{mA}, V_{nBx}=0V$	V _{CC} =1.65V	-	1.3	1.9	Ω	
		$I_{Ax} = 100 \text{mA}, V_{nBx} = 1.65 \text{V}$	V (C=1.03 V	-	2.0	2.8	32	
		$I_{Ax} = 100 \text{mA}, V_{nBx} = 3.15 \text{V}$	$V_{CC}=4.5V$	-	0.01	0.12		
AD	ON Resistance Match	$I_{Ax}=100 \text{mA}, V_{nBx}=2.1 \text{V}$	$V_{CC}=3.0V$	-	0.02	-	0	
$\Delta R_{\rm ON}$	Between Channels (1,2,3)	$I_{Ax} = 100 \text{mA}, V_{nBx} = 1.6 \text{V}$	$V_{CC}=2.3V$	-	0.03	-	Ω	
		$I_{Ax}=100 \text{mA}, V_{nBx}=1.15 \text{V}$	V _{CC} =1.65V	-	0.03	-	•	
	$I_{Ax}=100 \text{mA}, V_{nBx}=0 \text{V}, 2.4 \text{V}, 4.5 \text{V}$	V _{CC} =4.5V	-	0.2	0.4			
R _{ONF}	ON Resistance Flatness (1,2,4)	$I_{Ax}=100\text{mA}, V_{nBx}=0V,1.5V,3.3V$	V _{CC} =3.3V	-	0.2	0.4	Ω	
K _{ONF} ON Resistance Flatness	Of Resistance Flatness	$I_{Ax}=100\text{mA}, V_{nBx}=0V,1.1V,2.5V$	$V_{CC}=2.5V$	-	0.4	0.6		
	$I_{Ax}=100 \text{mA}, V_{nBx}=0 \text{V}, 0.7 \text{V}, 1.8 \text{V}$	$V_{CC}=1.8V$	-	1.0	1.4			
		$V_{CC} = 1.65V$	0.9	-	-			
		Logic High Level	$V_{CC}=2.3V$	1.0	-	-	V	
V_{IH}	Input High Voltage		$V_{CC}=3.0V$	1.1	-	-		
			$V_{CC}=4.2V$	1.2	-	-		
			$V_{\rm CC}=5.5V$	1.3	-	-		
			$V_{CC} = 1.65V$	-	-	0.6		
			$V_{CC}=2.3V$	-	-	0.6		
$V_{ m IL}$	Input Low Voltage	Logic Low Level	$V_{CC}=3.0V$	-	-	0.6	V	
			$V_{CC}=4.2V$	-	-	0.8]	
			$V_{CC}=5.5V$	-	-	0.8		
I _{OFF(Bn)}	Source Off Leakage Current	$V_{Ax} = 1V/4.5V, V_{nBx} = 1V/4.5V$	$V_{CC}=3.0V$	-20	-	+20	nA	
I _{ON(A, Bn)}	Channel On Leakage Current	-	V _{CC} =1.65V to 5.5V	-40	-	+40	nA	
T	Quiescent Supply Current	All Channels ON or OFF,	V _{CC} =3.6V	-	0.002	0.1	μA	
I_{CC}		$V_{nBx}=V_{CC}$ and GND, $I_{OUT}=0A$	V _{CC} =5.5V	-	0.002	0.1	μΑ	
I_{CCT}	Increase in I _{CC} per Input	Channel Input at 2.7V	V _{CC} =4.3V	-	0.2	10.0	μA	

Notes:

- 1. Measured by voltage drop between A and B pins at the indicated current through the device. ON resistance is determined by the lower of the voltages on two ports (Ax or nBx) x = 0 or 1, n = 0 or 1.
- 2. Parameter is characterized but not tested in production.
- 3. ΔR_{ON} = R_{ON} max R_{ON} min. measured at identical V_{CC} , temperature and voltage levels.
- 4. Flatness is defined as difference between maximum and minimum value of ON resistance over the specified range of conditions.

Capacitance (1)

Symbol	Parameter	Test Conditions	Supply Voltage	Temp (℃)	Min.	Тур.	Max.	Units
C_{IN}	Control Input				-	3.5	-	
C _{IO-B}	For Bn Port, Switch OFF	f = 1 MHz	$V_{CC} = 5.0V$	$T_A = 25$ °C	-	15.0	-	pF
C _{IOA-ON}	For An Port, Switch ON				-	34.0	-	

Notes

1. Capacitance is characterized but not tested in production





Switch and AC Characteristics $^{(1)}$

Parameter	Description	Test Conditions	Supply Voltage	Min	Тур	Max	Units	
t	Break Before	See Figure 2	$V_{CC} = 2.7V \text{ to } 3.6V$	-	10	-		
BBM	Make Time	See Figure 2	$V_{CC} = 4.5V \text{ to } 5.5V$	-	6	-		
t	Turn on Time	See Figure 1	$V_{CC} = 2.7V \text{ to } 3.6V$	-	16	-	ns	
t_{ON}	Turn on Time	See Figure 1	$V_{CC} = 4.5V \text{ to } 5.5V$	-	12	-	113	
f	Turn off Time	See Figure 1	$V_{CC} = 2.7V \text{ to } 3.6V$	-	8	-		
t_{OFF}	Turn on Time	See Figure 1	$V_{CC} = 4.5V \text{ to } 5.5V$	-	5	-		
0	Q Charge Injection	\sim	$V_{CC} = 5.0V$	-	35	-	рC	
Q			$V_{CC} = 3.3V$	-	25	-	рС	
OIRR	Off Isolation ⁽²⁾	R_L =50 Ω , V_{GEN} =0 V , R_{GEN} =0 Ω , f =1MHz. See Figure 4	$V_{CC} = 1.65 \text{V to } 5.5 \text{V}$	-	-70	-	dB	
X _{TALK}	Crosstalk Isolation	f=1MHz, See Figure 5	$V_{CC} = 1.65 \text{V to } 5.5 \text{V}$	-	-70	-		
f3dB	-3dB Bandwidth	See Figure 8	$V_{CC} = 1.65 \text{V to } 5.5 \text{V}$	-	150	-	MHz	
$T_{ m HD}$	Total Harmonic Distortion	R_L =600 Ω , V_{IN} =0.5 V pp, f=20 H z to 20 k Hz See Figure 9	$V_{CC} = 2.7V \text{ to } 4.2V$	-	0.015	-	%	

Notes:

- 1. Guaranteed by design.
- 2. Off Isolation = 20 Log_{10} [$V_{nBx}\,/V_{Ax}$] and is measured in dB.



Test Circuits and Timing Diagrams

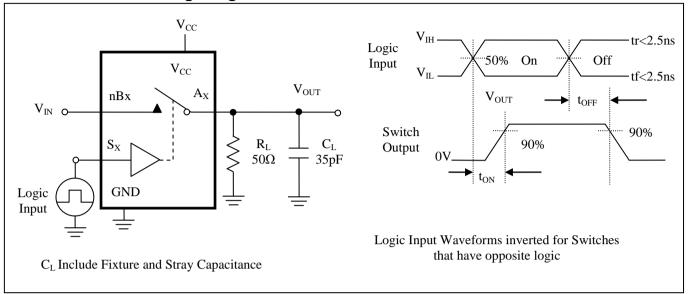


Figure 1. Turn ON/OFF Timing

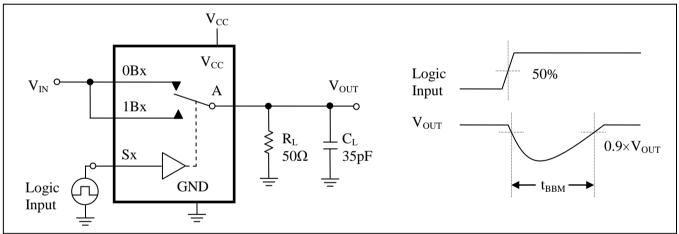


Figure 2. Break Before Make Interval Timing

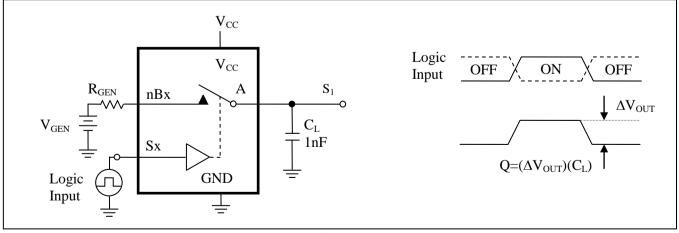


Figure 3. Charge Injection Test



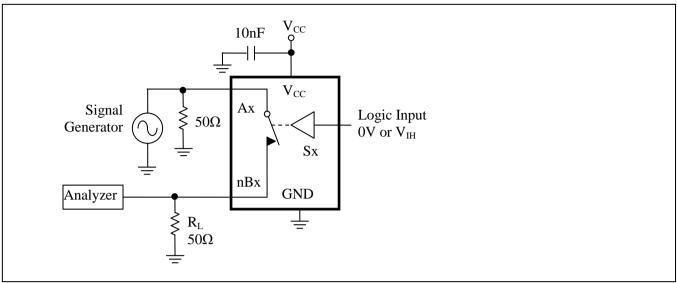


Figure 4. Off Isolation

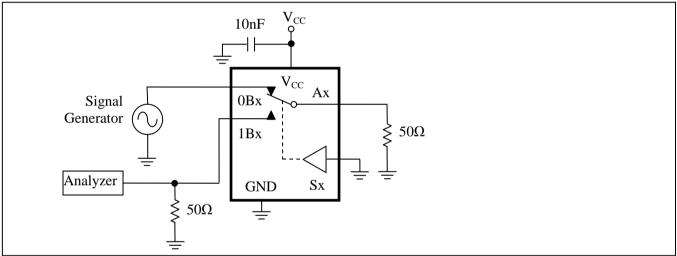


Figure 5. Crosstalk

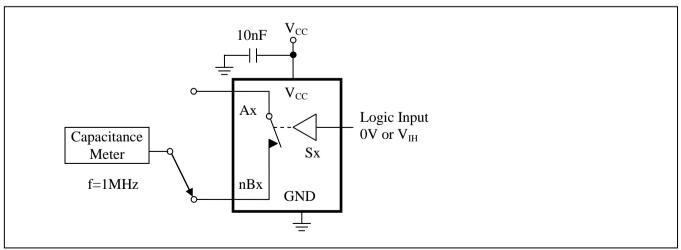


Figure 6. Channel Off Capacitance



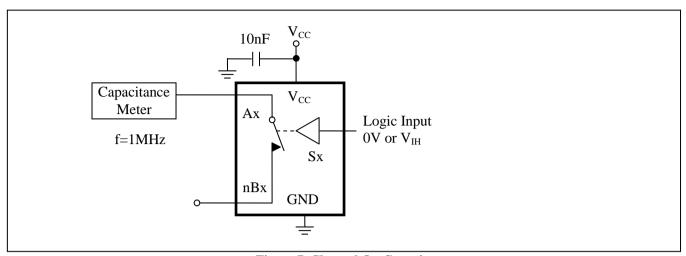


Figure 7. Channel On Capacitance

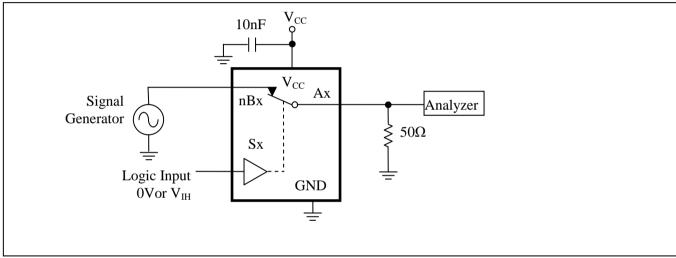


Figure 8. Bandwidth

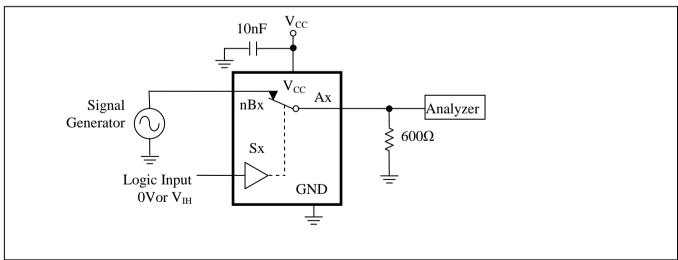


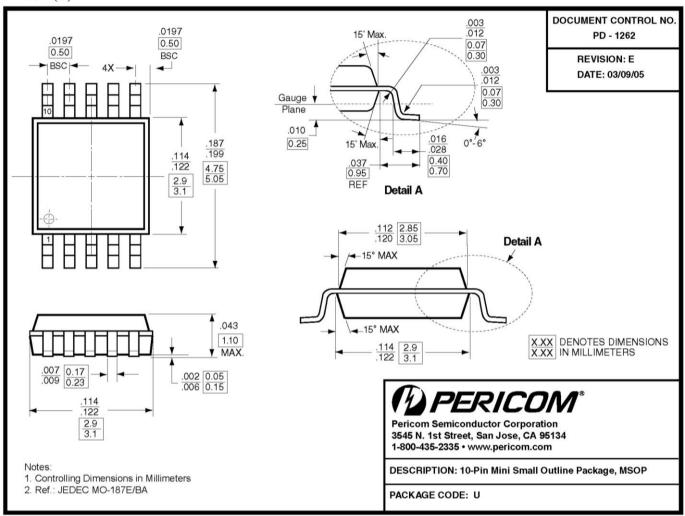
Figure 9. Harmonic Distortion





Packaging Mechanical

10-MSOP (U)



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Ordering Information

Part Number	Package Code	Package Description
PI5A23159UEX	U	10-Pin, Mini Small Outline Package (MSOP)

Notes:

- EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
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