

IGBT Chip in NPT-technology

FEATURES:

- 600V NPT technology
- 100µm chip
- short circuit prove
- positive temperature coefficient
- easy paralleling

This chip is used for:

- DuoPack SKP06N60
- G

Applications:

• drives

Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code
SIGC07T60SNC	600V	6A	2.6 x 2.6 mm ²	sawn on foil	Q67041-A4672- A003
SIGC07T60SNC	600V	6A	2.6 x 2.6 mm ²	unsawn	Q67041-A4672- A002

MECHANICAL PARAMETER:

Raster size	2.6 x 2.6 mn				
Area total / active	6.76 / 4.3				
Emitter pad size	1.107 x 1.78				
Gate pad size	0.5 x 0.7				
Thickness	100	μm			
Wafer size	150	mm			
Flat position	0 //180	deg			
Max.possible chips per wafer	2249				
Passivation frontside	Photoimide				
Emitter metallization	3200 nm Al Si 1%				
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500µm				
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				

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MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, Tj=25 °C	V _{CE}	600	V
DC collector current, limited by T _{jmax}	I _C	1)	А
Pulsed collector current, t _p limited by T _{jmax}	I _{cpuls}	18	А
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	T _j , T _{stg}	-55 +150	°C

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), $T_i=25$ °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
		oonaniona	min.	typ.	max.	O
Collector-emitter breakdown voltage	V _{(BR)CES}	V _{GE} =0V, I _C =500µA	600			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =6A	1.6	2	2.5	V
Gate-emitter threshold voltage	V _{GE(th)}	I_C =200µA, V_{GE} = V_{CE}	3	4	5	
Zero gate voltage collector current	I _{CES}	V _{CE} =600V, V _{GE} =0V			0.55	μA
Gate-emitter leakage current	I _{GES}	V_{CE} =0V, V_{GE} =20V			120	nA

DYNAMIC CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
Falameter			min.	typ.	max.	
Input capacitance	Ciss	V _{CE} =25V	-	350	420	pF
Output capacitance	Coss	$V_{GE}=0V$	-	38	46	
Reverse transfer capacitance	Crss	f=1MHz	-	23	28	

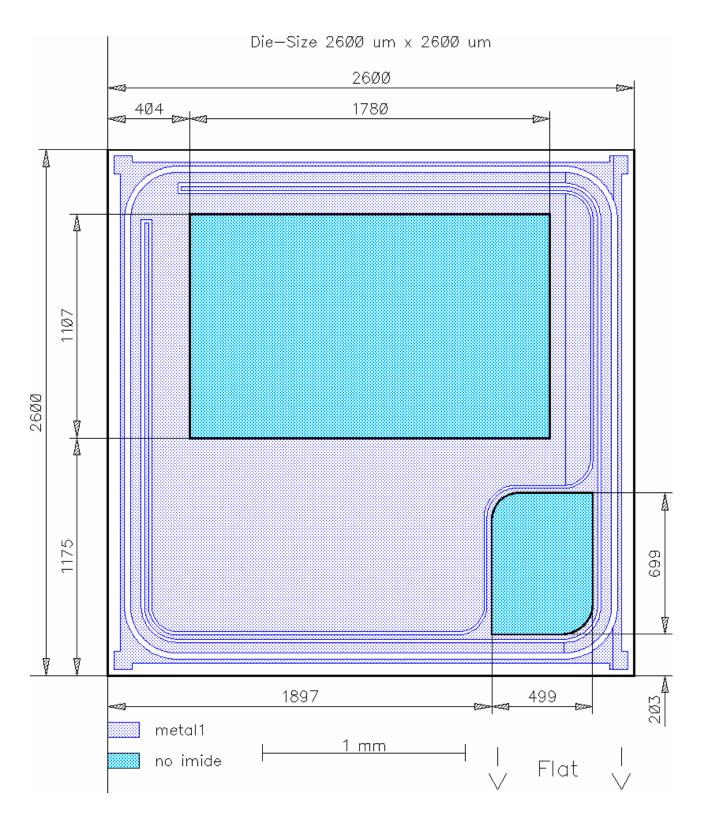
SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

Parameter	Symbol	Conditions ²⁾	Value			Unit
Falameter	Symbol		min.	typ.	max.	
Turn-on delay time	t _{d(on)}	$T_j=150^{\circ}C$ $V_{CC}=400V$	-	24	29	ns
Rise time	t _r	/ _C =6A	-	17	20	
Turn-off delay time	t _{d(off)}	V _{GE} =+15/0V R _G =50Ω	-	248	298	
Fall time	t _f		-	70	84	

²⁾ switching conditions different to 600V Standard IGBT 2, under comparable switching conditions 40% faster turnoff than Standard IGBT 2. Values also influenced by parasitic L- and C- in measurement and package.



CHIP DRAWING:



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FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet

SGP06N60

Package :TO220

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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