

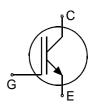
IGBT Chip in NPT-technology

Features:

- 1700V NPT technology
- 280µm chip
- short circuit prove
- positive temperature coefficient
- easy paralleling
- Qualified according to JEDEC for target applications

Recommended for:

- chip only
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- Applications:drives
- unves



Chip Type	V _{CE}	I _{Cn}	Die Size	Package
SIGC144T170R2C	1700V	75A	11.98 x 11.98 mm ²	sawn on foil

Mechanical Parameters

Mechanical Paramet	612		T	
Die size		11.98 x 11.98		
Emitter pad size (incl.	gate pad)	See chip drawing		
Gate pad size		0.757 x 1.48	- mm ²	
Area total		143.52		
Thickness		280	μm	
Wafer size		150	mm	
Max.possible chips pe	er wafer	93		
Passivation frontside		Photoimide		
Pad metal		3200 nm AlSiCu		
Backside metal		Ni Ag –system		
Die bond		Electrically conductive epoxy glue and soft solder		
Wire bond		Al, <500µm		
Reject ink dot size		Ø 0.65mm ; max 1.2mm		
	for original and sealed MBB bags	Ambient atmosphere air, Temperature 17°C – 2 < 6 month		
Storage environment	for open MBB bags	Acc. to IEC62258-3: Atmosphere >99% Nitrogen or inert gas, Humidity <25%RH, Temperature 17°C – 25°C, < 6 month		



Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-Emitter voltage, T_{vj} =25 °C	V _{CE}	1700	V
DC collector current, limited by $T_{vj max}$	I _C	1)	Α
Pulsed collector current, t_p limited by $T_{vj max}^{2}$	I _{c,puls}	225	А
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	$T_{\rm vj,} T_{stg}$	-55 +150	°C
Short circuit data ²⁾³⁾ $V_{GE} = 15V$, $V_{CC} = 1000V$, $T_{vj} = 150^{\circ}C$	t _{SC}	10	μs

¹⁾ depending on thermal properties of assembly

²) not subject to production test - verified by design/characterization

³⁾ allowed number of short circuits: <1000; time between short circuits: >1s.

Static Characteristics (tested on wafer), T_{vj} =25 °C

Parameter	Symbol	Conditions	Value			Unit
i arameter	Cymbol		min.	typ.	max.	
Collector-Emitter breakdown voltage	V _{(BR)CES}	<i>V</i> _{GE} =0V , <i>I</i> _C =2 mA	1700			
Collector-Emitter saturation voltage	V _{CEsat}	V _{GE} =15V, <i>I</i> _C =75A	2.18	2.6	2.92	V
Gate-Emitter threshold voltage	V _{GE(th)}	$I_{\rm C}$ =3.3mA , $V_{\rm GE}$ = $V_{\rm CE}$	4.6	5.5	6.4	
Zero gate voltage collector current	I _{CES}	V _{CE} =1700V , V _{GE} =0V			3	μA
Gate-Emitter leakage current	I _{GES}	V_{CE} =0V , V_{GE} =20V			480	nA
Integrated gate resistor	r _G			5		Ω

Electrical Characteristics (not subject to production test - verified by design / characterization)

Beremeter	Symbol	Conditions	Value			l Init
Parameter	Symbol		min.	typ.	max.	Unit
Collector-Emitter saturation voltage	V _{CEsat}	V _{GE} =15V, <i>I</i> _C =75A, <i>T</i> _{vj} =125 °C		3.1		V
Input capacitance	Cies	V _{CE} =25V, V _{GE} =0V, <i>f</i> =1MHz		10000		pF
Reverse transfer capacitance	C _{res}	$T_{\rm vj}$ =25 °C		500		P. 1



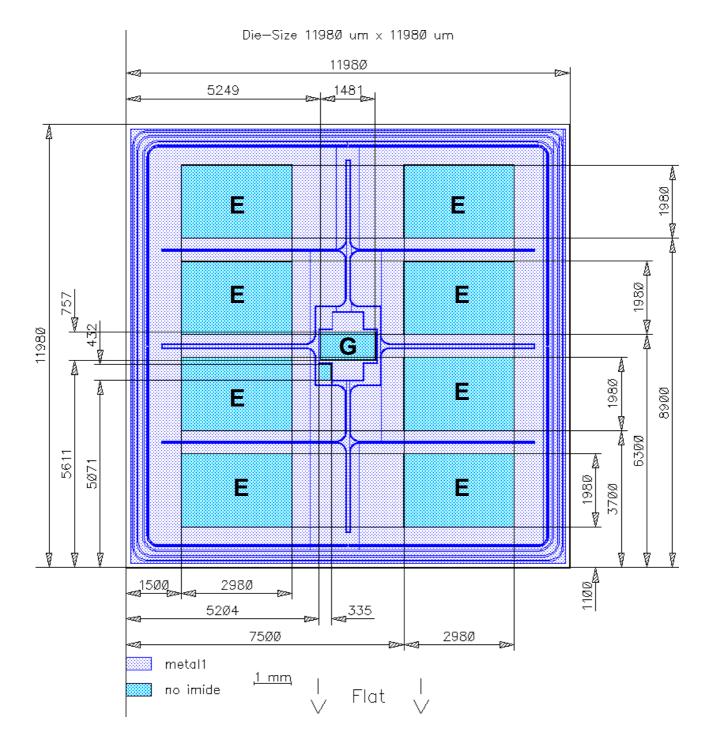
Further Electrical Characteristic

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

This chip data sheet refers to the device data sheet	FD600R17KF6C_B2 Rev.2.3	04.04.2013
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Chip Drawing



E = Emitter

 $\mathbf{G} = \text{Gate}$



Description

AQL 0,65 for visual inspection according to failure catalogue

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Revision History

Version	Subjects (major changes since last revision)	Date	
2.2	Operating junction and storage temperature	15.05.2013	

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