International **ICR** Rectifier

POWER MOSFET SURFACE MOUNT(SMD-1)

Product Summary

| Part Number | RDS(on) | ID |
|-------------|---------|-----|
| IRFN450 | 0.415 Ω | 12A |

HEXFET® MOSFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry design achieves very low on-state resistance combined with high transconductance. HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, high energy pulse circuits, and virtually any application where high reliability is required. The HEXFET transistor's totally isolated package eliminates the need for additional isolating material between the device and the heatsink. This improves thermal efficiency and reduces drain capacitance.

PD - 90418C

IRFN450 JANTX2N7228U JANTXV2N7228U REF:MIL-PRF-19500/592 500V, N-CHANNEL HEXFET[®] MOSFETTECHNOLOGY



Features:

- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Electrically Isolated
- Surface Mount
- Dynamic dv/dt Rating
- Light-weight

| | Parameter | | Units |
|--|--------------------------------------|--------------------|-------|
| ID @ VGS = 10V, TC = 25°C | Continuous Drain Current | 12 | |
| ID @ VGS = 10V, TC = 100°C | Continuous Drain Current | 8.0 | A |
| IDM | Pulsed Drain Current ① | 48 | |
| P _D @ T _C = 25°C | Max. Power Dissipation | 150 | W |
| | Linear Derating Factor | 1.2 | W/°C |
| VGS | Gate-to-Source Voltage | ±20 | V |
| EAS | Single Pulse Avalanche Energy 2 | 750 | mJ |
| IAR | Avalanche Current ① | 12 | A |
| EAR | Repetitive Avalanche Energy ① | 15 | mJ |
| dv/dt | Peak Diode Recovery dv/dt 3 | 3.5 | V/ns |
| TJ | Operating Junction | -55 to 150 | |
| TSTG | Storage Temperature Range | | °C |
| | Package Mounting Surface Temperature | 300(for 5 seconds) | |
| | Weight | 2.6 (Typical) | g |

Absolute Maximum Ratings

For footnotes refer to the last page

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| Electrical Characteristics @ Tj = 25°C (Un | less Otherwise Specified) |
|--|---------------------------|
|--|---------------------------|

| | Parameter | Min | Тур | Max | Units | Test Conditions |
|--------------------------------|--|-----|------|-------|-------|---|
| BVDSS | Drain-to-Source Breakdown Voltage | 500 | _ | — | V | $V_{GS} = 0V, I_{D} = 1.0mA$ |
| $\Delta BV_{DSS}/\Delta T_{J}$ | Temperature Coefficient of Breakdown Voltage | — | 0.68 | _ | V/°C | Reference to 25°C, ID = 1.0mA |
| RDS(on) | Static Drain-to-Source On-State | | _ | 0.415 | Ω | VGS = 10V, ID = 8.0A (4) |
| | Resistance | _ | — | 0.515 | 32 | V _{GS} = 10V, I _D = 12A |
| VGS(th) | Gate Threshold Voltage | 2.0 | — | 4.0 | V | $V_{DS} = V_{GS}$, $I_D = 250 \mu A$ |
| 9fs | Forward Transconductance | 6.5 | — | — | S (ひ) | V _{DS} > 15V, I _{DS} = 8.0A ④ |
| IDSS | Zero Gate Voltage Drain Current | _ | _ | 25 | μA | V _{DS} = 400V ,V _{GS} =0V |
| | | _ | — | 250 | μΛ | V _{DS} = 400V, |
| | | | | | | $V_{GS} = 0V, T_{J} = 125^{\circ}C$ |
| IGSS | Gate-to-Source Leakage Forward | — | — | 100 | nA | $V_{GS} = 20V$ |
| IGSS | Gate-to-Source Leakage Reverse | | — | -100 | | VGS = -20V |
| Qg | Total Gate Charge | _ | — | 120 | | VGS =10V, ID = 12A |
| Qgs | Gate-to-Source Charge | _ | — | 19 | nC | V _{DS} =250V |
| Q _{gd} | Gate-to-Drain ('Miller') Charge | _ | _ | 70 | | |
| ^t d(on) | Turn-On Delay Time | _ | — | 35 | | V _{DD} = 250V, I _D = 12A, |
| tr | Rise Time | _ | — | 190 | | V_{GS} =10V, R_{G} = 2.35 Ω |
| ^t d(off) | Turn-Off Delay Time | _ | — | 170 | ns | |
| tf | FallTime | _ | — | 130 | | |
| Ls+LD | Total Inductance | — | 4.0 | — | nH | Measured from the center of drain |
| | | | | | | pad to center of source pad. |
| Ciss | Input Capacitance | — | 2700 | — | | $V_{GS} = 0V, V_{DS} = 25V$ |
| C _{oss} | Output Capacitance | _ | 600 | — | pF | f = 1.0MHz |
| Crss | Reverse Transfer Capacitance | _ | 240 | — | | |

Source-Drain Diode Ratings and Characteristics

| | Parameter | Min | Тур | Max | Units | Test Conditions |
|-----|--|---|-----|------|-------|---|
| IS | Continuous Source Current (Body Diode) | _ | _ | 12 | Δ | |
| ISM | Pulse Source Current (Body Diode) ① | — | _ | 48 | A | |
| VSD | Diode Forward Voltage | — | _ | 1.7 | V | $T_j = 25^{\circ}C$, $I_S = 12A$, $V_{GS} = 0V$ (4) |
| trr | Reverse Recovery Time | — | — | 1600 | nS | Tj = 25°C, IF = 12A, di/dt ≤ 100A/μs |
| QRR | Reverse Recovery Charge | - | — | 14 | μC | $V_{DD} \leq 30V $ |
| ton | Forward Turn-On Time Intrinsic turn-o | me Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L _S + L _D . | | | | |

Thermal Resistance

| | Parameter | Min | Тур | Max | Units | Test Conditions |
|-----------------------|----------------------|-----|-----|------|-------|------------------------------------|
| RthJC | Junction-to-Case | — | _ | 0.83 | °C/W | |
| R _{th} J-PCB | Junction-to-PC board | — | 3.0 | — | °C/W | Soldered to a copper-clad PC board |

Note: Corresponding Spice and Saber models are available on the G&S Website.

For footnotes refer to the last page

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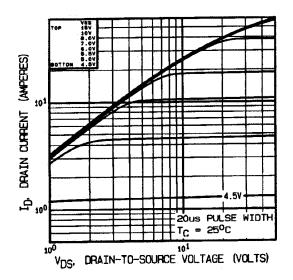


Fig 1. Typical Output Characteristics

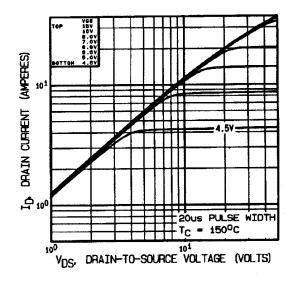


Fig 2. Typical Output Characteristics

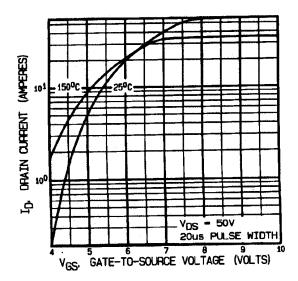
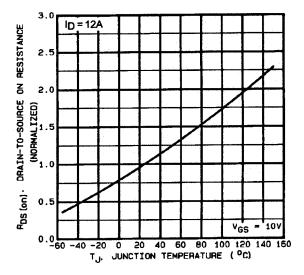
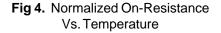


Fig 3. Typical Transfer Characteristics





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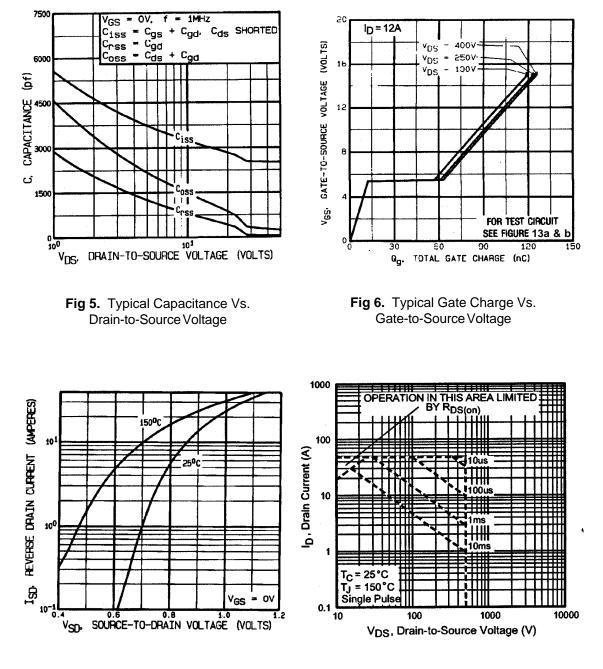
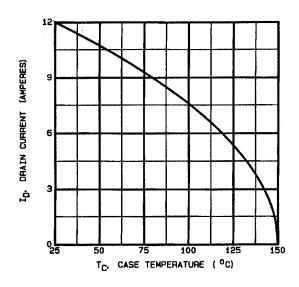


Fig 7. Typical Source-Drain Diode Forward Voltage Fig 8. Maximum Safe Operating Area

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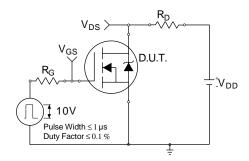


Fig 10a. Switching Time Test Circuit

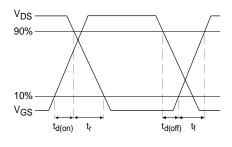


Fig 10b. Switching Time Waveforms

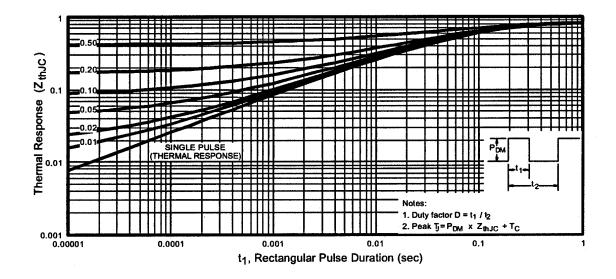


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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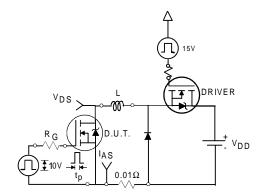


Fig 12a. Unclamped Inductive Test Circuit

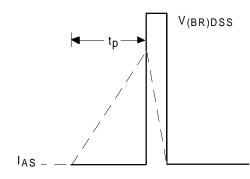


Fig 12b. Unclamped Inductive Waveforms

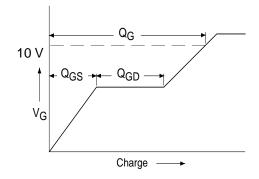


Fig 13a. Basic Gate Charge Waveform

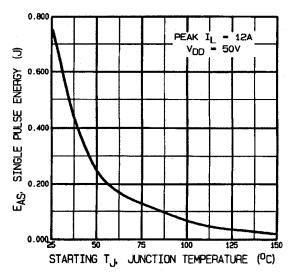


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

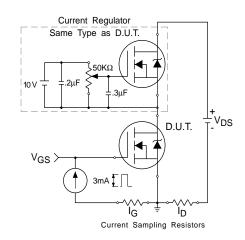


Fig 13b. Gate Charge Test Circuit

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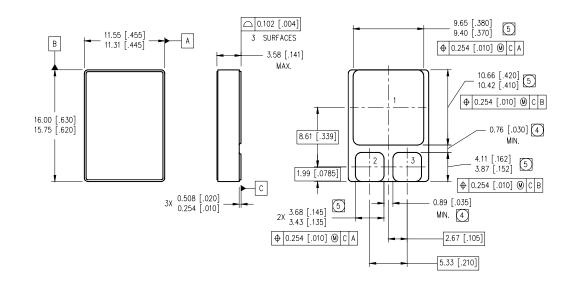
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Footnotes:

① Repetitive Rating; Pulse width limited by maximum junction temperature.

@ VDD = 25V, starting TJ = 25°C, L= 10.4mH Peak IL = 12A, VGS = 10V

- ④ Pulse width \leq 300 μ s; Duty Cycle \leq 2%



Case Outline and Dimensions — SMD-1

NOTES:

- 1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- (4) DIMENSION INCLUDES METALLIZATION FLASH.
- 5 DIMENSION DOES NOT INCLUDE METALLIZATION FLASH.

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Data and specifications subject to change without notice. 01/02

PAD ASSIGNMENTS

3- SOURCE

1- DRAIN

2- GATE