

SiC Diode

Features

- No reverse recovery current / no forward recovery
- High surge current capability
- Temperature independent switching behaviour
- Low forward voltage even at high operating temperature
- Tight forward voltage distribution
- Specified dv/dt ruggedness
- Pb-free lead plating; RoHS compliant



Pin definition

Pin 1 and backside: Cathode

Pin 2: Anode



Potential applications

- Industrial power supplies: Industrial UPS
- Infrastructure-Charge: Charger
- Metal treatment: Welding
- Solar central inverters, Solar string inverter and Solar optimizer

Product validation

Qualified for industrial applications according to the relevant tests of JEDEC 47/20/22

Description

- System efficiency improvement over Si diodes
- Enabling higher frequency / increased power density solutions
- System size/cost savings due to reduced heatsink requirements and smaller magnetics
- Reduced EMI
- Highest efficiency across the entire load range
- Robust diode operation during surge events
- High reliability
- Related Links: www.infineon.com/SiC









Key performance parameters

Туре	V _{DC}	I _F	Q c	$T_{vj,max}$	Marking	Package
IDWD20G120C5	1200 V	20 A	106nC	175°C	D2012C5	PG-TO247-2



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Maximum ratings

1 Maximum ratings

Note:

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Repetitive peak reverse voltage $T_{\rm C} \ge 25^{\circ}{\rm C}$	$V_{ m RRM}$	1200	V
Continuous forward current for $R_{th(j-c,max)}$ $T_c = 156$ °C, D=1 $T_c = 135$ °C, D=1 $T_c = 25$ °C, D=1	I _F	20 29 62	А
Surge repetitive forward current, sine halfwave ¹ $T_{\rm C}$ =25°C, $t_{\rm p}$ =10ms $T_{\rm C}$ =100°C, $t_{\rm p}$ =10ms	$I_{F,RM}$	80 60	A
Surge non-repetitive forward current, sine halfwave $T_{\rm C}$ =25°C, $t_{\rm p}$ =10ms $T_{\rm C}$ =150°C, $t_{\rm p}$ =10ms	I _{F,SM}	190 180	А
Non-repetitive peak forward current $T_{\rm C}$ = 25°C, $t_{\rm p}$ =10 μ s	I _{F,max}	1774	А
$i^{2}t$ value $T_{C} = 25^{\circ}C$, $t_{p}=10$ ms $T_{C} = 150^{\circ}C$, $t_{p}=10$ ms	∫i²dt	180 162	A ² s
Diode d v /d t ruggedness V_R =0960 V	dv/dt	150	V/ns
Power dissipation for $R_{th(j-c,max)}$ $T_c = 25^{\circ}C$	P _{tot}	250	w

¹ Not subject to production test. The test was performed with 20000 pulses (two consecutive half-wave rectified sines with 10 ms period).

Datasheet

Please read the Important Notice and Warnings at the end of this document

V 2



Maximum ratings

Operating temperature	T_{vj}	-55175	°C
Storage temperature	T_{stg}	-55150	°C
Soldering temperature, wave soldering only allowed at leads 1.6mm (0.063 in.) from case for 10 s	$T_{ m sold}$	260	°C
Mounting torque, M3 screw Maximum of mounting processes: 3	М	0.6	Nm



Thermal resistances

2 Thermal resistances

Parameter	Comple al	Candidana	Value			Unit
Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Characteristic						
Diode thermal resistance, junction – case	$R_{th(j-c)}$		-	0.45	0.6	K/W
Thermal resistance, junction – ambient	$R_{th(j-a)}$	leaded	-	-	62	K/W

SiC Diode

Electrical Characteristics



3 Electrical Characteristics

Static Characteristics, at $T_{\nu j}$ =25°C, unless otherwise specified

Parameter	Cymphol	Conditions	Value			11
	Symbol		min.	typ.	max.	Unit
DC blocking voltage	$V_{ m DC}$	<i>T_{vj}</i> = 25°C, I _R =500μA	1200	-	-	V
Diode forward voltage	1/	<i>I</i> _F = 20A, <i>T</i> _{<i>vj</i>} =25°C	-	1.4	1.65	V
	V_{F}	$I_{\rm F}$ = 20A, T_{vj} =150°C	-	1.7	-	
Reverse current	,	V _R =1200V, T _{vj} =25°C	-	12	166	μА
	I _R	V _R =1200V, T _{vj} =150°C	-	58	-	

Dynamic Characteristics, at $T_{\nu j}$ =25°C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
raiailletei			min.	typ.	max.	Oilit
Total capacitive charge		V_R = 800V, T_{vj} = 150°C & 25°C				
	Qc	$Q_C = \int_0^{V_R} C(V) dV$	-	106	-	nC
		<i>V</i> _R =1 V, <i>f</i> =1 MHz	-	1368	-	
Total Capacitance	С	V _R =400 V, <i>f</i> =1 MHz	-	96	-	pF
		V _R =800 V, <i>f</i> =1 MHz	-	76	-	

Electrical Characteristics Diagrams



Electrical Characteristics Diagrams 4

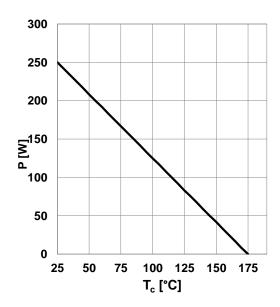


Figure 1. Power dissipation as function of case temperature, $P_{tot}=f(TC)$, $R_{th(j-c),max}$

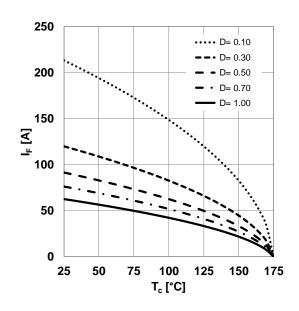


Figure 2. Diode forward current as function of temperature, parameter: $T_{\nu j} \leq 175^{\circ}\text{C}$, $R_{\text{th(j)}}$. c),max, D=duty cycle, V_{th} , R_{diff} @ T_{vj} =175°C

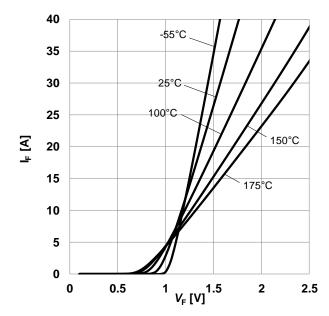


Figure 3. Typical forward characteristics, $I_F=f(V_F)$, $t_p=10 \mu s$, parameter: T_{vj}

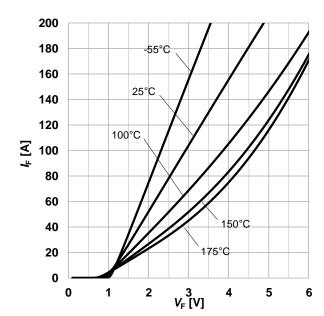


Figure 4. Typical forward characteristics in surge current, $I_F = f(V_F)$, $t_p = 10 \mu s$, parameter: T_{vj}

Electrical Characteristics Diagrams



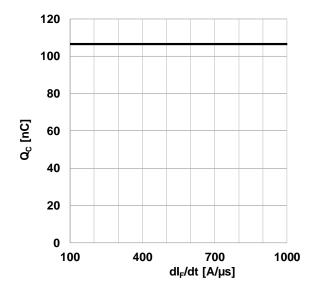


Figure 5. Typical capacitive charge as function of current slope², Q_C =f(dI_F/dt), $T_{\nu j}$ =150°C

2) guaranteed by design

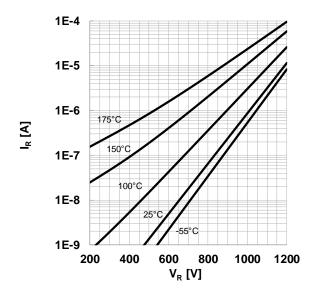


Figure 6. Typical reverse characteristics, I_R = $\mathbf{f}(V_R)$, parameter: T_{vj}

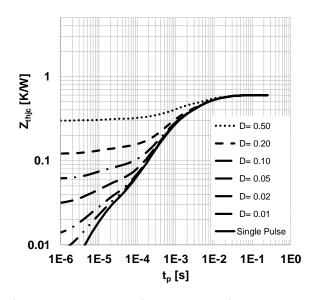


Figure 7. Max. transient thermal impedance, $Z_{th,j\text{-}c} = f(tP)$, parameter: D = tP/T

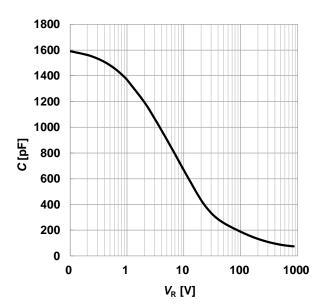


Figure 8. Typical capacitance as function of reverse voltage, C=f(VR); T_{vj} =25°C; f=1 MHz



Electrical Characteristics Diagrams

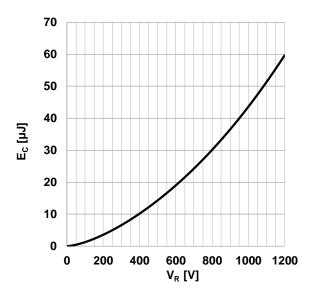


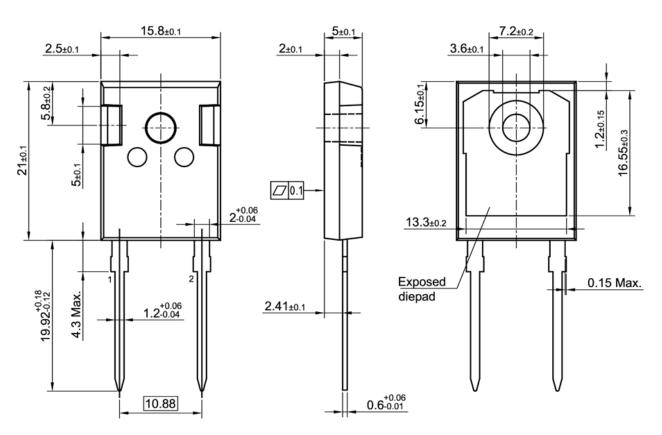
Figure 9. Typical capacitively stored energy as function of reverse voltage, $E_{\rm C}$ =f($V_{\rm R}$)

Package Drawing



5 Package Drawing

PG-TO247-2



All dimensions do not include mold flash or protrusions

All dimensions are in units mm

The drawing is in compliance with ISO 128-30, Projection Method 1 [

SiC-Diode

Revision history



Revision history

Document version	Date of release	Description of changes
V 1.0	2018-12-21	Preliminary Datasheet
V 2.0	2019-01-30	Final Datasheet
V 2.1	2021-03-01	Increased dv/dt ruggedness

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Edition 2021-03-01 Published by Infineon Technologies AG 81726 München, Germany

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Do you have a question about this document?

Email: erratum@infineon.com

Document reference

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