

Voltage Regulator

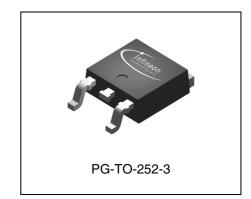
TLE 4284





Features

- Adjustable output voltage or 1.5V, 1.8V, 2.6V, 3.3 V, 5.0V output voltage
- 1.0 A output current
- Low dropout voltage, typ. 1 V
- Short circuit protection
- Overtemperature protection
- Wide operating range up to 40 V
- Wide temperature range of T_i = -40 to 150 °C
- Suitable for use in automotive electronics
- Green Product (RoHS compliant)
- AEC Qualified



Functional Description

The TLE 4284 is a monolithic integrated NPN type voltage regulator that can supply loads up to 1.0 A. The chip is housed in a surface mounted PG-TO252-3-11 package (DPAK). It is designed to supply microprocessor systems or other loads under the severe conditions of automotive applications and therefore it is equipped with additional protection against overload, short circuit and overtemperature.

An input voltage $V_{\rm I}$ in the range of $(V_{\rm Q} + V_{\rm DR}) < V_{\rm I} <$ 40 V is regulated to $V_{\rm Q}$. The dropout voltage $V_{\rm DR}$ ranges from 1.1 V to 1.4 V depending on the load current level.

The device operates in the temperature range of T_i = -40 to 150 °C.

Туре	Package	Marking
TLE 4284 DV	PG-TO252-3-11	4284V
TLE 4284 DV15	PG-TO252-3-11	4284V15
TLE 4284 DV18	PG-TO252-3-11	4284V18
TLE 4284 DV26	PG-TO252-3-11	4284V26
TLE 4284 DV33	PG-TO252-3-11	4284V33
TLE 4284 DV50	PG-TO252-3-11	4284V50

Data Sheet 1 Rev. 2.1, 2007-03-20



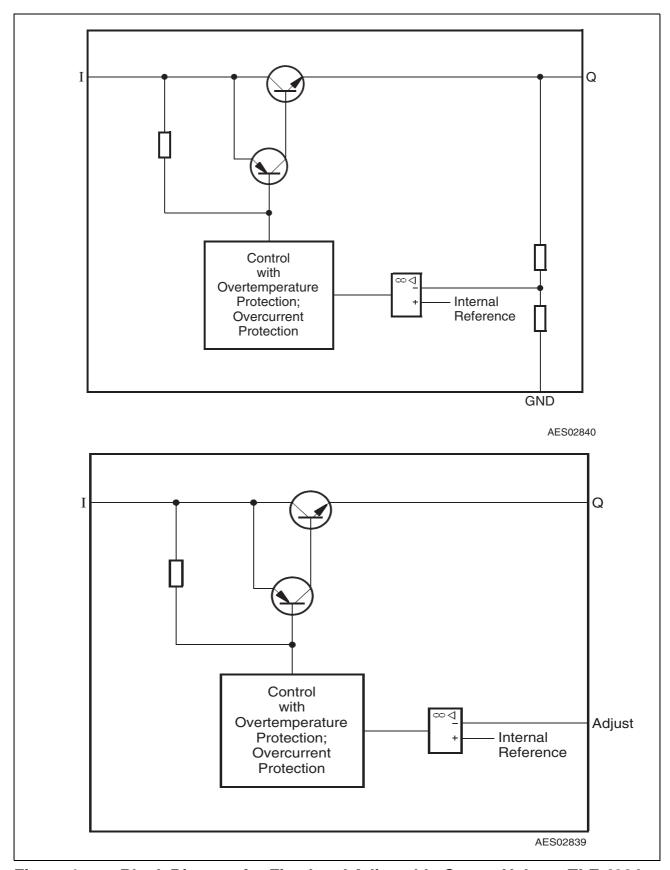


Figure 1 Block Diagram for Fixed and Adjustable Output Voltage TLE 4284



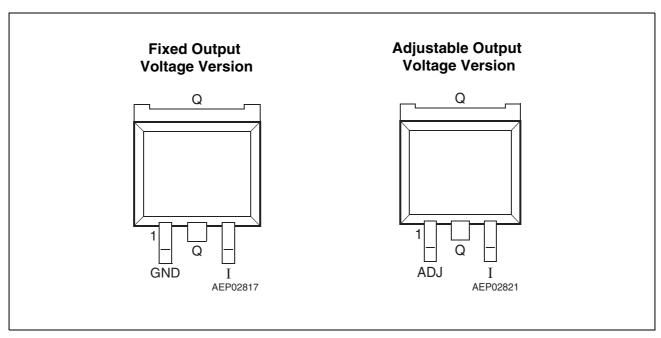


Figure 2 Pin Configuration (top view)

Table 1 Pin Definitions and Functions Fixed Output Voltage Versions

Pin No.	Symbol	Function
1	GND	Ground
2, Tab	Q	Output; Connect output pin to GND via a capacitor $C_{\rm Q} \ge 10~\mu {\rm F}$ with ESR $\le 10~\Omega$. Connect to heatsink area.
3	I	Input

 Table 2
 Pin Definitions and Functions Adjustable Output Version

Pin No.	Symbol	Function
1	ADJ	Adjust; defines output voltage by external voltage divider between Q, ADJ and GND.
2, Tab	Q	Output ; the output voltage is defined by the external voltage divider between Q, Adjust and Ground. Connect the output pin to GND via a capacitor $C_Q \ge 10~\mu\text{F}$ with ESR $\le 10~\Omega$. Connect to heatsink area.
3	I	Input



Table 3 Absolute Maximum Ratings

Parameter	Symbol	Lim	it Values	Unit	Test Condition
		Min.	Max.		
Input - Output Voltag	ge Differer	nce (varia	able device	only)	
Voltage	V_{I} - V_{Q}	-0.3	40	V	_
Input Voltage		•		•	
Voltage	V_{I}	-0.3	40	V	_
Output (fixed voltag	e version o	only)			
Voltage	V_{Q}	-0.3	40	V	_
Current	I_{Q}	_	_	_	Internally limited
Adjust (variable vers	sion only)				
Voltage	V_{ADJ}	-0.3	40	V	_
Current	I_{ADJ}	_	_	_	Internally limited
ESD Susceptibility					
Human Body Model	Class	_	3	_	_
(HBM) ¹⁾	Voltage	_	4	kV	_
Charged Device	Class	_	F5	_	_
Model (CDM) ²⁾	Voltage	_	1	kV	_
Temperature		•	•	•	•
Storage temperature	$T_{ m stg}$	-50	150	°C	_
Junction temperature	$T_{\rm j}$	-40	150	°C	_
1) ESD HBM tost according	a to JEDEC JE	ED22 A11	1	•	•

¹⁾ ESD HBM test according to JEDEC JESD22-A114

Note: Stresses above those listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

²⁾ ESD CDM test according to JEDEC JESD22-C101



Table 4 Operating Range

Parameter	Symbol	Limit	Values	Unit	Remarks
		Min.	Max.		
Input voltage	V_{I}	$V_{ m Qnom}$ + $V_{ m DR}$	40	V	_
Junction temperature	T_{j}	-40	150	°C	_
Thermal Resistance			•		
Junction ambient	R_{thja}	_	144	K/W	PG-TO252-3-11 footprint only ¹⁾
		_	78	K/W	PG-TO252-3-11 300 mm ² heat sink area ¹⁾
		_	54	K/W	PG-TO252-3-11 600 mm ² heat sink area ¹⁾
Junction case	R_{thic}	_	4	K/W	_

¹⁾ FR4, 80 x 80 x 1.5mm², 35µm Cu, 5µm Sn, horizontal position, zero airflow

Note: Within the operating range, the functions given in the circuit description are fulfilled.

The values listed in the "Electrical Characteristics" tables are ensured over the operating range of the integrated circuit unless otherwise specified. Typical characteristics specify mean values expected over the production spread. If not otherwise specified, typical characteristics apply at $T_A = 25\,^{\circ}\text{C}$ and the given supply voltage.



Table 5 Electrical Characteristics TLE 4284 DV (adjustable output voltage)

-40 °C < $T_{\rm j}$ < 150 °C; $V_{\rm l}$ - $V_{\rm Q}$ = 13.5 V, $I_{\rm Q}$ = 10 mA; unless otherwise specified

Parameter	Sym-	Lir	nit Val	ues	Unit	Measuring Conditions	
	bol	min.	typ.	max.			
Reference voltage	$V_{REF}^{-1)}$	1.20	1.25	1.30	V	_	
Line regulation	ΔV_{Q}	_	0.5	1.50	% ²⁾	$3 \text{ V} \le (V_{\text{I}} - V_{\text{Q}}) \le 40 \text{ V}$	
Load regulation	ΔV_{Q}	_	0.2	0.4	% ²⁾	10 mA \leq $I_{\rm Q}$ \leq 800 mA; ⁴⁾ $V_{\rm I}$ = 3.0 V; $V_{\rm Q}$ = $V_{\rm REF}$	
		_	0.25	0.5	% 2)	10 mA $\leq I_{\rm Q} \leq$ 1.0 A; ⁴⁾ $V_{\rm I} =$ 3.0 V; $V_{\rm Q} = V_{\rm REF}$	
Dropout voltage	V_{DR}	_	1.00	1.20	V	$I_{\rm Q}$ = 100 mA ³⁾	
		_	1.05	1.30	٧	$I_{\rm Q}$ = 500 mA $^{3)}$	
		_	1.10	1.35	V	$I_{\rm Q}$ = 800 mA ³⁾	
		_	1.30	1.40	V	$I_{\rm Q}$ = 1.0 A $^{3)}$	
Current consumption $I_q = I_l - I_Q$	I_{q}	_	100	120	μΑ	$I_{\rm Q}$ = 10 mA;	
Adjust current	I_{ADJ}	_	75	120	μΑ	$I_{\rm Q}$ = 10 mA	
Adjust current change	ΔI_{ADJ}	_	2	5	μΑ	$I_{\rm Q}$ = 10 mA 3 V \leq ($V_{\rm I}$ - $V_{\rm Q}$) \leq 40 V $^{4)}$	
		_	2	5	μΑ	10 mA \leq $I_{\rm Q}$ \leq 200 mA; $V_{\rm I}$ - $V_{\rm Q}$ = 3 V $^{4)}$	
Temperature stability	_	_	0.6	_	%	5)	
Minimum load current ⁶⁾	I_{Q}	_	1	5	mA	$V_{\rm I}$ < 40 V; $V_{\rm Q}$ = $V_{\rm REF}$	
Current limit	I_{Qmax}	1000	_	2200	mA	$1.4 \text{V} < V_{\text{I}} - V_{\text{Q}} < 18 \text{V};$ $V_{\text{Q}} = V_{\text{nom}} - 100 \text{ mV}$	
		50	200	_	mA	$V_{\rm I}$ = 40 V; $V_{\rm Q}$ = $V_{\rm nom}$ -100 mV $T_{\rm I}$ = 25 °C	
RMS Output Noise	_	_	30	_	ppm	ppm of $V_{\rm Q}$; $T_{\rm j}$ = 25 °C; 10 Hz $\leq f \leq$ 10kHz ⁵⁾	



Table 5 Electrical Characteristics TLE 4284 DV (adjustable output voltage)

-40 °C < $T_{\rm i}$ < 150 °C; $V_{\rm l}$ - $V_{\rm O}$ = 13.5 V, $I_{\rm O}$ = 10 mA; unless otherwise specified

Parameter	Sym-	Liı	Limit Values			Measuring Conditions
	bol	min.	typ.	max.		
Power Supply Ripple Rejection	PSRR	_	65	_	dB	$V_{\rm Q}$ = 10 V, $f_{\rm r}$ = 120 Hz, $V_{\rm r}$ = 0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 0 μ F ⁵⁾
		_	65	_	dB	$V_{\rm Q}$ = 10 V, $f_{\rm r}$ = 120 Hz, $V_{\rm r}$ = 0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 10 μ F ⁵⁾

 $^{1) \}quad V_{\mathsf{REF}} = V_{\mathsf{Q}} - V_{\mathsf{ADJ}}$

- 4) Constant Junction Temperature
- 5) Not subject to production test specified by design.
- 6) Minimum Output Current to maintain regulation

Table 6 Electrical Characteristics TLE 4284 DV15 (1.5 V fixed output voltage)

-40 °C < $T_{\rm j}$ < 150 °C; $V_{\rm l}$ = 13.5 V, $I_{\rm Q}$ = 10 mA; unless otherwise specified

Parameter	Symbol	Limit Values		Unit	Measuring Conditions	
		min.	typ.	max.		
Output voltage	V_{Q}	1.45	1.5	1.55	V	10 mA $\leq I_{\rm Q} \leq$ 1000 mA; 2.9 V $\leq V_{\rm I} \leq$ 16 V
		_	1.5	_	V	10 mA $\leq I_{\rm Q} \leq$ 1000 mA; 16 V $\leq V_{\rm I} \leq$ 40 V ¹⁾
Line regulation	ΔV_{Q}	_	4.8	22.5	mV	$2.9 \text{ V} \le V_1 \le 40 \text{ V}$
Load regulation	ΔV_{Q}	_	2.6	5.2	mV	10 mA $\leq I_{\rm Q} \leq$ 800 mA; ²⁾ $V_{\rm I} = V_{\rm Qnom} + V_{\rm DR}$
		_	3.1	6.25	mV	10 mA \leq $I_{\rm Q}$ \leq 1.0 A $^{2)}$ $V_{\rm I}$ = $V_{\rm Qnom}$ + $V_{\rm DR}$
Dropout voltage	V_{DR}	_	1.00	1.20	V	$I_{\rm Q}$ = 100 mA ³⁾
		_	1.05	1.30	V	$I_{\rm Q}$ = 500 mA ³⁾
		_	1.10	1.35	V	$I_{\rm Q}$ = 800 mA ³⁾
		_	1.30	1.40	V	$I_{\rm Q}$ = 1.0 A ³⁾
Current consumption $I_{q} = I_{l} - I_{Q}$	I_{q}	_	0.8	1.6	mA	$I_{\rm Q}$ = 10 mA
Temperature stability	_	_	8.8	_	mV	4)

²⁾ Related to V_{Q} measured at constant junction Temperature

³⁾ Dropout voltage measured when the output voltage has dropped 100 mV from the nominal value obtained at $V_{\rm Q} = V_{\rm REF}$.



Table 6 Electrical Characteristics TLE 4284 DV15 (1.5 V fixed output voltage)

-40 °C < $T_{\rm i}$ < 150 °C; $V_{\rm l}$ = 13.5 V, $I_{\rm Q}$ = 10 mA; unless otherwise specified

Parameter	Symbol	Limit Values		Unit	Measuring Conditions	
		min.	typ.	max.		
Current limit	I_{Qmax}	1000	_	2200	mA	$\begin{aligned} V_{\mathrm{I}} - V_{\mathrm{Q}} < &18 \mathrm{V}; \\ V_{\mathrm{Q}} = V_{\mathrm{nom}} - &100 \ \mathrm{mV} \end{aligned}$
		50	200	_	mA	$V_{\rm I}$ = 40 V; $V_{\rm Q}$ = $V_{\rm nom}$ - 100 mV $T_{\rm j}$ = 25 °C
RMS Output Noise	_	_	30		ppm	ppm of $V_{\rm Q}$, $T_{\rm j}$ = 25 °C 10 Hz \leq f \leq 10 kHz $^{4)}$
Power Supply Ripple Rejection	PSRR	_	65	_	dB	$f_{\rm r}$ = 120 Hz, $V_{\rm r}$ =0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 0 μ F ⁴⁾
		_	65	_	dB	$f_{\rm r}$ = 120 Hz, $V_{\rm r}$ =0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 10 μ F ⁴⁾

¹⁾ Device is usable within given range without destruction, but the accuracy of the output voltage can only be guarantied in the range specified in the line above.

Table 7 Electrical Characteristics TLE 4284 DV18 (1.8 V fixed output voltage) -40 °C < $T_{\rm j}$ < 150 °C; $V_{\rm l}$ = 13.5 V, $I_{\rm Q}$ = 10 mA; unless otherwise specified

Parameter Symbol **Limit Values** Unit **Measuring Conditions** min. typ. max. V_{Q} ٧ 10 mA $\leq I_{Q} \leq$ 1000 mA; Output voltage 1.75 1.8 1.85 $3.2 \text{ V} \le V_1 \le 16 \text{ V}$ 10 mA $\leq I_{\rm O} \leq$ 1000 mA; ٧ 1.8 16 $V \le V_1 \le 40 V^{-1}$ $3.2 \text{ V} \le V_1 \le 40 \text{ V}$ 7.2 27 Line regulation $\Delta V_{\rm O}$ mV 10 mA $\leq I_{\rm O} \leq$ 800 mA ²⁾ mV Load regulation $\Delta V_{\rm O}$ 3.4 7.6 $V_{\rm I} \!\!=\!\! V_{\rm Qnom} + V_{\rm DR}$ 10 mA $\leq I_Q \leq$ 1.0 A ²⁾ 9 mV 4.8 $V_{\mathsf{I}} = V_{\mathsf{Qnom}} + V_{\mathsf{DR}}$

²⁾ Measured at constant junction temperature

³⁾ Dropout voltage measured when the output voltage has dropped 100 mV from the nominal value.

⁴⁾ Not subject to production test - specified by design.



Table 7 Electrical Characteristics TLE 4284 DV18 (1.8 V fixed output voltage)

-40 °C < $T_{\rm i}$ < 150 °C; $V_{\rm i}$ = 13.5 V, $I_{\rm Q}$ = 10 mA; unless otherwise specified

Parameter	Symbol	Lir	nit Valu	ies	Unit	Measuring Conditions
		min.	typ.	max.		
Dropout voltage	V_{DR}	_	1.00	1.20	V	$I_{\rm Q}$ = 100 mA $^{3)}$
		_	1.05	1.30	V	$I_{\rm Q}$ = 500 mA $^{3)}$
		_	1.10	1.35	V	$I_{\rm Q}$ = 800 mA ³⁾
		_	1.30	1.40	V	$I_{\rm Q}$ = 1.0 A $^{3)}$
$\overline{\text{Current consumption}}$ $I_{q} = I_{l} - I_{Q}$	I_{q}	_	0.8	1.6	mA	$I_{\rm Q}$ = 10 mA
Temperature stability	_	_	11	_	mV	4)
Current limit	I_{Qmax}	1000	_	2200	mA	$\begin{aligned} V_{\rm I} - V_{\rm Q} &< 18 \mathrm{V}; \\ V_{\rm Q} &= V_{\rm nom} - 100 \; \mathrm{mV} \end{aligned}$
		50	200	_	mA	$V_{\rm I}$ = 40 V; $V_{\rm Q}$ = $V_{\rm nom}$ - 100 mV $T_{\rm j}$ = 25 °C
RMS Output Noise	_	_	30	_	ppm	ppm of $V_{\rm Q}$, $T_{\rm j}$ = 25 °C 10 Hz \leq f \leq 10 kHz $^{4)}$
Power Supply Ripple Rejection	PSRR	_	65	_	dB	$f_{\rm r}$ = 120 Hz; $V_{\rm r}$ = 0.5 $V_{\rm PP}$ $C_{\rm ADJ}$ = 0 $\mu{\rm F}^{4)}$
		_	65	_	dB	$f_{\rm r}$ = 120 Hz; $V_{\rm r}$ = 0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 10 $\mu{\rm F}^{4)}$

¹⁾ Device is usable within given range without destruction, but the accuracy of the output voltage can only be guarantied in the range specified in the line above.

Table 8 Electrical Characteristics TLE 4284 DV26 (2.6 V fixed output voltage)

-40 °C < $T_{\rm j}$ < 150 °C; $V_{\rm l}$ = 13.5 V, $I_{\rm Q}$ = 10 mA; unless otherwise specified

Parameter	Symbol	Limit Values			Unit	Measuring Conditions
		min.	typ.	max.		
Output voltage	V_{Q}	2.52	2.60	2.68	V	10 mA $\leq I_{\rm Q} \leq$ 1000 mA; 4.0 V $\leq V_{\rm I} \leq$ 16 V
		_	2.60	_	V	10 mA $\leq I_{\rm Q} \leq$ 1000 mA; 16 V $\leq V_{\rm I} \leq$ 40 V ¹⁾

²⁾ Measured at constant junction temperature

³⁾ Dropout voltage measured when the output voltage has dropped 100 mV from the nominal value.

⁴⁾ Not subject to production test - specified by design.



Table 8 Electrical Characteristics TLE 4284 DV26 (2.6 V fixed output voltage)

-40 °C < $T_{\rm i}$ < 150 °C; $V_{\rm l}$ = 13.5 V, $I_{\rm O}$ = 10 mA; unless otherwise specified

Parameter	Symbol	Lir	nit Valu	ıes	Unit	Measuring Conditions
		min.	typ.	max.		
Line regulation	ΔV_{Q}	_	11	40	mV	$4.0 \text{ V} \le V_1 \le 40 \text{ V}$
Load regulation	$\Delta V_{ m Q}$	_	5	11	mV	10 mA \leq $I_{\rm Q}$ \leq 800 mA; ²⁾ $V_{\rm I}$ = $V_{\rm Qnom}$ + $V_{\rm DR}$
		_	7	13	mV	$ \begin{array}{c} \mbox{10 mA} \leq I_{\rm Q} \leq \mbox{1.0 A}^{\mbox{2}} \\ V_{\rm I} = V_{\rm Qnom} + V_{\rm DR} \\ \end{array} $
Dropout voltage	V_{DR}	_	1.00	1.20	V	$I_{\rm Q}$ = 100 mA ³⁾
		_	1.05	1.30	V	$I_{\rm Q}$ = 500 mA ³⁾
		_	1.10	1.35	V	$I_{\rm Q}$ = 800 mA $^{3)}$
		_	1.30	1.40	V	$I_{\rm Q}$ = 1.0 A ³⁾
Current consumption; $I_{q} = I_{l} - I_{Q}$	I_{q}	_	0.8	1.6	mA	$I_{\rm Q}$ = 10 mA
Temperature stability	_	_	16	_	mV	4)
Current limit	I_{Qmax}	1000	_	2200	mA	$\begin{aligned} V_{\rm I} - V_{\rm Q} < &18 \text{V}; \\ V_{\rm Q} = V_{\rm nom} - &100 \text{ mV} \end{aligned}$
		50	200	_	mA	$V_{\rm I}$ = 40 V; $V_{\rm Q}$ = $V_{\rm nom}$ - 100 mV $T_{\rm j}$ = 25 °C
RMS Output Noise	_	_	30	_	ppm	ppm of $V_{\rm Q}$, $T_{\rm j}$ = 25 °C 10 Hz \leq f \leq 10 kHz $^{4)}$
Power Supply Ripple Rejection	PSRR	_	65	_	dB	$f_{\rm r}$ = 120 Hz, $V_{\rm r}$ =0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 0 μ F ⁴⁾
		_	65	_	dB	$f_{\rm r}$ = 120 Hz, $V_{\rm r}$ =0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 10 μ F $^{4)}$

¹⁾ Device is usable within given range without destruction, but the accuracy of the output voltage can only be guarantied in the range specified in the line above.

²⁾ Measured at constant junction temperature

³⁾ Dropout voltage measured when the output voltage has dropped 100 mV from the nominal value.

⁴⁾ Not subject to production test - specified by design.



Table 9 Electrical Characteristics TLE 4284 DV33 (3.3 V fixed output voltage)

-40 °C < $T_{\rm i}$ < 150 °C; $V_{\rm i}$ = 13.5 V, $I_{\rm Q}$ = 10 mA; unless otherwise specified

Parameter	Symbol	Limit Values			Unit	Measuring Conditions
		Min.	Тур.	Max.		
Output voltage	V_{Q}	3.20	3.3	3.40	V	10 mA $\leq I_{\rm Q} \leq$ 1000 mA; 4.7 V $\leq V_{\rm I} \leq$ 16 V
		_	3.3	_	V	10 mA \leq $I_{\rm Q}$ \leq 1000 mA ; 16 V \leq $V_{\rm I}$ \leq 40 V $^{1)}$
Line regulation	ΔV_{Q}	_	15	50	mV	$4.7 \text{ V} \le V_{\text{I}} \le 40 \text{ V}$
Load regulation	ΔV_{Q}	_	6	13	mV	10 mA \leq $I_{\rm Q}$ \leq 800 mA $^{2)}$ $V_{\rm I}$ = $V_{\rm Qnom}$ + $V_{\rm DR}$
		_	8	16	mV	$ \begin{array}{c} \mbox{10 mA} \leq I_{\rm Q} \leq \mbox{1.0 A}^{\mbox{2}} \\ V_{\rm I} = V_{\rm Qnom} + V_{\rm DR} \\ \end{array} $
Dropout voltage	V_{DR}	_	1.00	1.20	٧	$I_{\rm Q}$ = 100 mA ³⁾
		_	1.05	1.30	٧	$I_{\rm Q}$ = 500 mA ³⁾
		_	1.10	1.35	V	$I_{\rm Q}$ = 800 mA ³⁾
		_	1.30	1.40	٧	$I_{\rm Q}$ = 1.0 A $^{3)}$
Current consumption $I_{q} = I_{l} - I_{Q}$	I_{q}	_	0.8	1.6	mA	I _Q = 10 mA
Temperature stability	_	_	20	_	mV	4)
Current limit	I_{Qmax}	1000		2200	mA	$\begin{aligned} V_{\rm I} - V_{\rm Q} &< 18 \mathrm{V}; \\ V_{\rm Q} &= V_{\rm nom} - 100 \; \mathrm{mV} \end{aligned}$
		50	200	_	mA	$V_{\rm I}$ = 40 V; $V_{\rm Q}$ = $V_{\rm nom}$ - 100 mV $T_{\rm j}$ = 25 °C
RMS Output Noise	_	_	30	_	ppm	ppm of $V_{\rm Q}$; $T_{\rm j}$ = 25 °C; 10 Hz \leq f \leq 10 kHz ⁴⁾
Power Supply Ripple Rejection	PSRR	_	65	_	dB	$f_{\rm r}$ = 120 Hz; $V_{\rm r}$ = 0.5 Vpp; $C_{\rm ADJ}$ = 0 $\mu {\rm F}^{4)}$
		_	65	_	dB	$f_{\rm r}$ = 120 Hz; $V_{\rm r}$ = 0.5 Vpp; $C_{\rm ADJ}$ = 10 $\mu{\rm F}^{4)}$

¹⁾ Device is usable within given range without destruction, but the accuracy of the output voltage can only be guarantied in the range specified in the line above.

²⁾ Measured at constant junction temperature.

³⁾ Dropout voltage measured when the output voltage has dropped 100 mV from the nominal value.

⁴⁾ Not subject to production test - specified by design.



Table 10 Electrical Characteristics TLE 4284 DV50 (5.0 V fixed output voltage) -40 °C < T_i < 150 °C; V_i = 13.5 V, I_O = 10 mA; unless otherwise specified

Parameter	Symbol	Limit Values			Unit	Measuring Conditions
		min.	typ.	max.		
Output voltage	V_{Q}	4.85	5.00	5.15	V	10 mA $\leq I_{\rm Q} \leq$ 1000 mA; 6.4 V $\leq V_{\rm I} \leq$ 16 V
		_	5.00	_	V	10 mA $\leq I_{\rm Q} \leq$ 1000 mA; 16V $\leq V_{\rm I} \leq$ 40 V ¹⁾
Line regulation	ΔV_{Q}	_	20	75	mV	$6.4 \text{ V} \le V_{\text{I}} \le 40 \text{ V}$
Load regulation	ΔV_{Q}	_	9	20	mV	$10~\text{mA} \leq I_{\text{Q}} \leq 800~\text{mA}^{2)}$ $V_{\text{I}} = V_{\text{Qnom}} + V_{\text{DR}}$
		_	12	24	mV	$ \begin{array}{c} \mbox{10 mA} \leq I_{\rm Q} \leq \mbox{1.0 A}^{\mbox{2}} \\ V_{\rm I} = V_{\rm Qnom} + V_{\rm DR} \\ \end{array} $
Dropout voltage	V_{DR}	_	1.00	1.20	V	$I_{\rm Q}$ = 100 mA ³⁾
		_	1.05	1.30	٧	$I_{\rm Q}$ = 500 mA ³⁾
		_	1.10	1.35	٧	$I_{\rm Q}$ = 800 mA ³⁾
		_	1.30	1.40	V	$I_{\rm Q}$ = 1.0 A $^{3)}$
Current consumption $I_{q} = I_{l} - I_{Q}$	I_{q}	_	0.8	1.6	mA	I _Q = 10 mA
Temperature stability	_	_	30	_	mV	4)
Current limit	I_{Qmax}	1000	_	2200	mA	$\begin{aligned} V_{\rm I} - V_{\rm Q} < &18 \text{V}; \\ V_{\rm Q} = V_{\rm nom} - &100 \text{ mV} \end{aligned}$
		50	200	_	mA	$V_{\rm I}$ = 40 V; $V_{\rm Q}$ = $V_{\rm nom}$ - 100 mV $T_{\rm j}$ = 25 °C
RMS Output Noise	_	_	30	_	ppm	ppm of $V_{\rm Q}$, $T_{\rm j}$ = 25 °C 10 Hz \leq f \leq 10 kHz $^{4)}$
Power Supply Ripple Rejection	PSRR	_	65	_	dB	$f_{\rm r}$ = 120 Hz, $V_{\rm r}$ =0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 0 $\mu{\rm F}^{4)}$
		_	65	_	dB	$f_{\rm r}$ = 120 Hz, $V_{\rm r}$ =0.5 $V_{\rm PP}$, $C_{\rm ADJ}$ = 10 μ F ⁴⁾

¹⁾ Device is usable within given range without destruction, but the accuracy of the output voltage can only be guarantied in the range specified in the line above.

²⁾ Measured at constant junction temperature

³⁾ Dropout voltage measured when the output voltage has dropped 100 mV from the nominal value.

⁴⁾ Not subject to production test - specified by design.



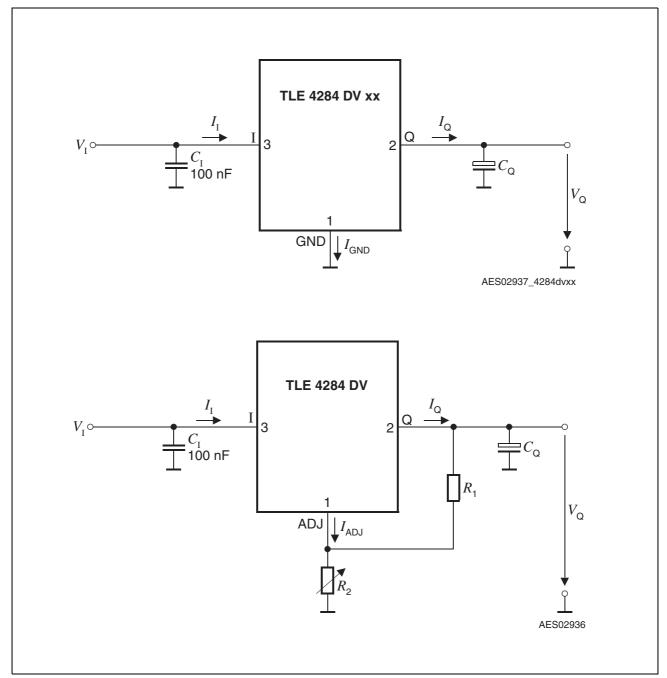


Figure 3 Measuring Circuit of fixed output voltage versions and adjustable output voltage version



Application Information

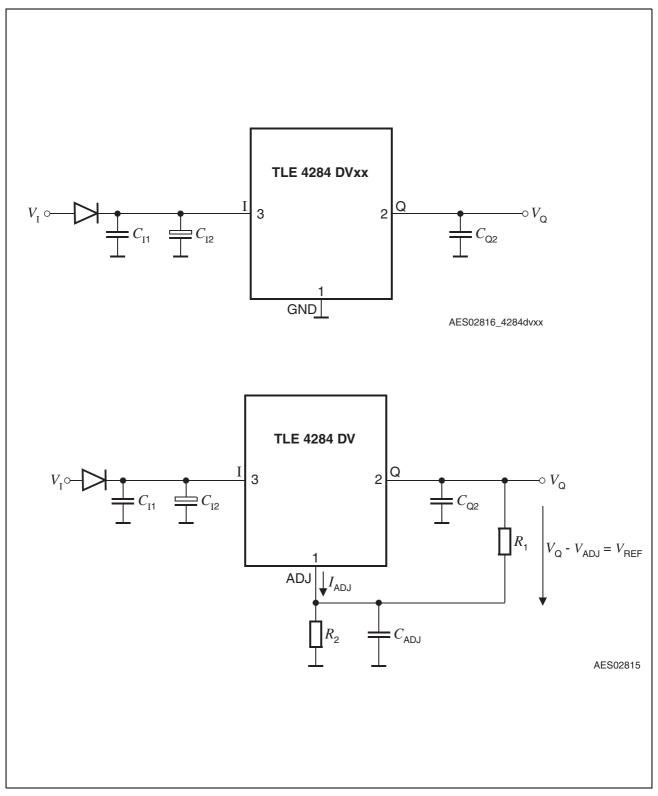
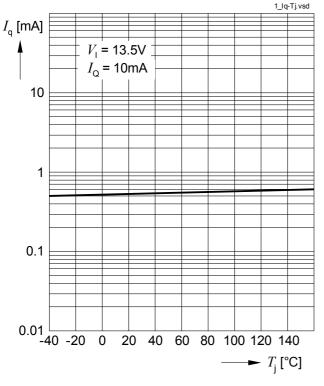


Figure 4 Typical application circuit of fixed output voltage versions and adjustable output voltage version

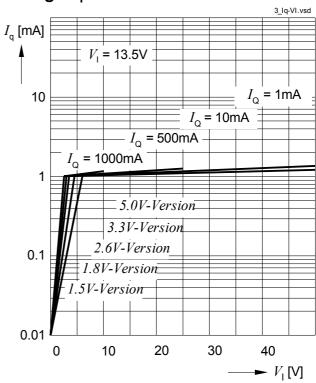


Typical Performance Characteristics

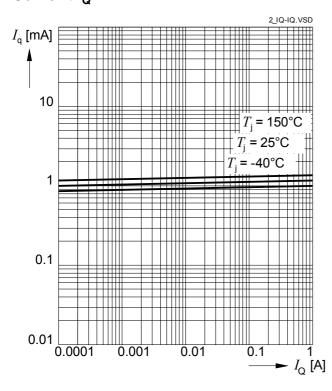
Current Consumption I_q versus Junction Temperature T_i



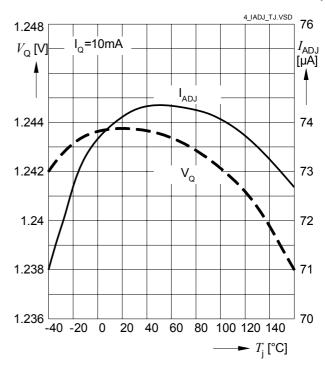
Current Consumption I_q versus Input Voltage V_I



Current Consumption I_q versus Output Current I_Q



Adjust Current I_{ADJ} and Reference Voltage V_{Ref} vs Junction Temperature T_j

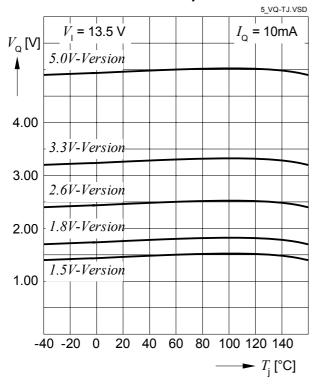




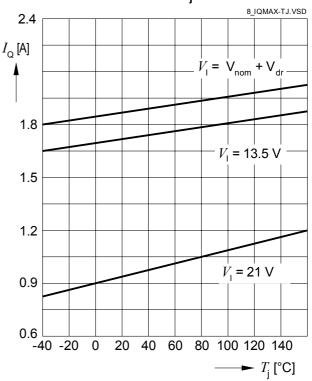
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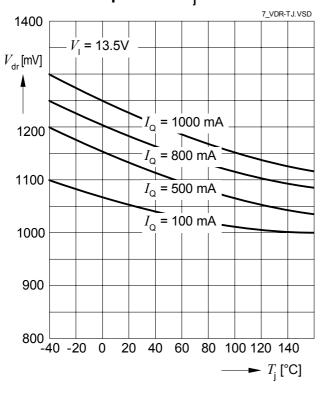
Output Voltage V_Q versus Junction Temperature T_j



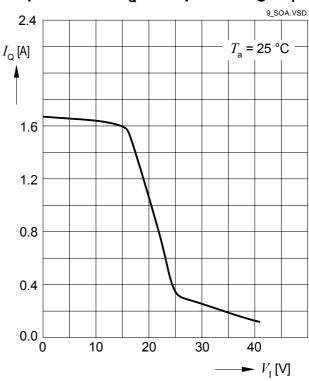
Output Current Limit I_{Qmax} versus Junction Temperature T_i



Dropout Voltage V_{DR} versus Junction Temperature T_i

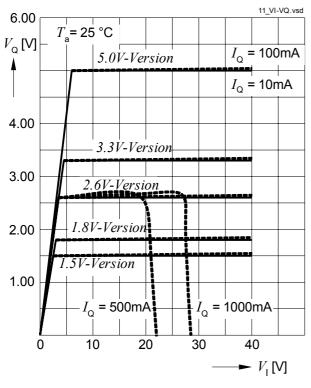


Safe Operation Area (SOA): Output Current I_O vs. Input Voltage V_I

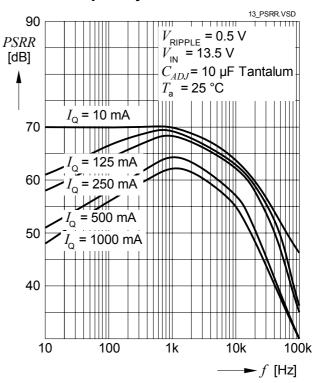




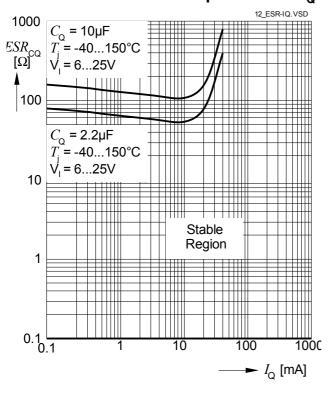
Output Voltage V_Q versus Input Voltage V_I



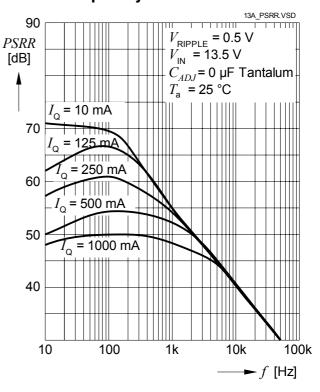
Power Supply Ripple Rejection versus Frequency



Stability Region: Equivalent Serial Resistor ESR versus Output Current I_O

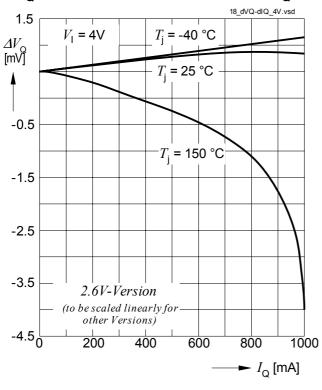


Power Supply Ripple Rejection versus Frequency

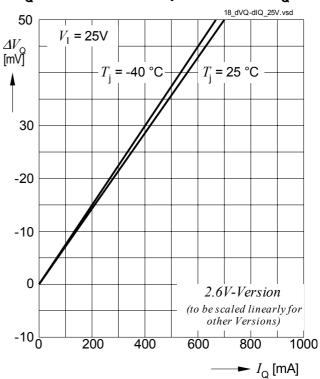




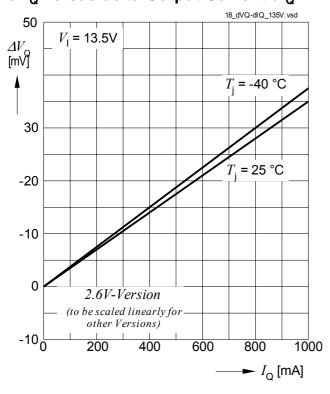
Load Regulation: Delta Output Voltage dV_O versus delta Output Current dI_O



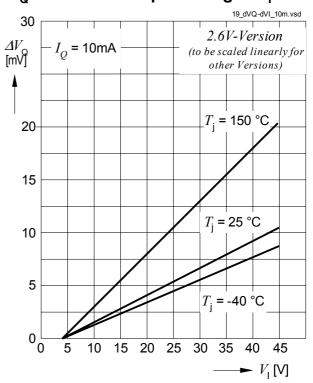
Load Regulation: Delta Output Voltage dV_O versus delta Output Current dI_O



Load Regulation: Delta Output Voltage dV_Q versus delta Output Current dI_Q

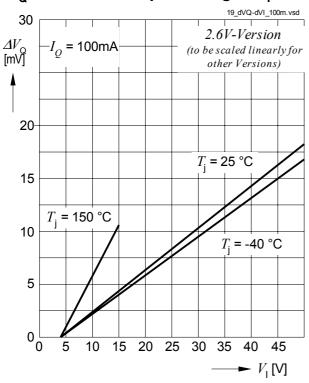


Line Regulation: Delta Output Voltage dV_O versus delta Input Voltage dV_I





Line Regulation: Delta Output Voltage dV_Q versus delta Input Voltage dV_I





Application Hints

Adjustable Version

At the fixed voltage TLE 4284 devices, the output voltage is divided internally and compared to an internal reference of 1.25 V typical. The regulation loop controls the output voltage to achieve the output voltage of 5 V, 3.3 V, 2.6 V, 1.8V or 1.5V. The variable version compares the voltage difference between the adjust pin ADJ and the output pin Q to the internal reference of typically 1.25 V. The output voltage is adjusted by an external voltage divider between Q, ADJ and GND and calculates:

$$V_{Q} = V_{REF} \times \left(1 + \frac{R_2}{R_1}\right) + I_{ADJ} \times R_2$$

For the variable regulator TLE 4284 DV, a minimum load current of 5 mA is necessary in order to keep the output voltage regulated. If the application does not assure this minimum load requirement, the output voltage divider should be dimensioned sufficiently low-ohmic: $R_1 \le 240~\Omega$.

For the variable voltage type an additional decoupling a capacitor C_{ADJ} at the adjust pin improves the ripple rejection ratios. Placing C_{ADJ} requires an increased output capacitance of $C_{\text{O}} \ge 22~\mu\text{F}$.

Output

The output current limitation is reduced as a function of the input voltage for high input voltages above 25 V.

The TLE 4284 requires a 10 μ F output capacitor with 0.1 Ω \leq ESR \leq 10 Ω for the stability of the regulation loop.

At the input of the regulator a capacitor is necessary for compensation of line influences. A serial diode should be used to eliminate negative voltages from the input. As a minimum, a 100 nF ceramic input capacitor should be used. If the regulator is used in an environment with long input lines, an input capacitance of 10 μ F is recommended.



Package Outlines

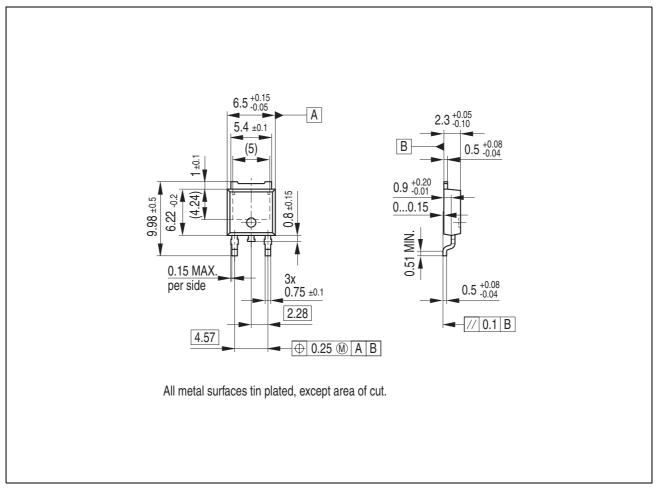


Figure 5 Dimensions PG-TO252-3-11

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

Find all packages, sorts of packing and others at the Infineon Internet Page: http://www.infineon.com/packages.

SMD = Surface Mounted Device

Dimensions in mm



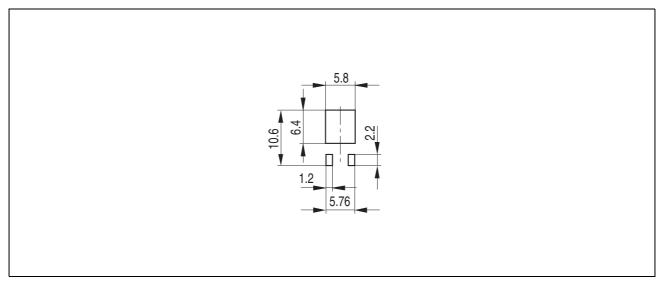


Figure 6 Footprint for PG-TO252-3-11

Find all packages, sorts of packing and others at the Infineon Internet Page: http://www.infineon.com/packages.

SMD = Surface Mounted Device

Dimensions in mm



Revision History

Version	Date	Changes	
Rev. 2.0	2006-02-13	Page 1: 1.5 V fixed voltage version changed to final status. Page 1: Ordering Codes updated. Table 1, 2: Low ESR requirement for C _Q removed. Table 3: Max. Ratings: ESD Susceptibility Human Body Model improved to 4 kV. Several: Typo and formatting corrections.	
Rev. 2.1	2007-03-20	Initial version of RoHS-compliant derivate of TLE 4284 Page 1: AEC certified statement added Page 1 and Page 22: RoHS compliance statement and Green product feature added Page 1 and Page 22: Package changed to RoHS compliant version Legal Disclaimer updated	

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