



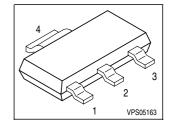


Features

- Logic Level Input
- Input Protection (ESD)
- Thermal shutdown with auto restart
- Green product (RoHS compliant)
- Overload protection
- Short circuit protection
- Overvoltage protection
- Current limitation
- Analog driving possible

Product Summary

Drain source voltage	$V_{\rm DS}$	42	V
On-state resistance	R _{DS(on)}	50	mΩ
Nominal load current	I _{D(Nom)}	3	Α
Clamping energy	E _{AS}	500	mJ

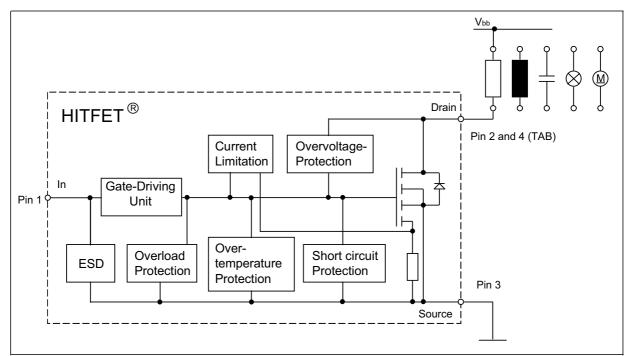


Application

- All kinds of resistive, inductive and capacitive loads in switching or linear applications
- μC compatible power switch for 12 V DC applications
- Replaces electromechanical relays and discrete circuits

General Description

N channel vertical power FET in Smart SIPMOS® technology. Fully protected by embedded protection functions.



Complete product spectrum and additional information http://www.infineon.com/hitfet



Maximum Ratings at T_i = 25°C, unless otherwise specified

Parameter	Symbol	Value	Unit	
Drain source voltage	V _{DS}	42	V	
Supply voltage for full short circuit protection	V _{bb(SC)}	42		
Continuous input voltage ¹⁾	V _{IN}	-0.2 ²⁾ +10		
Continuous input current ²⁾	I _{IN}		mA	
$-0.2V \le V_{IN} \le 10V$		self limited		
$V_{\rm IN}$ < -0.2V or $V_{\rm IN}$ > 10V		<i>I</i> _{IN} ≤ 2		
Operating temperature	$T_{\rm i}$	-40+150	°C	
Storage temperature	T _{stg}	-55 + 150		
Power dissipation ⁵⁾	P_{tot}	3.8	W	
<i>T</i> _C = 85 °C				
Unclamped single pulse inductive energy ²⁾	E _{AS} 500		mJ	
Load dump protection $V_{\text{LoadDump}}^{(2)3)} = V_{\text{A}} + V_{\text{S}}$	V_{LD}	53.5	V	
$V_{\rm IN}$ = 0 and 10 V, t _d = 400 ms, $R_{\rm I}$ = 2 Ω ,				
$R_{L} = 4.5 \Omega$, $V_{A} = 13.5 V$				
Electrostatic discharge voltage ²⁾ (Human Body Model)	V _{ESD}	2	kV	
according to Jedec norm				
EIA/JESD22-A114-B, Section 4				

Thermal resistance

junction - ambient:	R_{thJA}		K/W
@ min. footprint		125	
@ 6 cm ² cooling area ⁴⁾		72	
junction-soldering point:	R _{thJS}	17	K/W

 $^{^{1}}$ For input voltages beyond these limits I_{1N} has to be limited.

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²not subject to production test, specified by design

 $^{^3}V_{Loaddump}$ is setup without the DUT connected to the generator per ISO 7637-1 and DIN 40839

 $^{^4}$ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thick) copper area for drain connection. PCB mounted vertical without blown air.

 $^{^{5}}$ not subject to production test, calculated by $\rm R_{thJA}$ and $\rm R_{ds(on)}$



Electrical Characteristics

Parameter	Symbol	Values			Unit	
at T_i = 25°C, unless otherwise specified		min.	typ.	max.		
Characteristics			•			
Drain source clamp voltage	$V_{\rm DS(AZ)}$	42	-	55	V	
$T_{\rm j}$ = - 40+ 150, $I_{\rm D}$ = 10 mA						
Off-state drain current	I _{DSS}				μΑ	
$T_{\rm j}$ = -40+85 °C, $V_{\rm DS}$ = 32 V , $V_{\rm IN}$ = 0 V		-	1.5	8		
<i>T</i> _j = 150 °C		-	5	15		
Input threshold voltage	V _{IN(th)}				V	
$I_{\rm D}$ = 1.4 mA, $T_{\rm j}$ = 25 °C		1.3	1.7	2.2		
$I_{\rm D}$ = 1.4 mA, $T_{\rm j}$ = 150 °C		0.8	-	-		
On state input current	I _{IN(on)}	-	10	30	μΑ	
On-state resistance	R _{DS(on)}				$m\Omega$	
$V_{IN} = 5 \text{ V}, I_D = 3 \text{ A}, T_j = 25 ^{\circ}\text{C}$		-	45	60		
V_{IN} = 5 V, I_{D} = 3 A, T_{j} = 150 °C		-	75	100		
On-state resistance	R _{DS(on)}					
V_{IN} = 10 V, I_{D} = 3 A, T_{j} = 25 °C		_	35	50		
V_{IN} = 10 V, I_{D} = 3 A, T_{j} = 150 °C		_	65	90		
Nominal load current ⁵⁾	I _{D(Nom)}	3	4	-	Α	
$V_{\rm DS}$ = 0.5 V, $T_{\rm j}$ < 150°C, $V_{\rm IN}$ = 10 V, $T_{\rm A}$ = 85 °C						
Current limit (active if V _{DS} >2.5 V) ¹⁾	I _{D(lim)}	18	24	30		
V_{IN} = 10 V, V_{DS} = 12 V, t_{m} = 200 μ s						

¹Device switched on into existing short circuit (see diagram Determination of $b_{(lim)}$). If the device is in on condit and a short circuit occurs, these values might be exceeded for max. 50 μ s.

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 $^{^{5}}$ not subject to production test, calculated by $\rm R_{thJA}$ and $\rm R_{ds(on)}$



Electrical Characteristics

Parameter	Symbol	Values			Unit
at T_j = 25°C, unless otherwise specified		min.	typ.	max.	
Dynamic Characteristics					
Turn-on time V_{IN} to 90% I_{D} : $R_{L} = 4.7 \Omega$, $V_{IN} = 0$ to 10 V, $V_{bb} = 12 V$	t _{on}	-	60	100	μs
Turn-off time V_{IN} to 10% I_{D} : $R_{\text{L}} = 4.7 \ \Omega$, $V_{\text{IN}} = 10 \text{ to } 0 \text{ V}$, $V_{\text{bb}} = 12 \text{ V}$	t _{off}	-	60	100	
Slew rate on 70 to 50% V_{bb} : $R_{L} = 4.7 \Omega$, $V_{IN} = 0$ to 10 V, $V_{bb} = 12 \text{ V}$	-dV _{DS} /dt _{on}	-	0.3	1.5	V/µs
Slew rate off 50 to 70% V_{bb} : $R_{L} = 4.7 \Omega$, $V_{IN} = 10$ to 0 V, $V_{bb} = 12$ V	dV _{DS} /dt _{off}	1	0.7	1.5	
Protection Functions ¹⁾					
Thermal overload trip temperature	T_{it}	150	175	-	°C
Thermal hysteresis ²⁾	$\Delta T_{\rm it}$	-	10	-	K
Input current protection mode T _i = 150 °C	I _{IN(Prot)}	-	130	300	μA
Unclamped single pulse inductive energy ²⁾ $I_D = 3 \text{ A}, T_j = 25 ^{\circ}\text{C}, V_{bb} = 12 \text{ V}$	E _{AS}	500	-	-	mJ
Inverse Diode					
Inverse diode forward voltage	V _{SD}	-	1	1.5	V

¹Integrated protection functions are designed to prevent IC destruction under fault conditions described in the data sheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous repetitive operation.

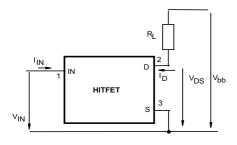
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²not subject to production test, specified by design

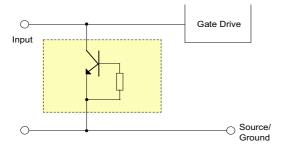


Block diagram

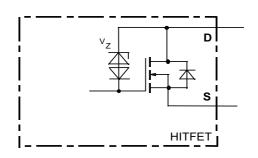
Terms



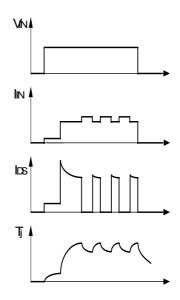
Input circuit (ESD protection)



Inductive and overvoltage output clamp



Short circuit behaviour

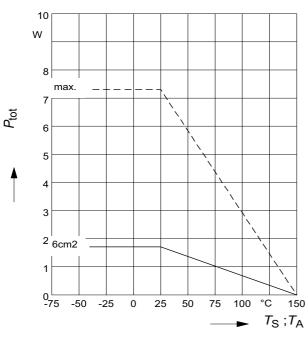




1 Maximum allowable power dissipation

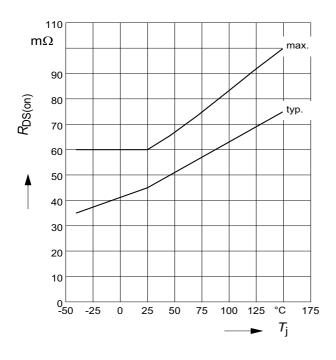
 $P_{tot} = f(T_S)$ resp.

 $P_{tot} = f(T_A) @ R_{thJA} = 72 \text{ K/W}$



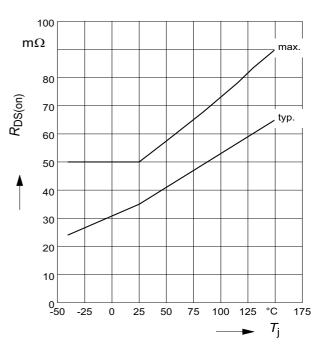
3 On-state resistance

 $R_{ON} = f(T_i); I_D = 3A; V_{IN} = 5V$



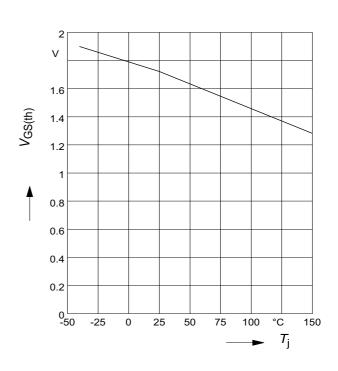
2 On-state resistance

 $R_{ON} = f(T_i); I_D = 3A; V_{IN} = 10V$



4 Typ. input threshold voltage

 $V_{IN(th)} = f(T_j); I_D = 0.7 \text{ mA}; V_{DS} = 12V$

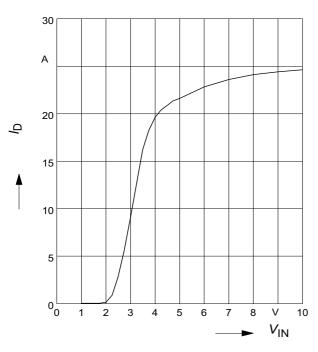


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5 Typ. transfer characteristics

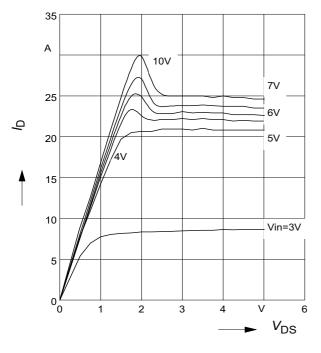
 $I_D=f(V_{IN}); V_{DS}=12V; T_{Jstart}=25^{\circ}C$



7 Typ. output characteristics

 I_D =f(V_{DS}); T_{Jstart} =25°C

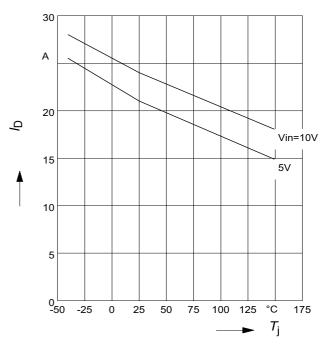
Parameter: V_{IN}



6 Typ. short circuit current

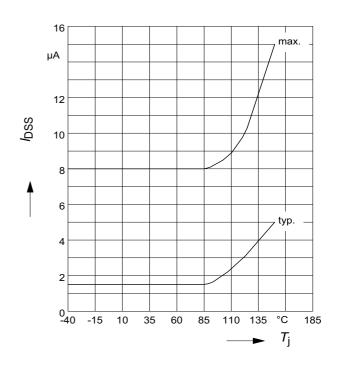
 $I_{D(lim)} = f(T_j); V_{DS} = 12V$

Parameter: V_{IN}



8 Off-state drain current

 $I_{DSS} = f(T_i)$

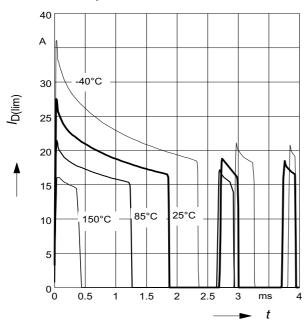




9 Typ. overload current

 $I_{D(lim)} = f(t)$, $V_{bb} = 12$ V, no heatsink

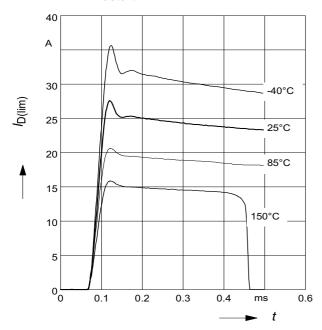
Parameter: T_{jstart}



11 Determination of $I_{D(lim)}$

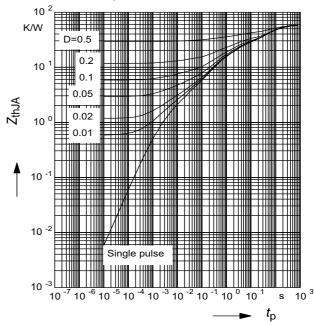
 $I_{D(lim)} = f(t); t_m = 200 \mu s$

Parameter: T_{Jstart}



10 Typ. transient thermal impedance Z_{thJA} =f(t_{p}) @ 6 cm² cooling area

Parameter: $D=t_p/T$





Package Outlines

1 Package Outlines

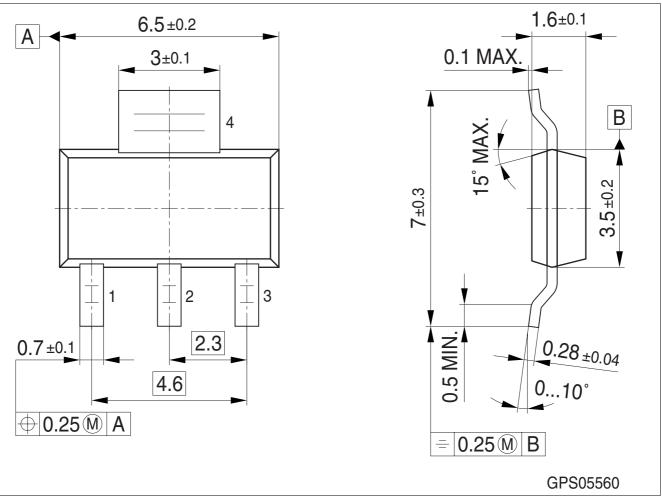


Figure 1 PG-SOT223-4 (Plastic Green Small Outline Transistor Package)

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

Please specify the package needed (e.g. green package) when placing an order

Dimensions in mm



Revision History

2 Revision History

Version	Date	Changes
Rev. 1.3	2008-04-14	Package information updated to SOT223-4
Rev. 1.2 2007-02-15		released automotive green version
	Package parameter (humidity and climatic) removed in Maximum ratings	
		AEC icon added
		RoHS icon added
		Green product (RoHS-compliant) added to the feature list
		Package information updated to green
		Green explanation added
Rev. 1.1	2004-03-05	released production version

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