

IRF7420PbF

HEXFET® Power MOSFET

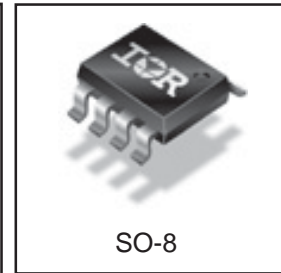
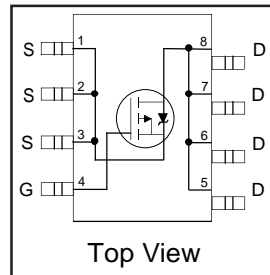
- Ultra Low On-Resistance
- P-Channel MOSFET
- Surface Mount
- Available in Tape & Reel
- Lead-Free

V_{DSS}	$R_{DS(on) \text{ max}}$	I_D
-12V	14mΩ @ $V_{GS} = -4.5V$	-11.5A
	17.5mΩ @ $V_{GS} = -2.5V$	-9.8A
	26mΩ @ $V_{GS} = -1.8V$	-8.1A

Description

These P-Channel HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve the extremely low on-resistance per silicon area. This benefit provides the designer with an extremely efficient device for use in battery and load management applications..

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infrared, or wave soldering techniques.



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain- Source Voltage	-12	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-11.5	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-9.2	
I_{DM}	Pulsed Drain Current ①	-46	
$P_D @ T_A = 25^\circ C$	Power Dissipation ③	2.5	W
$P_D @ T_A = 70^\circ C$	Power Dissipation ③	1.6	
	Linear Derating Factor	20	mW/°C
V_{GS}	Gate-to-Source Voltage	±8	V
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to +150	°C

Thermal Resistance

	Parameter	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ③	50	°C/W

www.irf.com

1

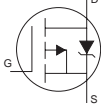
IRF7420PbF

International
IR Rectifier

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	-12	—	—	V	$V_{GS} = 0V, I_D = -250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.007	—	V/°C	Reference to 25°C , $I_D = -1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	—	14	m Ω	$V_{GS} = -4.5V, I_D = -11.5A$ ②
		—	—	17.5		$V_{GS} = -2.5V, I_D = -9.8A$ ②
		—	—	26		$V_{GS} = -1.8V, I_D = -8.1A$ ②
$V_{GS(th)}$	Gate Threshold Voltage	-0.4	—	-0.9	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
g_{fs}	Forward Transconductance	32	—	—	S	$V_{DS} = -10V, I_D = -11.5A$
I_{DSS}	Drain-to-Source Leakage Current	—	—	-1.0	μA	$V_{DS} = -9.6V, V_{GS} = 0V$
		—	—	-25		$V_{DS} = -9.6V, V_{GS} = 0V, T_J = 70^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	-100	nA	$V_{GS} = -8V$
	Gate-to-Source Reverse Leakage	—	—	100		$V_{GS} = 8V$
Q_g	Total Gate Charge	—	38	—	nC	$I_D = -11.5A$
Q_{gs}	Gate-to-Source Charge	—	8.1	—		$V_{DS} = -6V$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	8.7	—		$V_{GS} = -4.5V$ ②
$t_{d(on)}$	Turn-On Delay Time	—	8.8	13	ns	$V_{DD} = -6V, V_{GS} = -4.5V$
t_r	Rise Time	—	8.8	13		$I_D = -1.0A$
$t_{d(off)}$	Turn-Off Delay Time	—	291	437		$R_D = 6\Omega$
t_f	Fall Time	—	225	338		$R_G = 6\Omega$ ②
C_{iss}	Input Capacitance	—	3529	—		pF
C_{oss}	Output Capacitance	—	1013	—	$V_{DS} = -10V$	
C_{rss}	Reverse Transfer Capacitance	—	656	—	$f = 1.0\text{MHz}$	

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	-2.5	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	-46		
V_{SD}	Diode Forward Voltage	—	—	-1.2	V	$T_J = 25^\circ\text{C}, I_S = -2.5A, V_{GS} = 0V$ ②
t_{rr}	Reverse Recovery Time	—	62	93	ns	$T_J = 25^\circ\text{C}, I_F = -2.5A$
Q_{rr}	Reverse Recovery Charge	—	61	92	μC	$di/dt = -100A/\mu s$ ②

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width $\leq 400\mu s$; duty cycle $\leq 2\%$.
- ③ Surface mounted on 1 in square Cu board, $t \leq 10\text{sec}$.

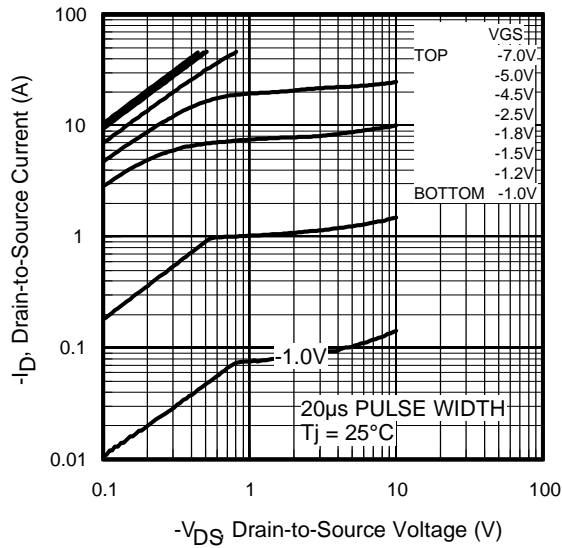


Fig 1. Typical Output Characteristics

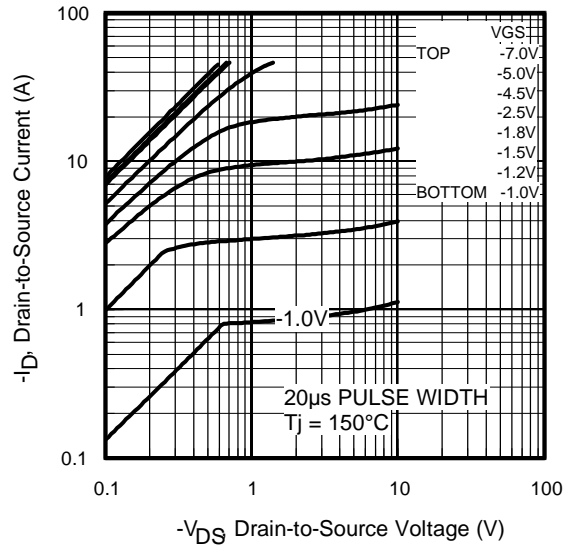


Fig 2. Typical Output Characteristics

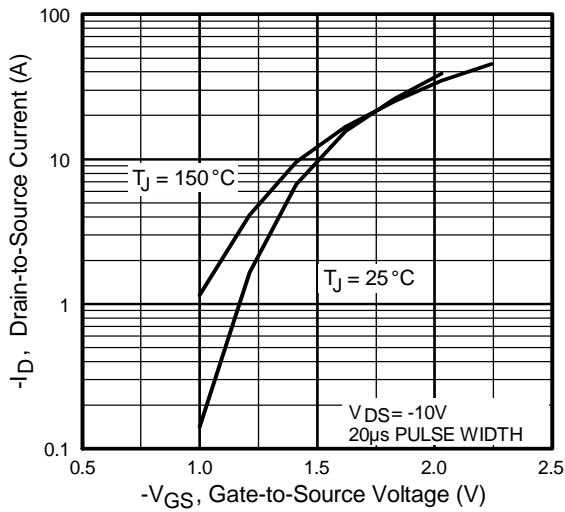


Fig 3. Typical Transfer Characteristics

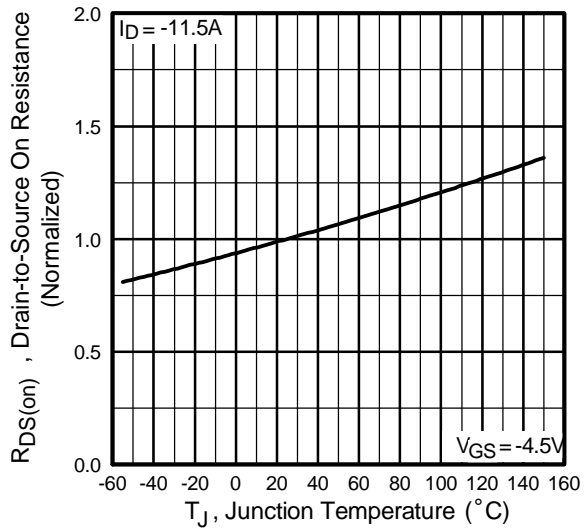


Fig 4. Normalized On-Resistance Vs. Temperature

IRF7420PbF

International
IR Rectifier

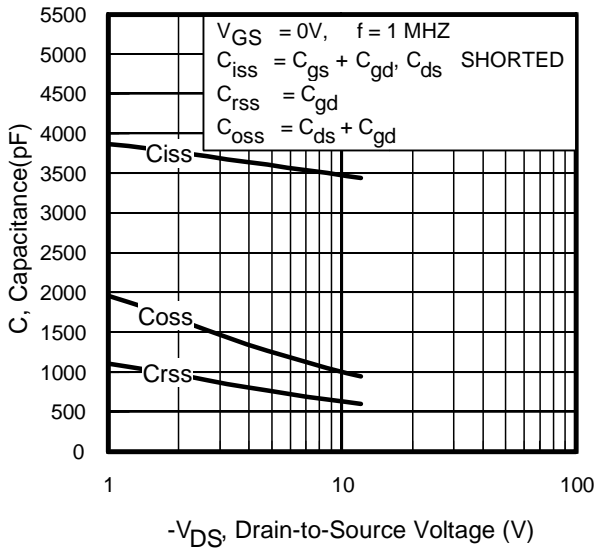


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

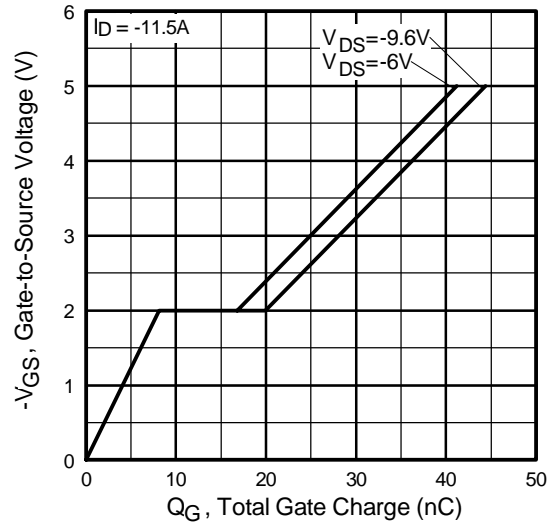


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

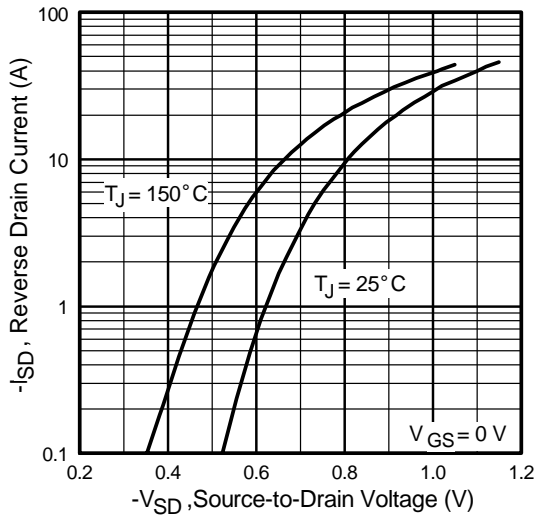


Fig 7. Typical Source-Drain Diode Forward Voltage

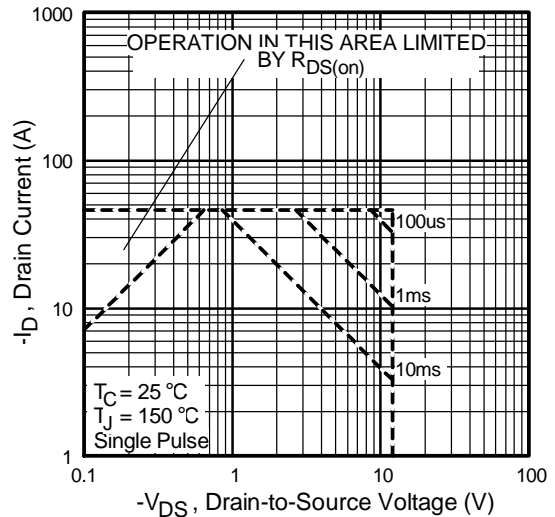


Fig 8. Maximum Safe Operating Area

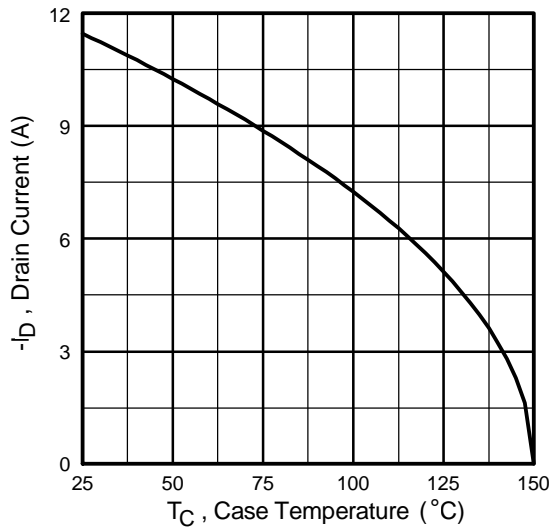


Fig 9. Maximum Drain Current Vs. Case Temperature

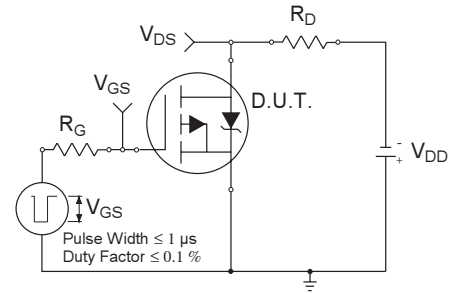


Fig 10a. Switching Time Test Circuit

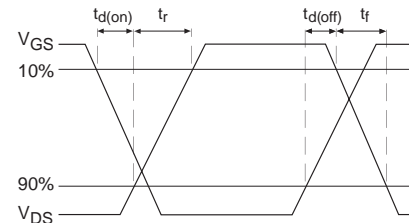


Fig 10b. Switching Time Waveforms

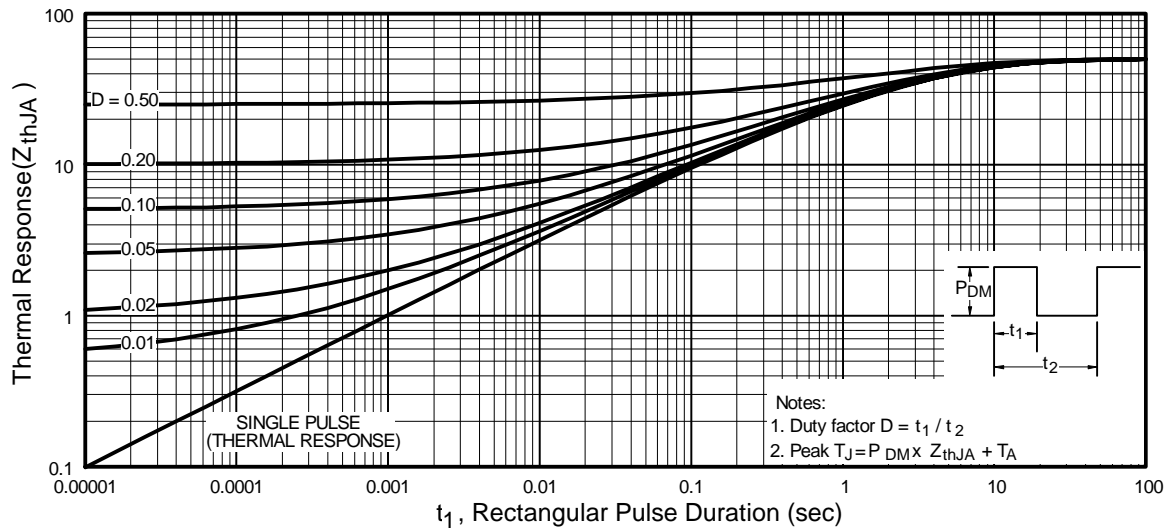


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

IRF7420PbF

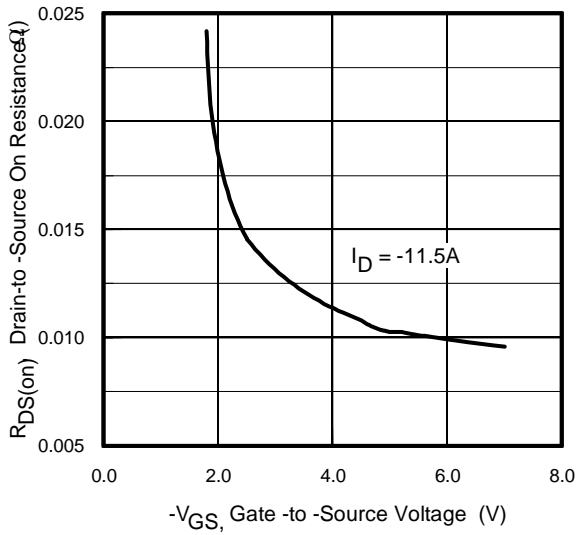


Fig 12. Typical On-Resistance Vs. Gate Voltage

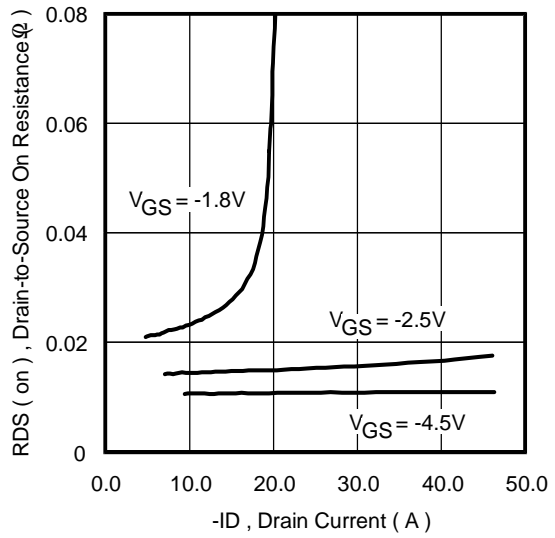


Fig 13. Typical On-Resistance Vs. Drain Current

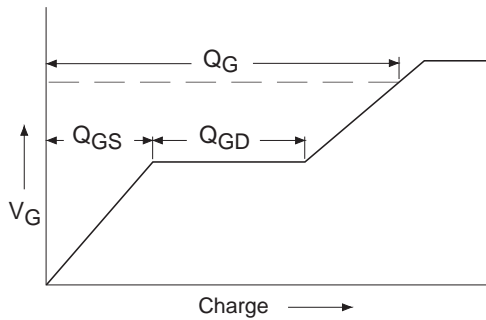


Fig 14a. Basic Gate Charge Waveform

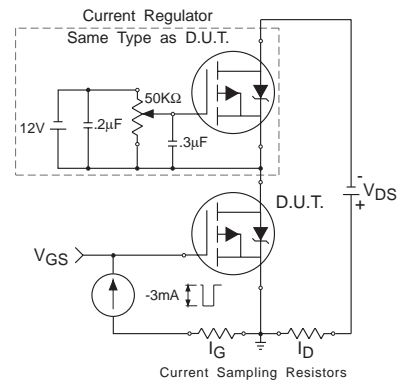


Fig 14b. Gate Charge Test Circuit

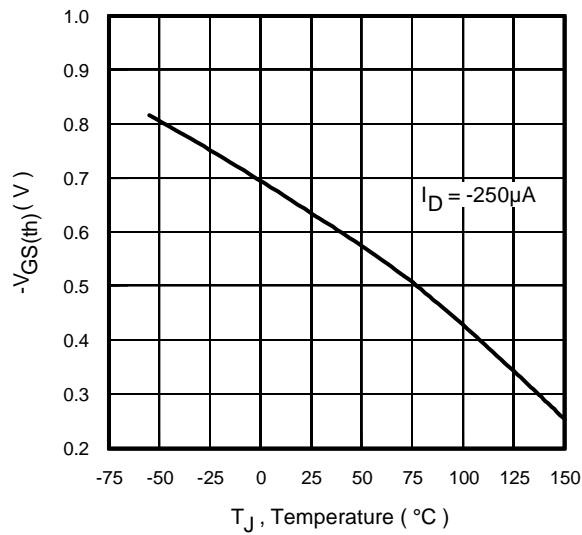


Fig 15. Typical $V_{GS(th)}$ Vs. Junction Temperature

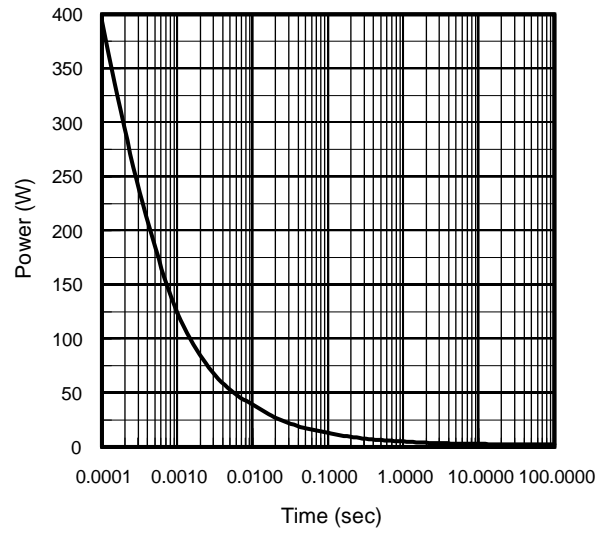


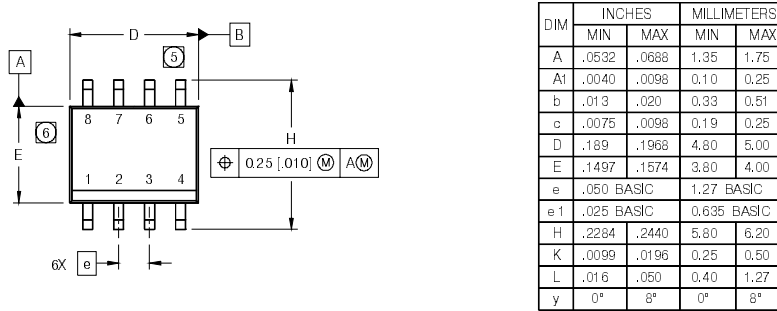
Fig 16. Typical Power Vs. Time

IRF7420PbF

International
IR Rectifier

SO-8 Package Outline

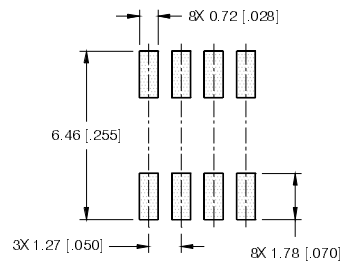
Dimensions are shown in millimeters (inches)



NOTES:

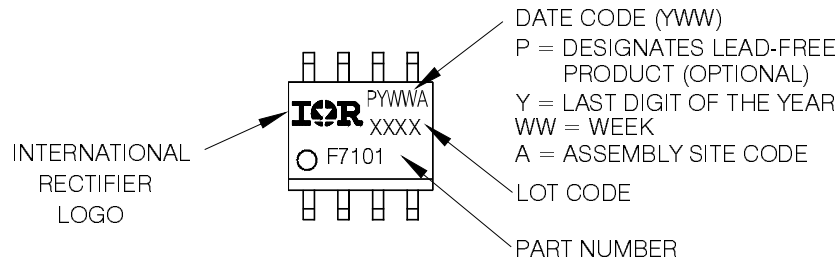
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 [0.006].
6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 [0.010].
7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

FOOTPRINT



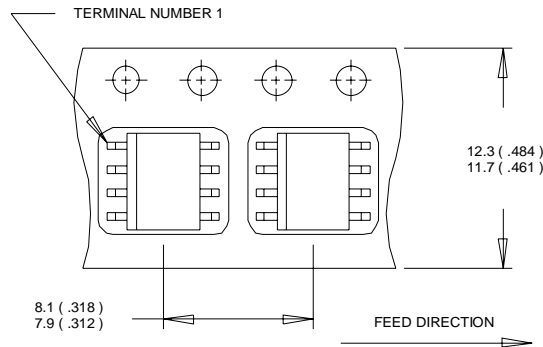
SO-8 Part Marking Information (Lead-Free)

EXAMPLE: THIS IS AN IRF7101 (MOSFET)

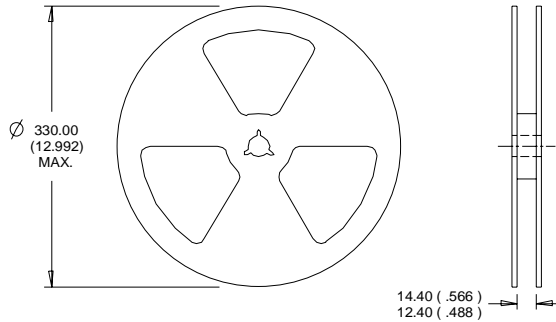


SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.
 This product has been designed and qualified for the Consumer market.
 Qualification Standards can be found on IR's Web site.

IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffheitsgarantie").

With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office (www.infineon.com).

WARNINGS

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.