

# **HFA40HF60**

Ultrafast, Soft Recovery Diode Thru-Hole (TO-254AA) 600V, 22A

### **Features**

- Single diode configuration .
- Reduced RFI and EMI •
- Reduced snubbing •
- Extensive characterization of recovery parameters •
- Hermetic package ٠
- Surface mount •

**Potential Applications** 

- DC-DC converter
- Motor drives

### **Product Validation**

Qualified according to MIL-PRF-19500 for space applications

### Description

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and di/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motor drives and other applications where switching losses are significant portion of the total losses.

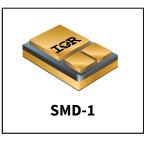
### **Ordering Information**

Table 1 Ordering options					
Part number	Package	Screening Level			
HFA40HF60	SMD-1	COTS			
HFA40HF60SCV	SMD-1	JANTXV-equivalent			
HFA40HF60SCX	SMD-1	JANTX-equivalent			
HFA40HF60SCS	SMD-1	S-level			

#### **V**<sub>R</sub>: 600V V<sub>F</sub>: 1.75V

**Product Summary** 

- **Q**<sub>rr</sub>: 190nC
- di(rec)M/dt: 270A/µs



PD-20381D



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# 1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings
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Symbol	Parameter	Value	Unit
V <sub>R</sub>	DC Reverse Voltage	600	V
I <sub>F</sub>	Continuous Forward Current, Tc=100 °C <sup>1</sup>	22	А
I <sub>FSM</sub>	Single pulse Forward Current, $T_c = 25^{\circ}C^2$	225	А
$P_{D} @ T_{C} = 25^{\circ}C$	Maximum Power Dissipation	83	W
TJ T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to 150	°C
Wt	Weight	2.6 (Typical)	g

 $^{1}$  DC = 50% rect. wave

<sup>2</sup> ½ sine wave, 60 Hz, Pulse width = 8.33 ms



**Device Characteristics** 

### 2 Device Characteristics

### 2.1 Electrical Characteristics

### Table 3Electrical Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
$V_{BR}$	Cathode Anode Breakdown Voltage	600	_	_	V	I <sub>R</sub> = 100μA
V <sub>F</sub>		—	_	1.55		I <sub>F</sub> = 22A, T <sub>J</sub> = -55°C
	Forward Voltage Drop See Fig. 1	_	1.63	1.75		I <sub>F</sub> = 22A, T <sub>J</sub> = 25°C
		_	2.07	2.25	V	I <sub>F</sub> = 45A, T <sub>J</sub> = 25°C
		_	1.52	1.64		I <sub>F</sub> = 22A, T <sub>J</sub> = 125°C
I <sub>R</sub>	Reverse Leakage Current	—	_	10	μA	$V_R = V_R$ Rated
	See Fig. 2	_	_	1.0	mA	V <sub>R</sub> = 480V, T <sub>J</sub> = 125°C
CT	Junction Capacitance See Fig. 3	_	56	59	pF	V <sub>R</sub> = 200V
Ls	Series Inductance	_	5.9	_	nH	Measured from center of cathode pad the center of anode pad

### 2.2 Dynamic Recovery Characteristics

### Table 4 Dynamic Recovery Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	<b>Test Condition</b>	S
t <sub>rr1</sub>	Reverse Recovery Time	_	60	97		T <sub>J</sub> = 25°C	
t <sub>rr2</sub>	See Fig. 5	_	110	—	ns	T <sub>J</sub> = 125°C	I <sub>F</sub> = 22A
I <sub>RRM1</sub>	Peak Recovery Current	_	5.2	_	_	T <sub>J</sub> = 25°C	
I <sub>RRM2</sub>	See Fig. 6	_	8.5	_	A	T <sub>J</sub> = 125°C	V <sub>R</sub> = 200V
Q <sub>rr1</sub>	Reverse Recovery Charge	_	190	_		T <sub>J</sub> = 25°C	
Q <sub>rr2</sub>	See Fig. 7	_	560	—	nC	T <sub>J</sub> = 125°C	$d_{if}/dt = 200 \text{ A}/\mu\text{s}$
di <sub>(rec)M</sub> /dt <sub>1</sub>	Peak Rate of Fall of Recovery	_	270	_		T <sub>J</sub> = 25°C	
$di_{(rec)M}/dt_2$	Current During t₀ See Fig. 8	_	170	_	A/ μs	T <sub>J</sub> = 125°C	

### 2.3 Thermal-Mechanical Characteristics

#### Table 5 Thermal-Mechanical Characteristics

Symbol	Parameter	Тур.	Max.	Unit
R <sub>θJC</sub>	Junction to Case, Single Leg Conducting	_	1.5	°C/W

**Electrical Characteristics Curves** 





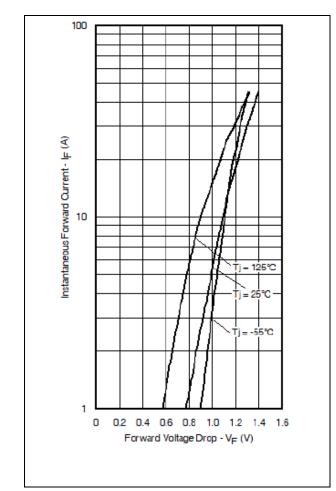
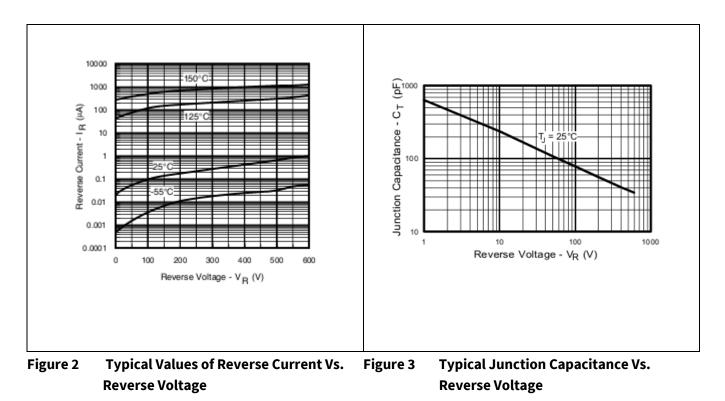


Figure 1 Maximum Forward Voltage Drop Characteristics





### **Electrical Characteristics Curves**

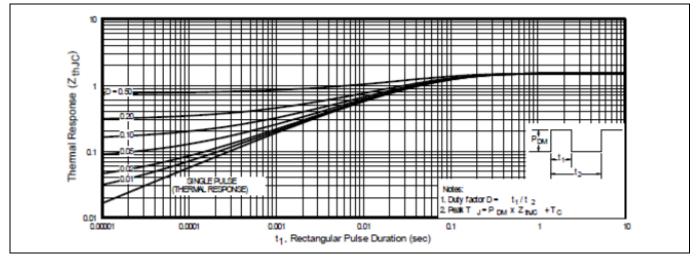
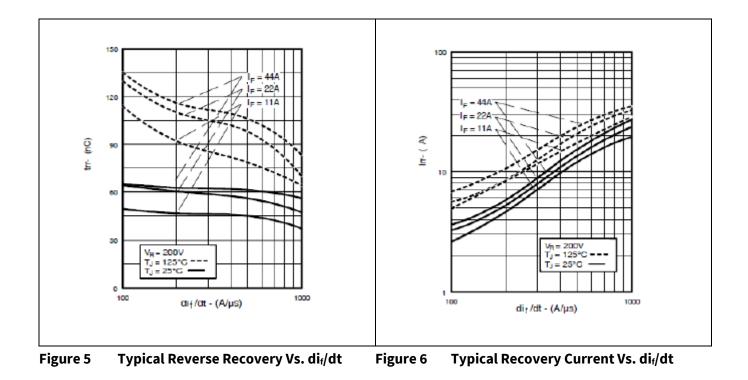
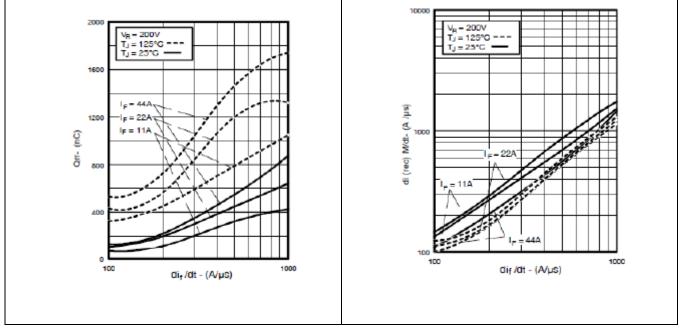


Figure 4 Maximum Thermal Impedance Z<sub>thJc</sub> Characteristics





#### **Electrical Characteristics Curves**



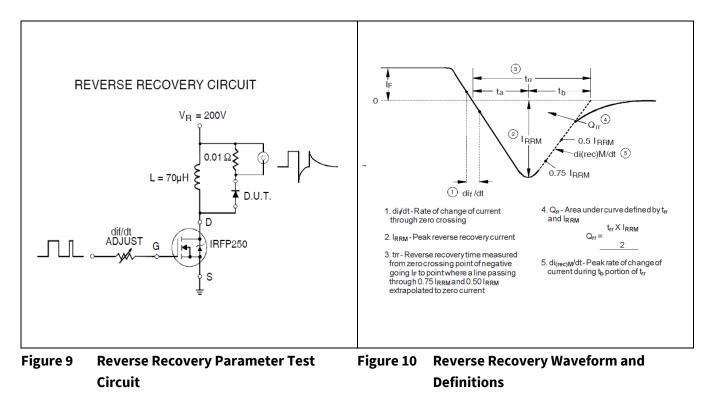






#### **Test Circuit**



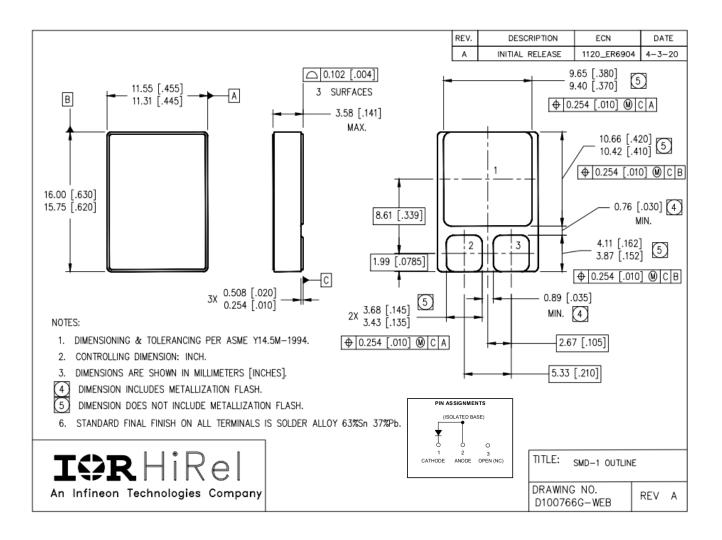




**Package Outline** 

### 5 Package Outline

#### Note: For the most updated package outline, please see the website: <u>SMD-1</u>





## **Revision history**

Document version	Date of release	Description of changes
	6/30/1999	Final datasheet (PD-20381)
Rev A	04/10/2010	Updated per ECN-17456
Rev B	03/04/2013	Updated per ECN-1120-0911
Rev C	10/14/2016	Updated per ECN-1120-04754
Rev D	06/02/2022	Updated per ECN-1120-08972

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