REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED

REV																				
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REV STATUS	REV															
OF SHEETS	SHEET	1	2	3	4	5	6	7	8	9	10	11				

PMIC N/A

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DRAWING APPROVAL DATE
06-06-12

REVISION LEVEL

STANDARD MICROCIRCUIT DRAWING

THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE

AMSC N/A

DEFENSE SUPPLY CENTER COLUMBUS
POST OFFICE BOX 3990
COLUMBUS, OHIO 43218-3990
http://www.dscc.dla.mil

MICROCIRCUIT, HYBRID, LINEAR, +5 AND ±12 VOLT, TRIPLE CHANNEL, DC/DC CONVERTER

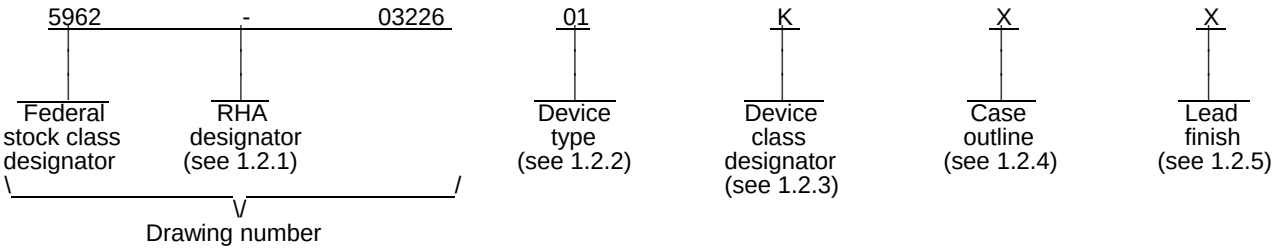
SIZE	CAGE CODE	
A	**67268**	**5962-03226**

SHEET 1 OF 11

1. SCOPE

1.1 Scope. This drawing documents five product assurance classes as defined in paragraph 1.2.3 and MIL-PRF-38534. A choice of case outlines and lead finishes which are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Radiation hardness assurance (RHA) designator. RHA marked devices shall meet the MIL-PRF-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) identify the circuit function as follows:

Device type	Generic number	Circuit function
01	M3G280512T/CK	DC/DC converter, 40 W, +5 V and ±12 V outputs

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level. All levels are defined by the requirements of MIL-PRF-38534 and require QML Certification as well as qualification (Class H, K, and E) or QML Listing (Class G and D). The product assurance levels are as follows:

Device class	Device performance documentation
K	Highest reliability class available. This level is intended for use in space applications.
H	Standard military quality class level. This level is intended for use in applications where non-space high reliability devices are required.
G	Reduced testing version of the standard military quality class. This level uses the Class H screening and In-Process Inspections with a possible limited temperature range, manufacturer specified incoming flow, and the manufacturer guarantees (but may not test) periodic and conformance inspections (Group A, B, C, and D).
E	Designates devices which are based upon one of the other classes (K, H, or G) with exception(s) taken to the requirements of that class. These exception(s) must be specified in the device acquisition document; therefore the acquisition document should be reviewed to ensure that the exception(s) taken will not adversely affect system performance.
D	Manufacturer specified quality class. Quality level is defined by the manufacturers internal, QML certified flow. This product may have a limited temperature range.

1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	Terminals	Package style
X	See figure 1	13	Straight leads with side mounting tabs (leads are ceramic sealed)

1.2.5 Lead finish. The lead finish shall be as specified in MIL-PRF-38534.

1.3 Absolute maximum ratings. 1/

Input voltage range	-0.5 V dc to +80 V dc
Lead temperature (soldering, 10 seconds)	+300°C
Storage temperature	-55°C to +135°C

1.4 Recommended operating conditions.

Input voltage range	+18 V dc to +50 V dc
Output power 2/ 3/	≤ 40 W
Case operating temperature range (T_C)	-55°C to +85°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38534 - Hybrid Microcircuits, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.
MIL-STD-1835 - Interface Standard for Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.
MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at http://assist.daps.dla.mil/quicksearch/ or http://assist.daps.dla.mil or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 may include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. Therefore, the tests and inspections herein may not be performed for the applicable device class (see MIL-PRF-38534). The manufacturer may eliminate, modify or optimize the tests and inspections herein, however the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class. In addition, the modification in the QM plan shall not affect the form, fit, or function of the device for the applicable device class.

1/ Stresses above the absolute maximum ratings may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.
2/ Derate output power linearly above case temperature +125°C to 0 at +135°C.
3/ Unless otherwise specified, “Rated” load is 20 watts on the main (+5 volt) output and 10 watts each on the auxiliary (±12 volt) outputs. Load currents of up to +5 A on the main output and ±1 A on the auxiliary outputs are acceptable as long as the total output power does not exceed 40 watts.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

3.5 Marking of device(s). Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked.

3.6 Data. In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DSCC-VA) upon request.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DSCC-VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.

(2) T_C = +125°C minimum. Test duration 320 hours minimum as specified in method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55°C \le T_C \le +85°C$ V_{IN} = 28 V dc ±5%, C_L = 0 unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Output voltage 1/	V_{OUT}	I_{OUT} = 4 A (main)	1	01	4.98	5.02	V
			2,3		4.93	5.07	
		I_{OUT} = ±833 mA (± aux.) each output	1		±11.50	±12.10	
			2,3		±11.30	±12.30	
Output current 1/ 2/ 3/ 4/	I_{OUT}	V_{IN} = 18, 28, and 50 V dc (main)	1,2,3	01	0.4	4	A
		V_{IN} = 18, 28, and 50 V dc (± aux.) each output			±83	±833	mA
Output ripple voltage 1/ 5/	V_{RIP}	I_{OUT} = 4 A (main)	1,2,3	01		50	mV p-p
		I_{OUT} = ±833 mA (± aux.) each output				60	
Line regulation 1/	VR_{LINE}	V_{IN} = 18, 28, and 50 V dc, I_{OUT} = 10%, 50%, and 100% (main)	1,2,3	01	-10	10	mV
		V_{IN} = 18, 28, and 50 V dc, I_{OUT} = 10%, 50%, and 100% (± aux.)			-120	120	
Load regulation 1/ 6/	VR_{LOAD}	V_{IN} = 18, 28, and 50 V dc, I_{OUT} = 10%, 50%, and 100% (main)	1,2,3	01	-50	50	mV
		V_{IN} = 18, 28, and 50 V dc, I_{OUT} = 10%, 50%, and 100% (± aux.)			-400	400	
Cross regulation	VR_{CROSS}	V_{IN} = 18, 28, and 50 V dc, I_{OUT} = 2.5 A to 1 A and 2.5 A to 4 A (main), I_{OUT} = ±417 mA (±aux.)	1,2,3	01	-3.5	+3.5	%
Total regulation	VR	All conditions of Line, Load and Cross regulation, and temperatures. (main)	1,2,3	01	4.90	5.10	V
		All conditions of Line, Load and Cross regulation, and temperatures. (± aux.)			±11.00	±13.00	

See footnotes at end of table.

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55°C \le T_C \le +85°C$ V_{IN} = 28 V dc ±5%, C_L = 0 unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Input current	I_{IN}	I_{OUT} = 0, pin 3 open	1,2,3	01		80	mA
		Inhibit (pin 3) shorted to Input return (pin 2)	1,2,3			5	
Inhibit input 7/	INH_{IN}	Open circuit voltage	---	01	3.0	5.00	V
		Drive current (sink)				100	µA
		Voltage range			-0.5	50	V
Efficiency 1/	E_{FF}	I_{OUT} = 4 A (main) I_{OUT} = ±833 mA (± aux.)	1,2,3	01	75		%
Isolation	ISO	Input to output or any pin to case (except pin 10) at 500 V dc, T_C = +25°C	1	01	100		MΩ
Maximum capacitive load 1/ 7/ 8/	C_L	No effect on dc performance, I_{OUT} = 4 A (main), T_C = +25°C	---	01		1000	µF
		No effect on dc performance, I_{OUT} = ±833 mA (± aux.) Each output T_C = +25°C				200	
Power dissipation load fault 9/	P_D	Short circuit	1,2,3	01		20	W
		Overload					
Current limit point 9/	I_{LIM}	Expressed as a percentage of full rated power	1,2,3	01		135	%
Switching frequency	F_S	Sync input (pin 4) open	1,2,3	01	450	550	kHz
Sync frequency range	F_{SYNC}	External clock on Sync input (pin 4)	1,2,3	01	450	600	kHz
Sync input 7/	$SYNC_{IN}$	Pulse high level	---	01	4	10	V
		Pulse level low			-0.5	0.5	V
		Pulse transition time			40		V/µs
		Pulse duty cycle			20	80	%

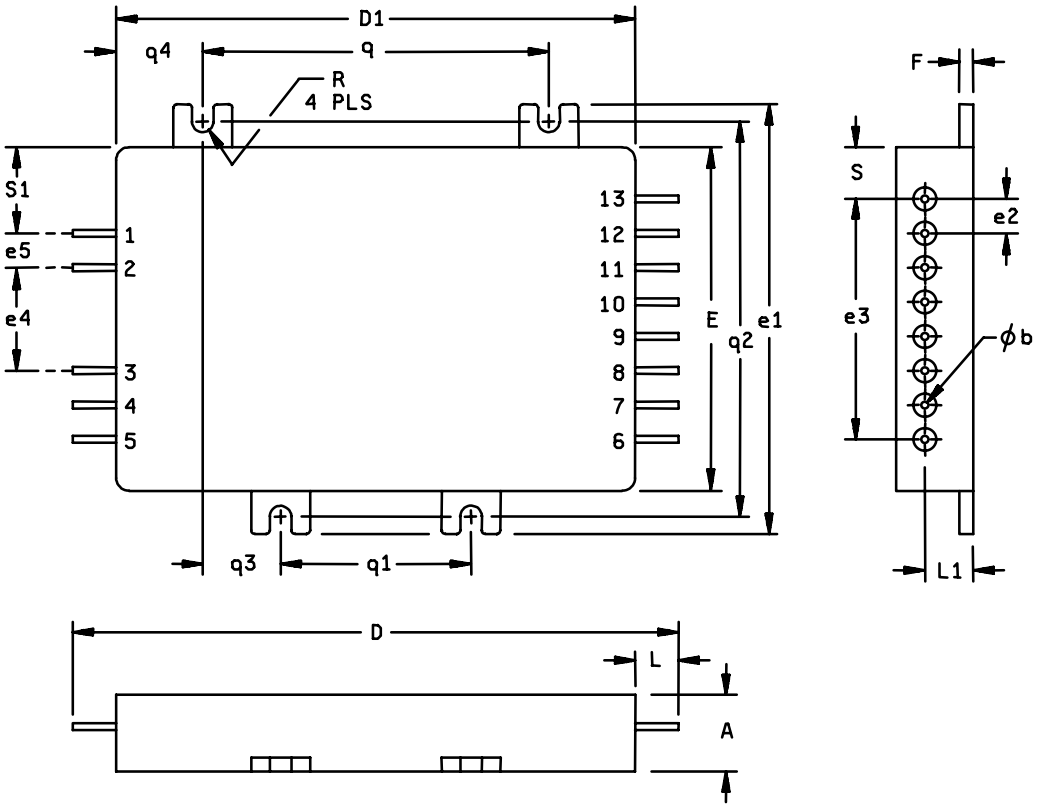
See footnotes at end of table..

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55°C \le T_C \le +85°C$ V_{IN} = 28 V dc ±5%, C_L = 0 unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Output response to step transient load changes 1/ 10/	VO_{TLOAD}	Any output after the 100% load. 50% load to/from 100% load, any output	4,5,6	01	-300	+300	mV pk
Recovery time, step transient load changes 1/ 10/ 11/	TT_{LOAD}	Any output after the 100% load. 50% load to/from 100% load, any output	4,5,6	01		200	µs
Output response to transient step line changes 1/ 7/ 12/	VO_{TLINE}	Input step, 18 V to/from 50 V dc, I_{OUT} = 4 A (main), I_{OUT} = ±833 mA (± aux.)	---	01	-300	+300	mV pk
Recovery time step transient line changes 1/ 7/ 11/ 12/	TT_{LINE}	Input step, 18 V to/from 50 V dc, I_{OUT} = 4 A (main), I_{OUT} = ±833 mA (± aux.)	---	01		200	µs
Turn on overshoot 1/ 13/	$VTon_{OS}$	I_{OUT} = 0, 4 A (main)	4,5,6	01		500	mV pk
		I_{OUT} = 0, ±833 mA (± aux.)				1200	
Turn on delay 1/ 13/	Ton_D	I_{OUT} = 4 A (main)	4,5,6	01		5	ms
		I_{OUT} = ±833 mA (± aux.)				5	

1/ Unless otherwise specified, "Rated" load is 20 watts on the main (+5 volt) output and 10 watts each on the auxiliary (±12 volt) outputs. Load currents of up to +5 A and ±1 A on the main and auxiliary outputs respectively, are acceptable as long as the total output power does not exceed 40 watts.
2/ Parameter verified during line and load regulation tests.
3/ Although operation with no load is permissible, light loading on the main (+5 volt) output may cause the output voltage of the auxiliary outputs (±12 volt) to drop out of regulation. It is therefore recommended that at least 200 mA or 20% of the total output power, whichever is greater, be taken from the main (+5 volt) output.
4/ Although operation with no load is permissible, heavy loading on the main (+5 volt) output may cause the output voltage of the auxiliary outputs (±12 volt) to rise out of regulation. It is therefore recommended that at least 50mA or 20% of the total output power, whichever is greater, be taken from the auxiliary (±12 volt) outputs.
5/ Guaranteed for a DC to 20 MHz bandwidth. Tested using a 20 kHz to 10 MHz bandwidth.
6/ Load is varied for output under test while the remaining outputs are loaded at 50% of rated. Regulation relative to output voltage at 50% rated load.
7/ Parameter is guaranteed to the limits specified in table I by design, but not tested. Limits apply to the operating range specified in table I, unless otherwise specified. No Group A subgroups are specified for this test.
8/ Capacitive load may be any value from 0 to the maximum limit without compromising dc performance. A capacitive load in excess of the maximum limit may interfere with the proper operation of the converter's overload protection, causing erratic behavior during turn-on.
9/ Overload power dissipation is defined as the device power dissipation with the load set such that V_{OUT} = 90% of nominal.
10/ Load step transition time ≤ 10 µs.
11/ Recovery time is measured from the initiation of the transient to where V_{OUT} has returned to within ±1% of its steady state value.
12/ Line step transition time ≤ 100 µs.
13/ Turn-on delay time is measured from either a step application of input power or a logic low to a logic high transition on the inhibit pin (pin 3) to the point where V_{OUT} = 90% of nominal.

Case outline X.



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A		12.07		.475
D	88.90 REF		3.50 REF	
D1	75.95	76.45	2.99	3.01
E/q	50.67	50.93	1.995	2.005
e1	63.37	63.63	2.495	2.505
e2/e5	4.95	5.21	.195	.205
e3	35.56 REF		1.400 REF	
e4	15.11	15.37	.595	.605
F		2.03		.080
L	6.1	6.6	.24	.26
L1	6.48	6.73	.255	.265
q1	27.81	28.07	1.095	1.105
q2	58.29	58.55	2.295	2.305
q3	11.30	11.56	.445	.455
q4/S1	12.57	12.83	.495	.505
R		1.588		.0625
S	7.49	7.75	.295	.305
∅b	0.89	1.14	.035	.045

NOTES:
1. The U.S. government preferred system of measurement is the metric SI. This item was designed using inch-pound units of measurement. In case of problems involving conflicts between the metric and inch-pound units, the inch-pound units shall rule.
2. Lead identification for reference only.
3. Case outline weight: 100 grams maximum.

FIGURE 1. Case outline(s).

Device type	01
Case outline	X
Terminal number	Terminal symbol
1	+V input
2	Input return
3	Inhibit
4	Sync input
5	Sync output
6	Main return
7	+Main output
8	No connection
9	No connection
10	Case ground
11	- Auxiliary (-aux.) output
12	Auxiliary output return
13	+ Auxiliary (+aux.) output

FIGURE 2. Terminal connections.

TABLE II. Electrical test requirements.

MIL-PRF-38534 test requirements	Subgroups (in accordance with MIL-PRF-38534, group A test table)
Interim electrical parameters	---
Final electrical parameters	1*, 2, 3, 4, 5, 6
Group A test requirements	1, 2, 3, 4, 5, 6
Group C end-point electrical parameters	1, 4
End-point electrical parameters for radiation hardness assurance (RHA) devices	Not applicable

* PDA applies to subgroup 1.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:

a. Tests shall be as specified in table II herein.

b. Subgroups 7, 8, 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

(2) T_C = +125°C.

(3) Test duration: 1,000 hours minimum, as specified in method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5 Radiation Hardness Assurance (RHA) inspection. RHA inspection is not currently applicable to this drawing.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated as specified in MIL-PRF-38534.

6.4 Record of users. Military and industrial users shall inform Defense Supply Center Columbus (DSCC) when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0544.

6.5 Comments. Comments on this drawing should be directed to DSCC-VA, Post Office Box 3990, Columbus, Ohio 43218-3990, or telephone (614) 692-1081.

6.6 Sources of supply. Sources of supply are listed in MIL-HDBK-103 and QML-38534. The vendors listed in MIL-HDBK-103 and QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DSCC-VA and have agreed to this drawing.

STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 06-06-12

Approved sources of supply for SMD 5962-03226 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38534 during the next revisions. MIL-HDBK-103 and QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This information bulletin is superseded by the next dated revisions of MIL-HDBK-103 and QML-38534. DSCC maintains an online database of all current sources of supply at http://www.dscc.dla.mil/Programs/Smcr/.

Standard microcircuit drawing PIN 1/	Vendor CAGE number	Vendor similar PIN 2/
5962-0322601KXA 5962-0322601KXC	52467 52467	M3G280512T/CK M3G280512T/CK

1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the Vendor to determine its availability.

2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number	Vendor name and address
52467	International Rectifier - HiRel Products, Incorporated 2270 Martin Ave Santa Clara, CA 95050

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.