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FAIRCHILD

SEMICONDUCTOR®

KSA931

Low Frequency Amplifier & Medium Speed Switching

- Complement to KSC2331
- Collector-Base Voltage : V_{CBO}= -80V
- Collector Power Dissipation : P_C=1W



1. Emitter 2. Collector 3. Base

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{CBO}	Collector-Base Voltage	-80	V
V _{CEO}	Collector-Emitter Voltage	-60	V
V _{EBO}	Emitter-Base Voltage	-8	V
I _C	Collector Current	-700	mA
P _C	Collector Power Dissipation	1	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = -100μA, I _E =0	-80			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -10mA, I _B =0	-60			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -100μΑ, I _C =0	-8			V
I _{CBO}	Collector Cut-off Current	V _{CB} = -60V, I _E =0			-0.1	μΑ
I _{EBO}	Emitter Cut-off Current	V _{EB} = -5V, I _C =0			-0.1	μΑ
h _{FE}	* DC Current Gain	V _{CE} = -2V, I _C = -50mA	40		240	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = -500mA, I _B = -50mA		-0.3	-0.7	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	I _C = -500mA, I _B = -50mA		-0.9	-1.2	V
f _T	Current Gain Bandwidth Productor	V _{CE} = -10V, I _C = -50mA		100		MHz
C _{ob}	Output Capacitance	V _{CB} = -10V, I _E =0, f=1MHz		13		pF

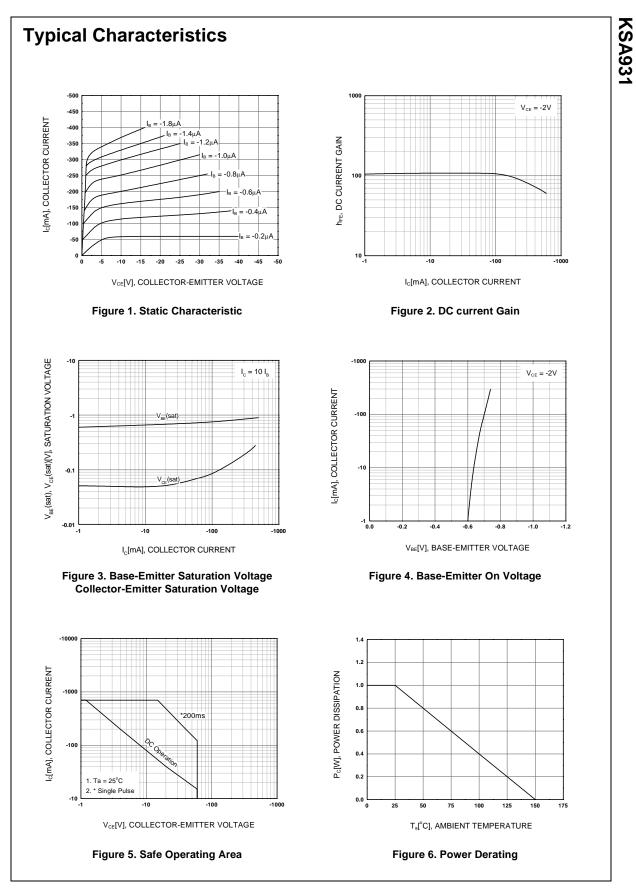
* Pulse Test: PW≤350µs, Duty cycle≤2%

h_{FE} Classification

Classification	R	0	Y
h _{FE}	40 ~ 80	70 ~ 140	120 ~ 240

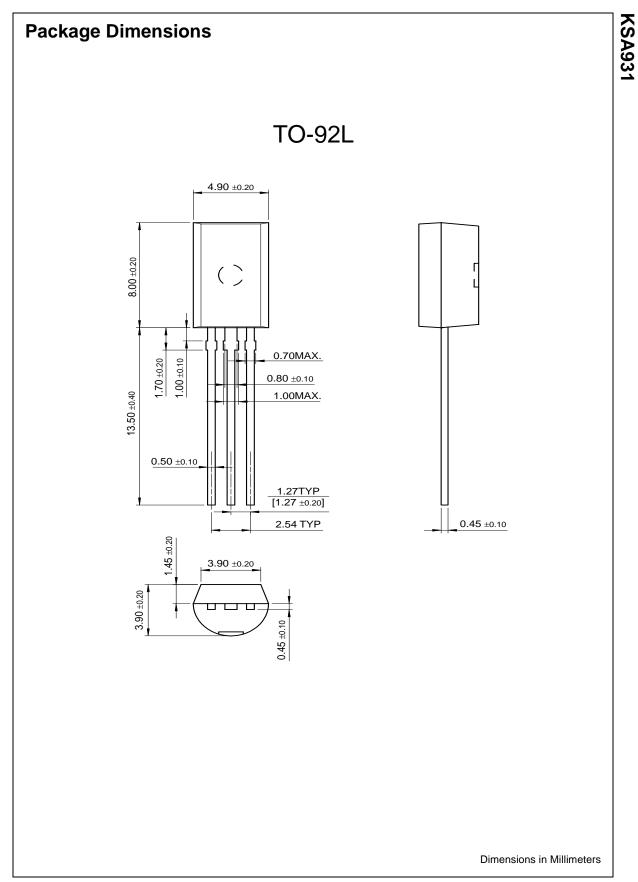
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KSA931



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Programmable Active Droop™	OPTOPLANAR™	SMART START™	

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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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