

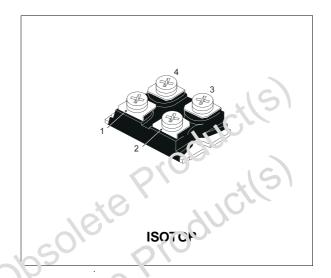
# ESM5045DV

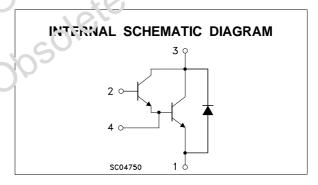
# NPN DARLINGTON POWER MODULE

- HIGH CURRENT POWER BIPOLAR MODULE
- VERY LOW Rth JUNCTION CASE
- SPECIFIED ACCIDENTAL OVERLOAD AREAS
- ULTRAFAST FREEWHEELING DIODE
- FULLY INSULATED PACKAGE (UL COMPLIANT)
- EASY TO MOUNT
- LOW INTERNAL PARASITIC INDUCTANCE

### **INDUSTRIAL APPLICATIONS:**

- MOTOR CONTROL
- SMPS & UPS
- WELDING EQUIPMENT





# ABSOLUTE MAXIMUM RATINGS

symbol	Parameter	Value	Unit
Vcev	Color-Emitter Voltage (V <sub>BE</sub> = -5 V)	600	V
V <sub>CEO(,us</sub> )	Collector-Emitter Voltage (I <sub>B</sub> = 0)	450	V
V/5=3	Emitter-Base Voltage (I <sub>C</sub> = 0)	7	V
lc	Collector Current	60	Α
Ісм	Collector Peak Current (t <sub>p</sub> = 10 ms)	90	Α
I <sub>B</sub>	Base Current	6	Α
I <sub>BM</sub>	Base Peak Current (t <sub>p</sub> = 10 ms)	12	Α
P <sub>tot</sub>	Total Dissipation at T <sub>c</sub> = 25 °C	175	W
V <sub>isol</sub>	Insulation Withstand Voltage (RMS) from All Four Terminals to Exernal Heatsink	2500	V
T <sub>stg</sub>	Storage Temperature	-55 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

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### THERMAL DATA

R <sub>thj-case</sub>	Thermal Resistance Junction-case (transist	or) Max	0.71	°C/W	l
R <sub>thj-case</sub>	Thermal Resistance Junction-case (diode)	Max	1.2	°C/W	
$R_{thc-h}$	Thermal Resistance Case-heatsink With Co	onductive			l
	Grease Applied	Max	0.05	°C/W	

# **ELECTRICAL CHARACTERISTICS** (T<sub>case</sub> = 25 °C unless otherwise specified)

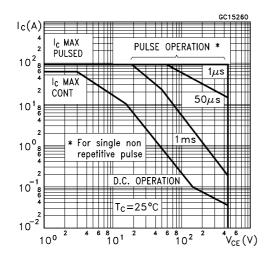
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>CER</sub> #	Collector Cut-off Current ( $R_{BE} = 5 \Omega$ )	$V_{CE} = V_{CEV}$ $V_{CE} = V_{CEV}$ $T_j = 100$ °C			1.5 20	mA mA
I <sub>CEV</sub> #	Collector Cut-off Current (V <sub>BE</sub> = -5)	$V_{CE} = V_{CEV}$ $V_{CE} = V_{CEV}$ $T_j = 100$ °C			1 13	mA mA
I <sub>EBO</sub> #	Emitter Cut-off Current (I <sub>C</sub> = 0)	V <sub>EB</sub> = 5 V			CÎ	mA
V <sub>CEO</sub> (SUS)*	Collector-Emitter Sustaining Voltage (I <sub>B</sub> = 0)	$I_C = 0.2 \text{ A}$ L = 25 mH $V_{clamp} = 450 \text{ V}$	450	00,		V
h <sub>FE</sub> *	DC Current Gain	Ic = 50 A V <sub>CE</sub> = 5 V		150		
V <sub>CE(sat)</sub> *	Collector-Emitter Saturation Voltage	$I_C = 35 \text{ A}$ $I_B = 0.7 \text{ A}$ $I_C = 35 \text{ A}$ $I_B = 0.7 \text{ A}$ $I_j = 100 ^{\circ} \text{C}$ $I_C = 50 \text{ A}$ $I_B = 2.8 \text{ A}$	, 	1.2 1.4 1.4	2	V V V
		$I_C = 50 \text{ A}$ $I_B = 2.2  \Lambda$ $I_I = 100  ^{\circ}\text{C}$	<u> </u>	1.6	2	V
V <sub>BE(sat)</sub> *	Base-Emitter Saturation Voltage	$I_C = 50 \text{ A}$ $I_B := 2.8 \text{ A}$ . $I_C = 50 \text{ A}$ $I_B = 2.8 \text{ A}$ $T_j = 100 ^{\circ}\text{C}$		2.3 2.3	3	V V
di <sub>C</sub> /dt	Rate of Rise of On-state Collector	$V_{CC} = 360 \text{ V}$ $R_C = 0$ $t_p = 3 \mu s$ $I_{31} = 1.05 \text{ A}$ $T_j = 100  ^{\circ}\text{C}$	300	400		A/μs
V <sub>CE</sub> (3 μs)••	Collector-Emitter Dynamic Voltage	$V_{CC} = 300 \text{ V}$ $R_{C} = 8.5 \Omega$ $I_{B1} = 1.05 \text{ A}$ $T_{j} = 100 ^{\circ}\text{C}$		4.5	8	V
V <sub>CE</sub> (5 μs)••	Collector Enitter Dynamic Voltage	$V_{CC} = 300 \text{ V}$ $R_{C} = 8.5 \Omega$ $I_{B1} = 1.05 \text{ A}$ $T_{j} = 100 \text{ °C}$		2.5	4.5	V
ts tf tc	Storage Time Fall Time Cross-over Time	$\begin{array}{lll} I_{C} = 35A & V_{CC} = 50 \ V \\ V_{BB} = -5 \ V & R_{BB} = 0.6 \ \Omega \\ V_{clamp} = 450 \ V & I_{B1} = 0.7 \ A \\ L = 0.07 \ mH & T_{j} = 100 \ ^{\circ}C \end{array}$		3.2 0.25 0.75	5 0.5 1.5	μs μs μs
V <sub>CEW</sub>	Maximum Collector Emitter Voltage Without Snubber	$\begin{split} I_{CWoff} &= 60 \text{ A}  I_{B1} = 2.8 \text{ A} \\ V_{BB} &= -5 \text{ V}  V_{CC} = 50 \text{ V} \\ L &= 42  \mu\text{H}  R_{BB} = 0.6  \Omega \\ T_{j} &= 125  ^{\circ}\text{C} \end{split}$	450			V
V <sub>F</sub> *	Diode Forward Voltage	$I_F = 50 \text{ A}$ $T_j = 100  ^{\circ}\text{C}$		1.5	1.8	V
I <sub>RM</sub>	Reverse Recovery Current	$V_{CC} = 200 \text{ V}$ $I_F = 50 \text{ A}$ $di_F/dt = -300 \text{ A}/\mu\text{s}$ $L < 0.05 \mu\text{H}$ $T_j = 100 ^{\circ}\text{C}$		32	38	A

<sup>\*</sup> Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %

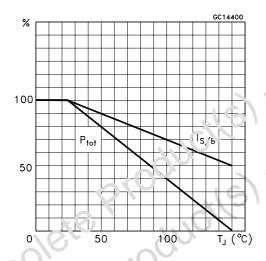
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To evaluate the conduction losses of the diode use the following equations:  $V_F = 1.5 + 0.0055 \ I_F \qquad P = 1.5 \ I_{F(AV)} + 0.0055 \ I^2_{F(RMS)}$  # See test circuits in databook introduction

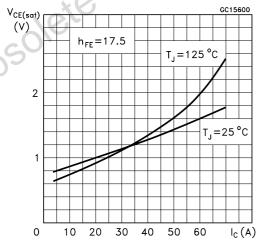
### Safe Operating Areas



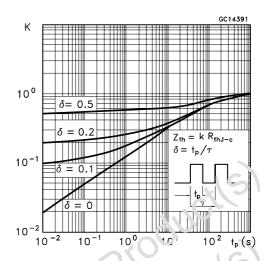
### **Derating Curve**



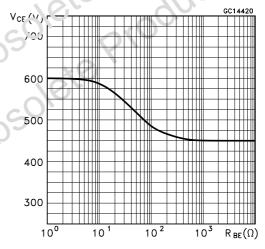
### Collector Emitter Saturation Voltage



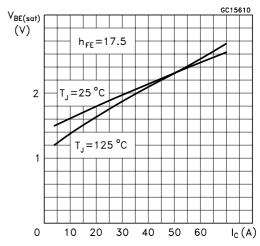
### Thermal Impedance



# Collector-emitter Vollage Versus base-emitter Resistance

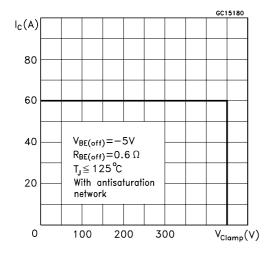


### Base-Emitter Saturation Voltage

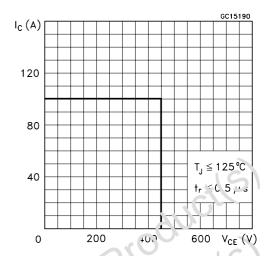


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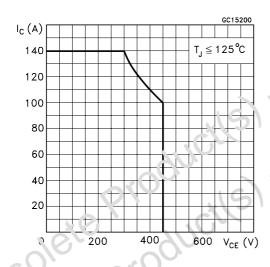
### Reverse Biased SOA



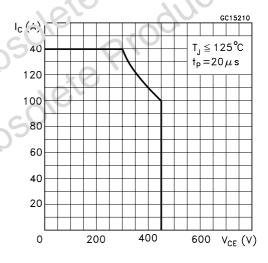
### Foward Biased SOA



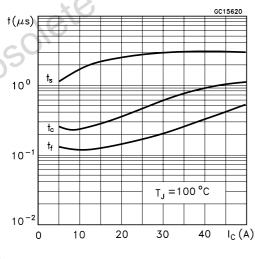
### Reverse Biased AOA



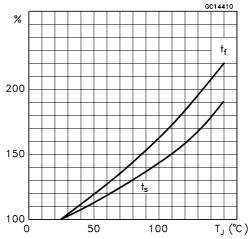
Forward Biased AOA



### Swinching Times Inductive Load

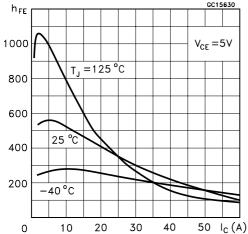


# Switching Times Inductive Load Versus Temperature

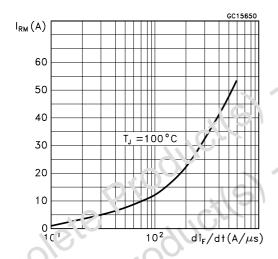


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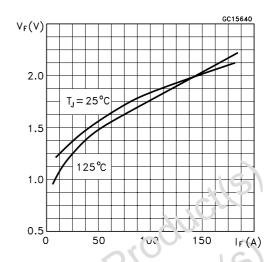
### Dc Current Gain



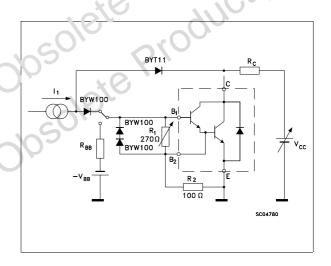
Peak Reverse Current Versus diF/dt



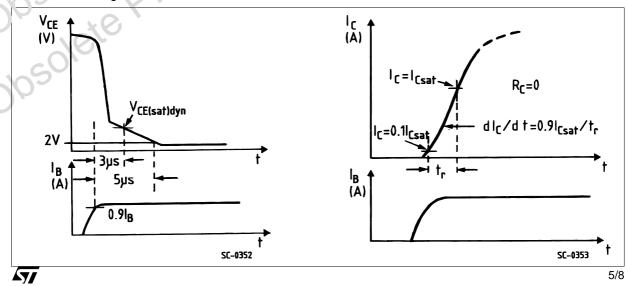
Typical V<sub>F</sub> Versus I<sub>F</sub>



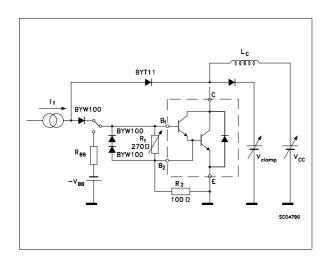
Turn-on Switching Test Circuit



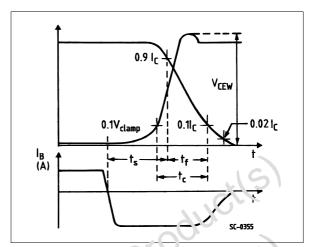
Turn-on Switching Waveforms



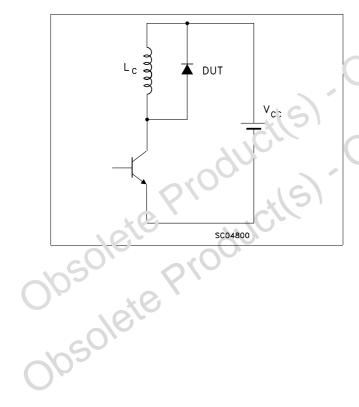
### Turn-on Switching Test Circuit



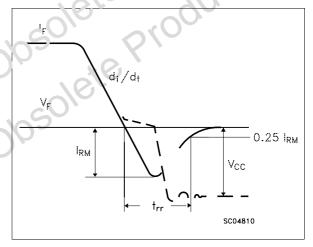
### Turn-off Switching Waveforms



Turn-off Switching Test Circuit of Diode



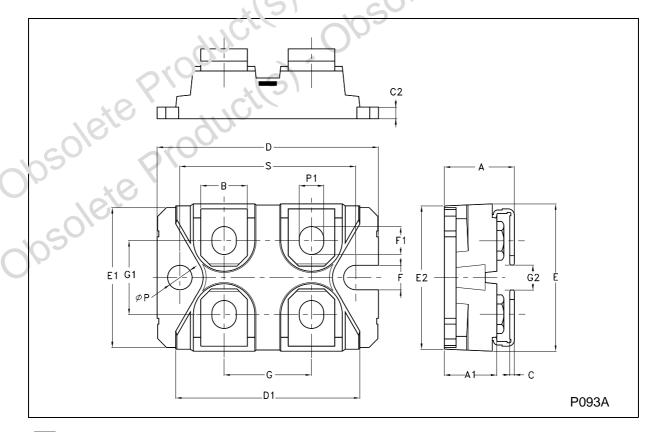
Turn-off Switching Waveform of Diode



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## **ISOTOP MECHANICAL DATA**

DIM.	mm			inch			
DIWI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Α	11.8		12.2	0.465		0.480	
A1	8.9		9.1	0.350		0.358	
В	7.8		8.2	0.307		0.322	
С	0.75		0.85	0.029		0.033	
C2	1.95		2.05	0.076		0.080	
D	37.8		38.2	1.488		1.503	
D1	31.5		31.7	1.240		1.243	
E	25.15		25.5	0.990		1.003	
E1	23.85		24.15	0.938	40	0.950	
E2		24.8			(1.976		
G	14.9		15.1	0.586		0.594	
G1	12.6		12.8	0.496		0.503	
G2	3.5		4.3	C.i3,	AL	1.169	
F	4.1		4.3	7. io1	100,	0.169	
F1	4.6		5	0.181	2/0	0.196	
Р	4		4.3	0.157		0.169	
P1	4		5.4	0.157		0.173	
S	30.1		30.3	1.185		1.193	



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