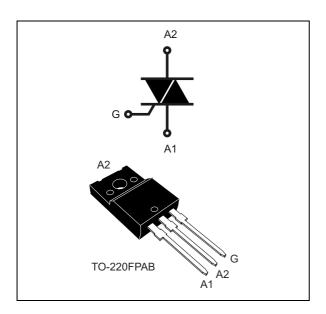
# T810T-8FP

# 8 A logic level Triac

#### Datasheet - production data



life.augmented

## Description

Available in through-hole full pack package, the T810T-8FP Triac can be used for the on/off or phase angle control function in general purpose AC switching. This device can be directly driven by a microcontroller due to its 10 mA gate current requirement. Provide UL certified insulation rated at 2000 VRMS.

Symbol	Value	Unit
I <sub>T(rms)</sub>	8	A
V <sub>DRM</sub> , V <sub>RRM</sub>	800	V
V <sub>DSM</sub> , V <sub>RSM</sub>	900	V
I <sub>GT</sub>	10	mA

### Features

- Medium current Triac
- Three quadrants
- ECOPACK<sup>®</sup>2 and RoHS compliant component
- Complies with UL standards (File ref: E81734)
- High performance Triac:
  - High T<sub>i</sub> family
  - High dl/dt family
  - High dV/dt family
- Insulated package TO-220FPAB:
  - Insulated voltage: 2000 VRMS

## Applications

- General purpose AC line load switching
- Motor control circuits
- Small home appliances
- Lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

### February 2015

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# 1 Characteristics

Symbol	Paramete	Value	Unit			
I <sub>T(rms)</sub>	On-state rms current (full sine wave	)	$T_c = 113^{\circ}C$	8	А	
Ι.	Non repetitive surge peak on-state			60	А	
I <sub>TSM</sub>	current (full cycle, $T_j$ initial = 25 °C)	F = 60 Hz	t = 16.7 ms	63	A	
l²t	$I^{2}t$ value for fusing, $T_{j}$ initial = 25 °C		t <sub>p</sub> = 10 ms	24	A²s	
V <sub>DRM</sub> ,	Popotitivo surgo poak offictato volta	T <sub>j</sub> = 150 °C	600	V		
V <sub>RRM</sub>	RM, Repetitive surge peak off-state voltage		T <sub>j</sub> = 125 °C	800	v	
V <sub>DSM</sub> , V <sub>RSM</sub>	Non repetitive surge peak off-state	surge peak off-state voltage t <sub>p</sub> = 10 ms				
dI/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ , $t_r \le 100$ ns	F = 100 Hz		100	A/µs	
I <sub>GM</sub>	Peak gate current	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 150 °C	4	А	
P <sub>G(AV)</sub>	Average gate power dissipation	1	W			
T <sub>stg</sub>	Storage junction temperature range	- 40 to + 150	°C			
Тj	Operating junction temperature range	- 40 to + 150				
ΤL	Maximum lead temperature for sold	ering during	10 s	260	°C	
V <sub>ins</sub>	Insulation rms voltage, 1 minute			2	kV	

Symbol	Test conditions	Quadrant		Value	Unit
I <sub>GT</sub> <sup>(1)</sup>	$V_{\rm D} = 12 \text{ V}, \text{ R}_{\rm I} = 30 \Omega$	1 - 11 - 111	Min.	0.5	mA
'GT`´	$v_{\rm D} = 12 v, n_{\rm L} = 30.32$	1 - 11 - 111	Max.	10	mA
V <sub>GT</sub>	$V_D$ = 12 V, $R_L$ = 30 $\Omega$	1 - 11 - 111	Max.	1.3	V
V <sub>GD</sub>	$V_D = V_{DRM}, R_L = 3.3 \text{ k} \Omega, T_j = 150 \text{ °C}$	1 - 11 - 111	Min.	0.2	V
I <sub>H</sub> <sup>(1)</sup>	I <sub>T</sub> = 500 mA		Max.	15	mA
1	I <sub>G</sub> = 1.2 I <sub>GT</sub>	1 - 111	Max.	20	mA
۱L		II	Max.	25	mA
dV/dt <sup>(1)</sup>	$V_D = V_R = 536 V$ , gate open	T <sub>j</sub> = 125 °C	Min.	250	V/µs
	$V_D = V_R = 402 V$ , gate open	T <sub>j</sub> = 150 °C	IVIII I.	170	V/µs
(dl/dt)c <sup>(1)</sup>	(dV/dt)c = 0.1 V/µs	T <sub>j</sub> = 125 °C	Min.	6.0	A/ms
		T <sub>j</sub> = 150 °C	IVIIII.	4.2	
(dl/dt)c <sup>(1)</sup>	(dV/dt)c = 10 V/uc	T <sub>j</sub> = 125 °C	Min.	3.2	A/ms
	(dV/dt)c = 10 V/µs	T <sub>j</sub> = 150 °C	11111.	1.4	

1. For both polarities of A2 referenced to A1



Symbol	Test conditions			Value	Unit
$V_{T}^{(1)}$	I <sub>TM</sub> = 11.3 A, t <sub>p</sub> = 380 μs	T <sub>j</sub> = 25 °C	Max.	1.55	V
V <sub>t0</sub> <sup>(1)</sup>	Threshold voltage	T <sub>j</sub> = 150 °C	Max.	0.85	V
R <sub>d</sub> <sup>(1)</sup>	Dynamic resistance	T <sub>j</sub> = 150 °C	Max.	57	mΩ
		T <sub>j</sub> = 25 °C	Max.	5	μA
	$V_{DRM} = V_{RRM} = 800 V$	T <sub>j</sub> = 125 °C	wax.	0.8	
IRRM	$V_{\text{DRM}} = V_{\text{RRM}} = 600 \text{ V}$	T <sub>j</sub> = 150 °C	Max.	2.4	mA

### Table 4. Static characteristics

1. For both polarities of A2 referenced to A1

Table	5.	Thermal	resistance
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Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (AC)	3.8	°C/W
R <sub>th(j-a)</sub>	Junction to ambient (DC)	60	°C/W

# Figure 1. Maximum power dissipation versus on-state rms current (full cycle)

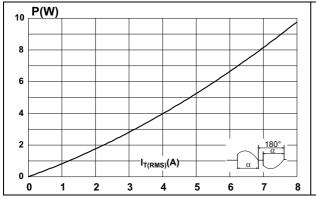
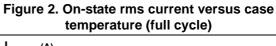


Figure 3. On-state rms current versus ambient temperature (free air convection)



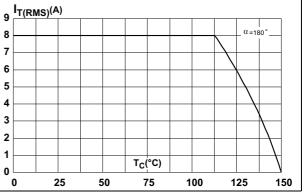
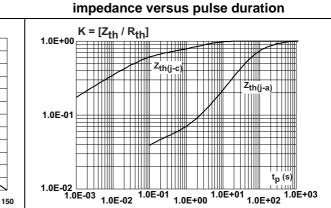


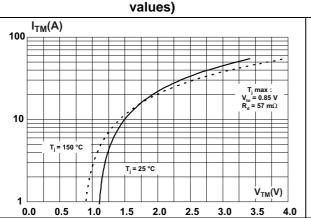
Figure 4. Relative variation of thermal



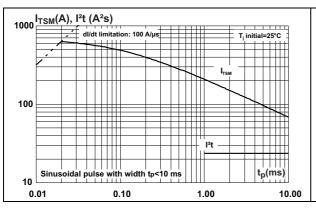
I<sub>T(RMS)</sub>(A) 3.0 α **= 180**° 2.5 2.0 1.5 1.0 0.5 ⁻T<sub>a</sub>(°C) 0.0 l 0 25 75 100 125 50

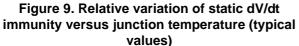


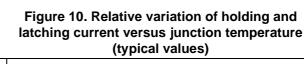
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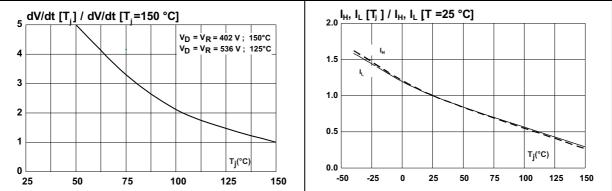


### Figure 7. Non repetitive surge peak on-state current and corresponding values of l<sup>2</sup>t









### Figure 6. Surge peak on-state current versus number of cycles

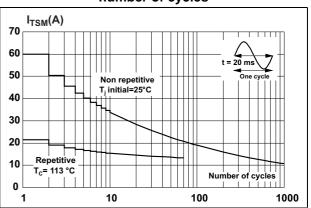
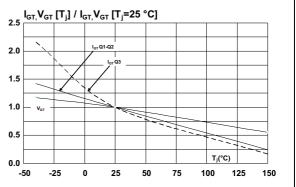


Figure 8. Relative variation of gate trigger current and gate voltage versus junction

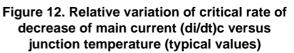


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temperature (typical values)

Figure 11. Relative variation of critical rate of decrease of main current (di/dt)c versus reapplied (dV/dt)c (typical values)



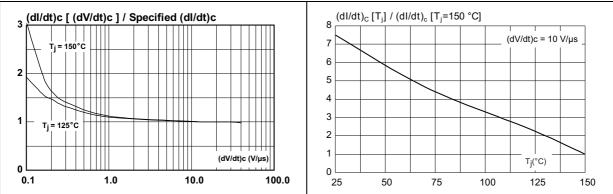
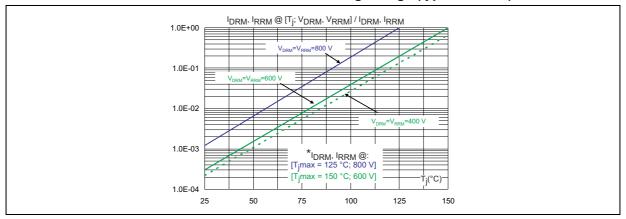


Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage (typical values)

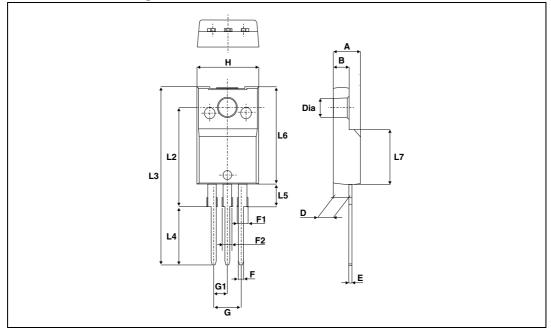




## 2 Package information

- Epoxy meets UL94, V0
- Lead-free package
- Recommended torque: 0.4 to 0.6 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: *www.st.com.* ECOPACK<sup>®</sup> is an ST trademark.







	Dimensions							
Ref.	Millim		Incl	hes				
	Min.	Max.	Min.	Max.				
А	4.4	4.6	0.173	0.181				
В	2.5	2.7	0.098	0.106				
D	2.5	2.75	0.098	0.108				
E	0.45	0.70	0.018	0.027				
F	0.75	1	0.030	0.039				
F1	1.15	1.70	0.045	0.067				
F2	1.15	1.70	0.045	0.067				
G	4.95	5.20	0.195	0.205				
G1	2.4	2.7	0.094	0.106				
Н	10	10.4	0.393	0.409				
L2	16	16 Тур.		Тур.				
L3	28.6	30.6	1.126	1.205				
L4	9.8	10.6	0.386	0.417				
L5	2.9	3.6	0.114	0.142				
L6	15.9	16.4	0.626	0.646				
L7	9.00	9.30	0.354	0.366				
Dia.	3.00	3.20	0.118	0.126				

Table 6. TO-220FPAB dimension values



## **3** Ordering information

		8 I	10 T	- 8 - 1	B FP
Triac					
Current					
8 = 8 A					
Gate sensitivity					
10 = 10 mA					
Specific application					
T = Increased (dl/dt)c and dV/dt producing reduced	TSM				
Voltage (V <sub>DRM</sub> , V <sub>RRM</sub> ) 8 = 800 V					
Package FP = TO-220FPAB					

### Figure 15. Ordering information scheme

### Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T810T-8FP	T810T-8FP	TO-220FPAB	2.0 g	50	Tube

# 4 Revision history

Table 8. Document	revision history
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Date	Revision	Changes	
05-Feb-2014	1	Initial release.	
12-Feb-2015	2	Updated Features and Table 2.	



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