

BUL49D BULB49D

High voltage fast-switching NPN power transistors

Features

- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- High ruggedness

Applications

- Electronic transformers for halogen lamps
- Flyback and forward single transistor low power converters

Description

The devices are manufactured using high voltage multi-epitaxial planar technology for high switching speeds and high voltage capability. The devices are designed for use in electronic transformer for halogen lamps.



Figure 1. Internal schematic diagram



Table 1. Device summary

Order code	Marking	Package	Packaging
BUL49D	BUL49D	TO-220	Tube
BUL49DFP	BUL49DFP	TO-220FP	Tube
BULB49D-1	BULB49D	I ² PAK	Tube
BULB49DT4	BULB49D	D ² PAK	Tape and reel

Contents

1	Electrical ratings
2	Electrical characteristics
	2.1 Electrical characteristics (curves) 5
	2.2 Test circuits
3	Package mechanical data 8
4	Packaging information13
5	Revision history14



1 Electrical ratings

Table 2.	Absolute maximum ratings	
		_

		Va		
Symbol	Parameter	D ² PAK I ² PAK TO-220	TO-220FP	Unit
V _{CES}	Collector-emitter voltage (V _{BE} =0)	8	V	
V _{CEO}	Collector-emitter voltage (I _B =0)	450		V
V _{EBO}	Emitter-base voltage (I _C =0, I _B <2 A, t_P < 10 ms)	V _{(BR)EBO}		v
Ι _C	Collector current	5		A
I _{CM}	Collector peak current (t _P < 5ms)	10		A
I _B	Base current	2		A
I _{BM}	Base peak current (t _P < 5 ms)		4	
P _{tot}	Total dissipation at $T_c \leq 25^{\circ}C$	80 34		W
T _{stg}	Storage temperature	-65 to 150		°C
TJ	Max. operating junction temperature	1	50	°C

Table	3.	Thermal	data
Iabio	U .	morman	Mara

Symbol	Parameter		D ² PAK I ² PAK TO-220	TO-220FP	Unit
R _{thj-case}	Thermal resistance junction-case	max	1.56	3.67	°C/W
R _{thj-amb}	Thermal resistance junction-ambient	max	62.5	62.5	°C/W



57

Electrical characteristics 2

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 4.	Electrical characteristics				
Symbol	Parameter	Test conditions			
I _{CES}	Collector cut-off current (V _{BE} =0)	V _{CE} = 850 V V _{CE} = 850 V T _c = 125 °C			
	Emitter cut-off current				

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} =0)	V _{CE} = 850 V V _{CE} = 850 V T _c = 125 °C			100 500	μΑ μΑ
I _{EBO}	Emitter cut-off current (I _C =0)	V _{EB} = 9 V			100	μA
V _{(BR)EBO}	Emitter-base brakdown voltage (I _C = 0)	I _E =10 mA	10		18	V
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage $(I_B = 0)$	I _C =10 mA	450			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$\begin{array}{ll} I_{\rm C} = 1 \ {\rm A} & I_{\rm B} = 0.2 \ {\rm A} \\ I_{\rm C} = 2 \ {\rm A} & I_{\rm B} = 0.4 \ {\rm A} \\ I_{\rm C} = 4 \ {\rm A} & I_{\rm B} = 0.8 \ {\rm A} \end{array}$		0.1	0.3 0.6 1.2	V V V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	$I_{C} = 1 A$ $I_{B} = 0.2 A$ $I_{C} = 4 A$ $I_{B} = 0.8 A$			1 1.3	V V
h _{FE} ⁽¹⁾	DC current gain	$\begin{array}{ccc} I_{C} = 10 \text{ mA} & V_{CE} = 5 \text{ V} \\ I_{C} = 500 \text{ mA} & V_{CE} = 5 \text{ V} \\ I_{C} = 7 \text{ A} & V_{CE} = 10 \text{ V} \end{array}$	10 4		60 10	
V _{CEW} ⁽¹⁾	Maximum collector- emitter voltage without snubber	$\begin{array}{ll} I_{C} = 8 \mbox{ A} & V_{BB} = -2.5 \mbox{ V} \\ L = 50 \mu \mbox{ H} & R_{BB} = 0 \\ t_{p} = 10 \mu \mbox{ s} \end{array}$	450			v
t _s t _f	Resistive load Storage time Fall time	$V_{CC} = 250 V$ $I_{C} = 2 A$ $I_{B1} = -I_{B2} = 400 mA$ (see <i>Figure 12</i>)	2		3 0.8	μs μs
t _s t _f	Inductive load Storage time Fall time	$\begin{array}{ll} V_{CL}\!=\!300 \ V & I_{C}\!=\!4 \ A \\ I_{B(on)}\!=\!800 \ mA & R_{BB(off)}\!=\!0 \\ V_{BE(off)}\!=\!\!-\!5 \ V & L\!=\!1 \ mH \\ (see \ Figure \ 13) \end{array}$		0.6 50	1.3 100	μs ns
V _F	Diode forward voltage	I _C = 3 A			1.5	V

1. Pulsed duration = 300 ms, duty cycle $\leq 1.5\%$

2.1 Electrical characteristics (curves)





Figure 4. Derating curves







Figure 6. Collector-emitter saturation voltage

h_{FE} =5

100

V_{CE(sat)} (V)

10

10⁰

10

10-2

10

Figure 7. Base-emitter saturation voltage



57

Figure 8. DC current gain

Figure 9. DC current gain









Figure 12. Reverse biased safe operating area



2.2 Test circuits





Figure 14. Inductive load switching test circuit





3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



Dim		mm			inch		
Dim	Min	Тур	Мах	Min	Тур	Max	
Α	4.40		4.60	0.173		0.181	
b	0.61		0.88	0.024		0.034	
b1	1.14		1.70	0.044		0.066	
С	0.48		0.70	0.019		0.027	
D	15.25		15.75	0.6		0.62	
D1		1.27			0.050		
E	10		10.40	0.393		0.409	
е	2.40		2.70	0.094		0.106	
e1	4.95		5.15	0.194		0.202	
F	1.23		1.32	0.048		0.051	
H1	6.20		6.60	0.244		0.256	
J1	2.40		2.72	0.094		0.107	
L	13		14	0.511		0.551	
L1	3.50		3.93	0.137		0.154	
L20		16.40			0.645		
L30		28.90			1.137		
ØP	3.75		3.85	0.147		0.151	
Q	2.65		2.95	0.104		0.116	





57

1

TO-220FP mechanical data							
Dim		mm.			inch		
Dim.	Min.	Тур	Max.	Min.	Тур.	Max.	
А	4.40		4.60	0.173		0.181	
В	2.5		2.7	0.098		0.106	
D	2.5		2.75	0.098		0.108	
E	0.45		0.70	0.017		0.027	
F	0.75		1.00	0.030		0.039	
F1	1.15		1.50	0.045		0.067	
F2	1.15		1.50	0.045		0.067	
G	4.95		5.20	0.195		0.204	
G1	2.40		2.70	0.094		0.106	
Н	10		10.40	0.393		0.409	
L2		16			0.630		
L3	28.6		30.6	1.126		1.204	
L4	9.80		10.60	0.385		0.417	
L5	2.9		3.6	0.114		0.141	
L6	15.90		16.40	0.626		0.645	
L7	9		9.30	0.354		0.366	
Dia	3		3.2	0.118		0.126	



I²PAK (TO-262) mechanical data

Dim	mm			inch		
	Min	Тур	Max	Min	Тур	Max
A	4.40		4.60	0.173		0.181
A1	2.40		2.72	0.094		0.107
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
С	0.49		0.70	0.019		0.027
c2	1.23		1.32	0.048		0.052
D	8.95		9.35	0.352		0.368
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
E	10		10.40	0.393		0.410
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L2	1.27		1.40	0.050		0.055





(TO-263)	mechanical	data
	meenamear	uutu

Dim		mm			inch		
	Min	Тур	Мах	Min	Тур	Мах	
A	4.40		4.60	0.173		0.181	
A1	0.03		0.23	0.001		0.009	
b	0.70		0.93	0.027		0.037	
b2	1.14		1.70	0.045		0.067	
С	0.45		0.60	0.017		0.024	
c2	1.23		1.36	0.048		0.053	
D	8.95		9.35	0.352		0.368	
D1	7.50			0.295			
E	10		10.40	0.394		0.409	
E1	8.50			0.334			
е		2.54			0.1		
e1	4.88		5.28	0.192		0.208	
Н	15		15.85	0.590		0.624	
J1	2.49		2.69	0.099		0.106	
L	2.29		2.79	0.090		0.110	
L1	1.27		1.40	0.05		0.055	
L2	1.30	1	1.75	0.051		0.069	
R		0.4			0.016		
V2	0°		8°	0°	İ	8°	



4 Packaging information

D²PAK FOOTPRINT



TAPE AND REEL SHIPMENT



57

5 Revision history

Table 5.Document revision history

Date	Revision	Changes
10-Sep-2003	1	First release.
04-May-2007	2	The document has been reformatted.
09-Jun-2008	3	Inserted devices in: D ² PAK and I ² PAK.



Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZE REPRESENTATIVE OF ST, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS, WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2008 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan -Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

