

2N2222AHR

Hi-Rel 40 V, 0.8 A NPN transistor

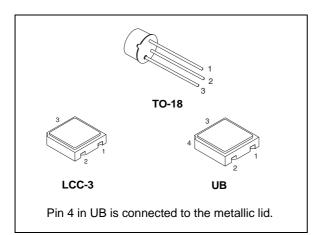
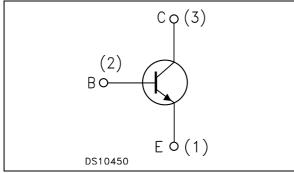


Figure 1. Internal schematic diagraml



$\mathbf{H}^{-1}\mathbf{X}\mathbf{H}^{-1}\mathbf{H}^{-1}\mathbf{X}\mathbf{H}^{-1}\mathbf{H}^{-1}\mathbf{X}\mathbf{H}^{-1}\mathbf{H}^$

Datasheet - production data

Features

Parameter	ESCC JANS				
BV _{CEO min}	40 V 50 V				
I _C (max)	0.8 A				
h _{FE} at 10 V - 150 mA	100				

- Hermetic packages
- ESCC and JANS qualified
- Up to 100 krad(Si) low dose ratee

Description

The 2N2222AHR is a silicon planar NPN transistor specifically designed and housed in hermetic packages for aerospace and Hi-Rel applications. It is available in the JAN qualification system (MIL-PRF19500 compliance) and in the ESCC qualification system (ESCC 5000 compliance). In case of discrepancies between this datasheet and the relevant agency specification, the latter takes precedence.

Device	Qualification system	Agency specification	Package	Radiation level	EPPL
JANSR2N2222AUBx	JANSR	MIL-PRF-19500/255	UB	100 krad high and low dose rate	-
JANS2N2222AUBx	JANS	MIL-PRF-19500/255	UB	-	-
2N2222ARUBx	ESCC Flight	5201/002	UB	100 krad - low dose rate	Target
2N2222AUBx	ESCC Flight	5201/002	UB	-	Target
SOC2222ARHRx	ESCC Flight	5201/002	LCC-3	100 krad - low dose rate	Yes
SOC2222AHRx	ESCC Flight	5201/002	LCC-3	-	Yes
2N2222ARHRx	ESCC Flight	5201/002	TO-18	100 krad - low dose rate	Target
2N2222AHRx	ESCC Flight	5201/002	TO-18	-	-

Table 1. Device summary

August 2015

DocID16558 Rev 20

1/24

Contents

1	Elec	trical ratings
2	Elec	trical characteristics4
	2.1	JANS electrical characteristics 4
	2.2	ESCC electrical characteristics 5
	2.3	Electrical characteristics (curves) 6
	2.4	Test circuits
3	Radi	ation hardness assurance9
4	Pacl	kage mechanical data 12
	4.1	UB package information 13
	4.2	LCC-3 package information 15
	4.3	TO-18 package information 17
5	Orde	er codes
6	Ship	ping details
	6.1	Date code
	6.2	Documentation
7	Revi	sion history



1 Electrical ratings

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage $(I_E = 0)$	75	V
V	Collector-emitter voltage ($I_B = 0$) for JANS devices	50	V
V _{CEO}	Collector-emitter voltage ($I_B = 0$) for ESCC devices	40	V
V _{EBO}	Emitter-base voltage ($I_C = 0$)	6	V
۱ _C	Collector current	0.8	Α
P _{TOT}	Total dissipation at $T_{amb} \pounds 25 \degree C$ ESCC: TO-18 LCC-3 and UB LCC-3 and UB ⁽¹⁾ JANS: LCC-3UB Total dissipation at $T_{case} \pounds 25 \degree C$ ESCC: TO-18 Total dissipation at $T_{cas}(s) = 25 \degree C$	0.5 0.5 0.73 0.5 1.8	w
	Total dissipation at T _{sp(IS)} = 25 °C JANS: UB	1	W
T _{STG}	Storage temperature	-65 to 200	°C
TJ	Max. operating junction temperature	200	°C

Table 2. Absolute m	naximum ratings
---------------------	-----------------

1. When mounted on a $15 \times 15 \times 0.6$ mm ceramic substrate.

	Table	3.	Thermal	data
--	-------	----	---------	------

Symbol	Parameter	LCC-3 and UB	TO-18	Unit
D	Thermal resistance junction-case (max) for JANS	-	-	
R _{thJC}	Thermal resistance junction-case (max) for ESCC	-	97	
Dt	Thermal resistance junction-solder pad (infinite sink) (max) for JANS	90	-	
Rt _{hJSP(IS)}	Thermal resistance junction-solder pad (infinite sink) (max) for ESCC	-	-	°C/W
R _{thJA}	Thermal resistance junction-ambient (max) for JANS	325	-	
	Thermal resistance junction-ambient (max) for ESCC	350 240 ⁽¹⁾	350	

1. When mounted on a 15 x 15 x 0.6 mm ceramic substrate.



2 Electrical characteristics

JANS and ESCC version of the products are assembled and tested in compliance with the agency specification it is qualified in. The electrical characteristics of each version are provided in dedicated tables.

 T_{case} = 25 °C unless otherwise specified.

2.1 JANS electrical characteristics

Symbol	Parameter	Test con	ditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = 75 V V _{CB} = 60 V V _{CB} = 60 V	T _{amb} = 150 °C			10 10 10	μA nA μA
I _{CES}	Collector cut-off current $(I_E = 0)$	V _{CE} = 50 V				50	nA
I _{EBO}	Emitter cut-off current $(I_{C} = 0)$	V _{EB} = 6 V V _{EB} = 4 V				10 10	μA nA
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	I _C = 10 mA		50			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 150 mA I _C = 500 mA	I _B = 15 mA I _B = 50 mA			0.3 1	V V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 150 mA I _C = 500 mA	I _B = 15 mA I _B = 50 mA	0.6		1.2 2	V V
h _{FE} ⁽¹⁾	DC current gain	$I_{C} = 0.1 \text{ mA}$ $I_{C} = 1 \text{ mA}$ $I_{C} = 10 \text{ mA}$ $I_{C} = 150 \text{ mA}$ $I_{C} = 500 \text{ mA}$ $I_{C} = 10 \text{ mA}$ $T_{amb} = -55 \text{ °C}$	$V_{CE} = 10 V$	50 75 100 100 30 35		325 300	
h _{fe}	Small signal current gain	V _{CE} = 20 V f = 100 MHz V _{CE} = 10 V f = 1 kHz	$I_{\rm C} = 20 \text{ mA}$ $I_{\rm C} = 1 \text{ mA}$	2.5 50			
C _{obo}	Output capacitance (I _E = 0)	V _{CB} = 10 V 100 kHz £ f £ 1	MHz			8	pF
C _{ibo}	Output capacitance (I _E = 0)	V _{EB} = 0.5 V 100 kHz £ f £ 1	MHz			25	pF





ſ					· · /	1	
	Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	t _{on}	Turn-on time	$V_{CC} = 30 V$ $I_{C} = 150 mA$ $I_{B1} = 15 mA$			35	ns
	t _{off}	Turn-off time	$V_{CC} = 30 V$ $I_{C} = 150 mA$ $I_{B1} = -I_{B2} = 15 mA$			300	ns

1. Pulsed duration = 300 μ s, duty cycle £ 2 %

2.2 ESCC electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = 60 V V _{CB} = 60 V T _{amb} = 150 °C	c		10 10	nΑ μΑ
I _{EBO}	Emitter cut-off current $(I_{C} = 0)$	V _{EB} = 3 V			10	nA
V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	I _C = 100 μA	75			V
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	I _C = 30 mA	40			V
V _{(BR)EBO}	Emitter-base breakdown voltage (I _C = 0)	I _E = 100 μA	6			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 150 mA I _B = 15 mA			0.3	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 150 mA I _B = 15 mA		0.87	1.2	V
h _{FE} ⁽¹⁾	DC current gain	$ \begin{array}{ll} I_{C} = 0.1 \text{ mA} & V_{CE} = 10 \text{ V} \\ I_{C} = 10 \text{ mA} & V_{CE} = 10 \text{ V} \\ I_{C} = 150 \text{ mA} & V_{CE} = 10 \text{ V} \\ I_{C} = 500 \text{ mA} & V_{CE} = 10 \text{ V} \\ I_{C} = 10 \text{ mA} & V_{CE} = 10 \text{ V} \\ I_{C} = 55 \text{ °C} \end{array} $	35 75 100 40 35		300	
h _{fe}	Small signal current gain	V _{CE} = 20 V I _C = 20 mA f = 100 MHz	3		10	
C _{obo}	Output capacitance (I _E = 0)	V _{CB} = 10 V 100 kHz £ f £ 1 MHz			8	pF

Table 5. ESCC electrical characteristics

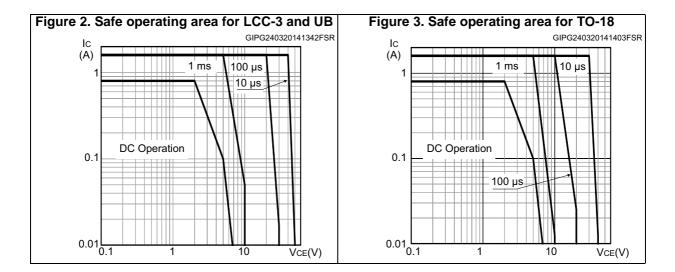


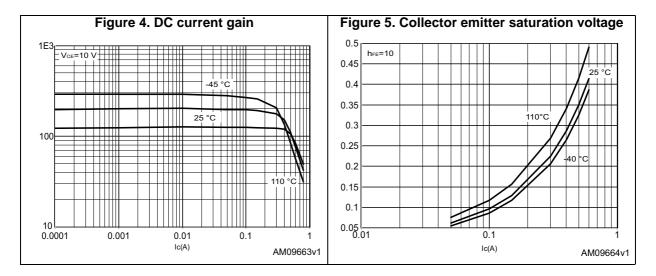
		0100011001 01101 00101 00100 (0	•	e u)		
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{on}	Turn-on time	$V_{CC} = 30 V$ $I_{C} = 150 mA$ $I_{B1} = 15 mA$			35	ns
t _{off}	Turn-off time	$V_{CC} = 30 V$ $I_{C} = 150 mA$ $I_{B1} = -I_{B2} = 15 mA$			285	ns

 Table 5. ESCC electrical characteristics (continued)

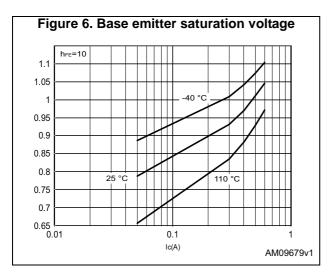
1. Pulsed duration = 300 $\mu s,$ duty cycle \leq 2 %

2.3 Electrical characteristics (curves)









2.4 Test circuits

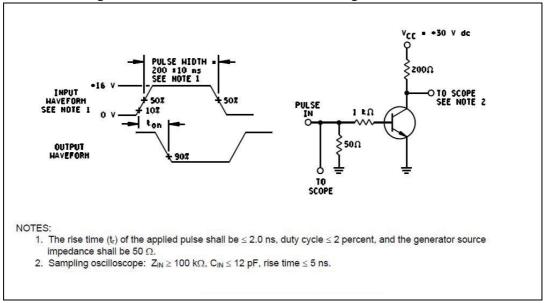


Figure 7. JANS saturated turn-on switching time test circuit



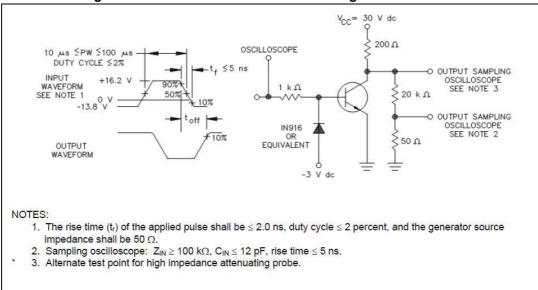
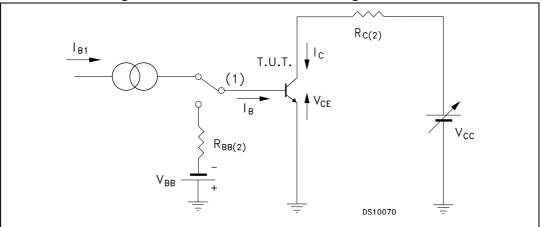


Figure 8. JANS saturated turn-off switching time test circuit





1. Fast electronic switch

2. Non-inductive resistor





3 Radiation hardness assurance

The products guaranteed in radiation within the JANS system fully comply with the MIL-PRF-19500/255 specification.

The products guaranteed in radiation within the ESCC system fully comply with the ESCC 5201/002 and ESCC 22900 specifications.

JANS radiation assurance

ST JANS parts guaranteed at 100 krad (Si), tested, in full compliancy with the MIL-PRF-19500 specification, specifically the Group D, subgroup 2 inspection, between 50 and 300 rad/s. On top of the standard JANSR high dose rate by wafer lot guarantee, ST 2N2222AHR series include an additional wafer by wafer 100 krad Low dose rate guarantee at 0.1 rad/s, identical to the ESCC 100 krad guarantee. It is supported with the same Radiation Verification Test report provided with each shipment. A brief summary of the standard High Dose Rate by wafer lot JANSR guarantee is provided below:

- All test are performed in accordance to MIL-PRF-19500 and test method 1019 of MIL-STD-750 for total Ionizing dose.
- The table below provides for each monitored parameters of the test conditions and the acceptance criteria

Symbol	Parameter	Test conditions	Va	lue	Unit
Symbol	Parameter	Test conditions	Min.	Max.	Unit
	Collector to base	V _{CB} = 75		20	μA
I _{СВО}	cutoff current	V _{CB} = 60 V		20	nA
	Emitter to base	V _{EB} = 6 V		20	μA
I _{EBO}	cutoff current	V _{EB} = 4 V		20	nA
V _{(BR)CEO}	Breakdown voltage, collector to emitter	I _C = 10 mA	50		V
I _{CES}	Collector to emitter cutoff current	V _{CE} = 50 V		100	nA
		$V_{CE} = 10 \text{ V}; \text{ I}_{C} = 0.1 \text{ mA}$	[25] ⁽¹⁾		
		V _{CE} = 10 V; I _C = 1.0 mA	[37.5] ⁽¹⁾	325	
h _{FE}	Forward-current transfer ratio	$V_{CE} = 10 \text{ V}; I_{C} = 10 \text{ mA}$	[50] ⁽¹⁾		
		V _{CE} = 10 V; I _C = 150 mA	[50] ⁽¹⁾	300	
		V _{CE} = 10 V; I _C = 500 mA	[15] ⁽¹⁾		
Mana	Collector-emitter	I _C = 150 mA; I _B = 15 mA		0.35	V
V _{CE(sat)}	saturation voltage	I _C = 500 mA; I _B = 50 mA		1.15	v
V	Base-emitter	I _C = 150 mA; I _B = 15 mA	0.6	1.38	V
V _{BE(sat)}	saturation voltage	I _C = 500 mA; I _B = 50 mA		2.3	v

Table 6. MIL-PRF-19500 (test method 1019) post radiation electrical characteristics



1. See method 1019 of MIL-STD-750 for how to determine $[h_{FE}]$ by first calculating the delta $(1/h_{FE})$ from the pre- and Post-radiation h_{FE} . Notice the $[h_{FE}]$ is not the same as h_{FE} and cannot be measured directly. The $[h_{FE}]$ value can never exceed the pre-radiation minimum h_{FE} that it is based upon.

ESCC radiation assurance

Each product lot is tested according to the ESCC basic specification 22900, with a minimum of 11 samples per diffusion lot and 5 samples per wafer, one sample being kept as unirradiated sample, all of them being fully compliant with the applicable ESCC generic and/or detailed specification.

ST goes beyond the ESCC specification by performing the following procedure:

- Test of 11 pieces by wafer, 5 biased at least 80% of V_{(BR)CEO}, 5 unbiased and 1 kept for reference
- Irradiation at 0.1 rad (Si)/s
- Acceptance criteria of each individual wafer if as 100 krad guaranteed if all 10 samples comply with the post radiation electrical characteristics provided in Table 8
- Delivery together with the parts of the radiation verification test (RVT) report of the particular wafer used to manufacture the products. This RVT includes the value of each parameter at 30, 50, 70 and 100 krad (Si) and after 24 hour annealing at room temperature and after an additional 168 hour annealing at 100°C.

Radiation test	100 krad ESCC
Wafer test	each
Part tested	5 biased + 5 unbiased
Dose rate	0.1 rad/s
Acceptance	MIL-STD-750 method 1019
Displacement damage	Optional
Agency part number (ex)	5202/001/02 ⁽¹⁾
ST part number (ex)	SOC2222ARHRG
Documents	CoC + RVT

Table 7. Radiation summary

1. Example of the 2N2222A in LCC-3 gold finish.





Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = 60 V			10	nA
I _{EBO}	Emitter cut-off current $(I_{C} = 0)$	V _{EB} = 3 V			10	nA
V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	I _C = 100 μA	75			V
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	$I_{\rm C} = 30 \text{ mA}$ $I_{\rm C} = 10 \text{ mA}$	40 50			V V
V _{(BR)EBO}	Emitter-base breakdown voltage (I _C = 0)	I _E = 100 μA	6			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 150 mA I _B = 15 mA			0.3	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 150 mA I _B = 15 mA			1.2	V
[h _{FE}] ⁽¹⁾	Post irradiation gain calculation ⁽²⁾		[17.5] [37.5] [50] [20]		300	

Table 8. ESCC 5201/002	post radiation	electrical	characteristics
	postruaration	cicotiioui	

1. Pulsed duration = 300 μ s, duty cycle £ 2 %

 The post-irradiation gain calculation of [h_{FE}], made using h_{FE} measurements from prior to and on completion of irradiation testing and after each annealing step if any, shall be as specified in MILSTD-750 method 1019.



4 Package mechanical data

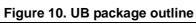
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

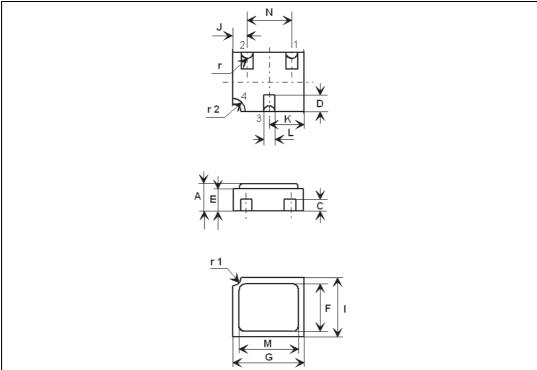
Package	Mass (g)
UB	0.06
LCC-3	0.06
TO-18	0.40

Table 9. Product mass summary



4.1 UB package information







		echanical data	
Dim.		mm.	
Dini.	Min.	Тур.	Max.
А	1.16		1.42
С	0.46	0.51	0.56
D	0.56	0.76	0.96
E	0.92	1.02	1.12
F	1.95	2.03	2.11
G	2.92	3.05	3.18
I	2.41	2.54	2.67
J	0.42	0.57	0.72
K	1.37	1.52	1.67
L	0.41	0.51	0.61
Μ	2.46	2.54	2.62
Ν	1.81	1.91	2.01
r		0.20	
r1		0.30	
r2		0.56	

Table 10	IIR	mechanical	data
Table TV.	UΒ	mechanical	uala



4.2 LCC-3 package information

Figure 11. LCC-3 package outline

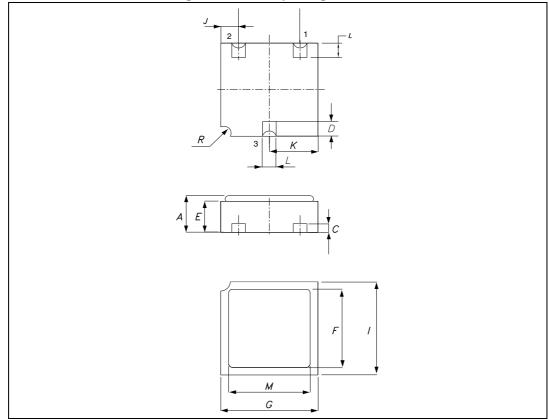




	Table 11. LCC-3	mechanical data	
Dim.		mm.	
Dim.	Min.	Тур.	Max.
A	1.16		1.42
С	0.45	0.50	0.56
D	0.60	0.76	0.91
E	0.91	1.01	1.12
F	1.95	2.03	2.11
G	2.92	3.05	3.17
I	2.41	2.54	2.66
J	0.42	0.57	0.72
К	1.37	1.52	1.67
L	0.40	0.50	0.60
М	2.46	2.54	2.62
Ν	1.80	1.90	2.00
R		0.30	

Table 11. LCC-3 mechanical data



4.3 TO-18 package information

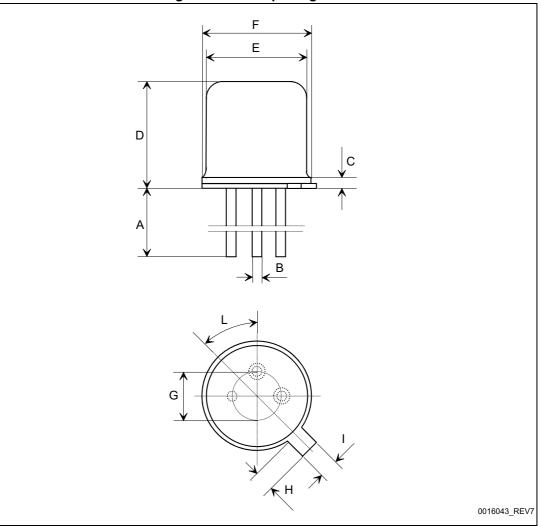


Figure 12. TO-18 package outline



Dim.		mm.	
Dini.	Min.	Тур.	Max.
А	12.70	13.20	14.20
В	0.41	0.45	0.48
С	0.36		0.47
D	4.88		5.33
E	4.63		4.70
F	5.31		5.45
G	2.49	2.54	2.59
Н	0.80	0.90	1.00
I	0.95	1.00	1.05
L	42°	45°	48°

Table 12. TO-18 mechanical data



			Table 13	Table 13. Ordering information				
CPN	Agency specification	EPPL	Quality level	Radiation level ⁽¹⁾	Package	Lead finish	Marking ⁽²⁾	Packing
J2N2222AUB1	-		Engineering model JANS	-	UB	Gold	J2222AUB1	WafflePack
2N2222AUB1		•	Engineering model ESCC		UB	Gold	2N2222AUB1	WafflePack
SOC2222A1	·	·	Engineering model ESCC		LCC-3	Gold	SOC2222A1	WafflePack
JANSR2N2222AUBG	MIL-PRF- 19500/255	ı	JANSR	100 krad high and low dose rate	UB	Gold	JSR2222	WafflePack
JANSR2N2222AUBT	MIL-PRF- 19500/255	•	NANAR	100 krad high and low dose rate	UB	Solder Dip	JSR2222	WafflePack
JANS2N2222AUBG	MIL-PRF- 19500/255	I	SNAL		UB	Gold	7222S	WafflePack
JANS2N2222AUBT	MIL-PRF- 19500/255		SNAL		UB	Solder Dip	7222S	WafflePack
2N2222ARUBG	5201/002/11R	Target	ESCC Flight	100 krad - low dose rate	UB	Gold	520100211R	WafflePack
2N2222ARUBT	5201/002/12R	Target	ESCC Flight	100 krad - low dose rate	UB	Solder Dip	520100212R	WafflePack
2N2222AUBG	5201/002/11	Target	ESCC Flight		UB	Gold	520100211	WafflePack
2N2222AUBT	5201/002/12	Target	ESCC Flight		UB	Solder Dip	520100212	WafflePack
SOC2222ARHRG	5201/002/04R	Yes	ESCC Flight	100 krad - low dose rate	LCC-3	Gold	520100204R	WafflePack
SOC2222ARHRT	5201/002/05R	Yes	ESCC Flight	100 krad - low dose rate	LCC-3	Solder Dip	520100205R	WafflePack
SOC2222AHRG	5201/002/04	Yes	ESCC Flight		LCC-3	Gold	520100204	WafflePack
SOC2222AHRT	5201/002/05	Yes	ESCC Flight		LCC-3	Solder Dip	520100205	WafflePack
2N2222ARHRG	5201/002/01R	Target	ESCC Flight	100 krad - low dose rate	TO-18	Gold	520100201R	Strip Pack
2N2222ARHRT	5201/002/02R	Target	ESCC Flight	100 krad - low dose rate	TO-18	Solder Dip	520100202R	Strip Pack

DocID16558 Rev 20

Downloaded from Arrow.com.

Order codes

S

57

			Table 13. Orde	Table 13. Ordering information (continued)	tinued)			
CPN	Agency specification	EPPL	Quality level	Radiation level ⁽¹⁾	Package	Package Lead finish	Marking ⁽²⁾	Packing
2N2222AHRG	5201/002/01	ı	ESCC Flight	-	TO-18	Gold	520100201	Strip Pack
2N2222AHRT	5201/002/02	T	ESCC Flight	-	TO-18	TO-18 Solder Dip	520100202	Strip Pack

High dose rate as per MIL-PRF-19500 specification group D, subgroup 2 inspection. Low dose rate as per ESCC specification 22900. ..

Specific marking only. The full marking includes in addition: For the Engineering Models: ST logo, date code; country of origin (FR), For ESCC flight parts: ST logo, date code, country of origin (FR), ESA logo, serial number of the part within the assembly lot. For JANS flight parts: ST logo, date code, country of origin (FR), manufacturer code (CSTM), serial number of the part within the assembly lot. ы К

Contact ST sales office for information about the specific conditions for:

- Products in die form T
- Other JANS quality levels I
 - Tape and reel packing I

20/24



6 Shipping details

6.1 Date code

Date code xyywwz is structured as below table:

Table 14. Date code					
	x	уу	ww	z	
EM (ESCC & JANS)	3	last two digits of the year	week digits	lot index in the week	
ESCC FLIGHT	-				
JANS FLIGHT (diffused in Singapore)	W				

6.2 Documentation

Quality level	Radiation level	Documentation	
Engineering model	-	-	
JANS Flight	-	Certificate of conformance	
JANSR Flight	MIL-STD 100krad	Certificate of conformance 50 rad/s radiation verification test report	
	ST 100Krad	Certificate of conformance 0.1 rad/s radiation verification test report on each wafer	
ESCC Flight	-	Certificate of conformance	
	100 krad	Certificate of conformance	
		0.1 rad/s radiation verification test report	



7 Revision history

Date	Revision	Changes
04-Jan-2010	1	Initial release
16-Apr-2010	2	Added Table 1 on page 1
09-Jul-2010	3	Modified: Table 1 on page 1 and Table 12 on page 18
30-Nov-2011	4	 Modified: Table 5 on page 5 Added: Section 2.3: Electrical characteristics (curves) Modified: Table 1 and 2 Added: Table 2, 11, 12 Minor text changes in the document title and description on the cover page.
12-Dec-2011	5	Minor text changes to improve readability
17-Apr-2012	6	 Updated: Title and description in cover page. P_{TOT} in <i>Table 2: Absolute maximum ratings</i>. The entire <i>Section 2: Electrical characteristics</i>. Added: Table 3: Thermal data, Section 3: Radiation hardness assurance and Table 13: Ordering information. Figure 7: JANS saturated turn-on switching time test circuit and Figure 8: JANS saturated turn-off switching time test circuit. Section 6: Shipping details.
19-Apr-2012	7	Updated titles in Figure 7: JANS saturated turn-on switching time test circuit and Figure 8: JANS saturated turn-off switching time test circuit.
24-Apr-2012	8	Updated R _{thJA} value in <i>Table 3: Thermal data</i> .
14-May-2012	9	Updated Table 13: Ordering information.
21-Feb-2013	10	Table 1: Device summary and Table 13: Ordering information have been updated.Updated text in Section 3: Radiation hardness assurance.
04-Apr-2013	11	Inserted Table 7: Radiation summary
06-Jun-2013	12	Updated package name for UB.
18-Sep-2013	13	Table 1: Device summary and Table 13: Ordering information have been updated.
25-Mar-2014	14	Table 1: Device summary and Table 13: Ordering information have been updated.Updated Section 3: Radiation hardness assurance.and Section 4: Package mechanical dataInserted Figure 2: Safe operating area for LCC-3 and UB and Figure 3: Safe operating area for TO-18

Table 16. Document revision history

22/24



Date	Revision	Changes		
01-Apr-2014	15	Modified note in package silhouette on cover page.		
29-May-2014	16	Updated Table 1: Device summary and Table 13: Ordering information.		
17-Feb-2015	17	Updated Table 1.: Device summary		
		Minor text changes.		
27-Feb-2015	18	Minor text changes		
05-May-2015	19	Updated Table 1.: Device summary		
		Minor text changes.		
21-Aug-2015	20	Updated: Section 4.3: TO-18 package information		
		Minor text changes		

Table 16. Document revision history (continued)



IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics – All rights reserved

