

# PNP Epitaxial Silicon Transistor

# **BC638**

#### **Features**

- Switching and Amplifier Applications
- Complement to BC637
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **ABSOLUTE MAXIMUM RATINGS**

(Values are at  $T_A = 25^{\circ}C$  unless otherwise noted)

Parameter	Symbol	Value	Unit
Collector–Emitter Voltage at $R_{BE} = 1 \text{ k}\Omega$	V <sub>CER</sub>	-60	V
Collector-Emitter Voltage	V <sub>CES</sub>	-60	V
Collector-Emitter Voltage	$V_{CEO}$	-60	V
Emitter-Base Voltage	$V_{EBO}$	<b>-</b> 5	V
Collector Current	I <sub>C</sub>	-1	Α
Peak Collector Current	I <sub>CP</sub>	-1.5	Α
Base Current	Ι <sub>Β</sub>	-100	mA
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>STG</sub>	-65 to 150	°C

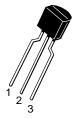
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS (Note 1)

(Values are at T<sub>A</sub> = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Power Dissipation	P <sub>D</sub>	1	W
Dissipation Derate Above 25°C	P <sub>D</sub>	8	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	°C/W

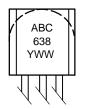
<sup>1.</sup> PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.



TO-92-3 CASE 135AR Bent Lead

- 1. Emitter
- 2. Collector
- 3. Base

#### **MARKING DIAGRAM**



A = Assembly Code BC638 = Device Code YWW = Date Code

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

### **BC638**

#### **ELECTRICAL CHARACTERISTICS**

(Values are at  $T_A = 25$ °C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-60			V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = -30 \text{ V}, I_{E} = 0$			-0.1	μΑ
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = -5 \text{ V}, I_{C} = 0$			-10	μΑ
h <sub>FE1</sub>	DC Current Gain	$V_{CE} = -2 \text{ V}, I_{C} = -5 \text{ mA}$	25			
h <sub>FE2</sub>		$V_{CE} = -2 \text{ V}, I_{C} = -150 \text{ mA}$	40		160	
h <sub>FE3</sub>		$V_{CE} = -2 \text{ V}, I_{C} = -500 \text{ mA}$	25			
V <sub>CE</sub> (sat)	Collector–Emitter Saturation Voltage	$I_C = -500 \text{ mA}, I_B = -50 \text{ mA}$			-0.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	$V_{CE} = -2 \text{ V}, I_{C} = -500 \text{ mA}$			-1	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}, f = 50 \text{ MHz}$		100		MHz

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### **ORDERING INFORMATION**

Part Number	Top Mark	Package	Shipping
BC638TA	BC638	TO-92-3, case 135AR (Pb-Free)	2,000 Units / Fan Fold

#### TYPICAL PERFORMANCE CHARACTERISTICS

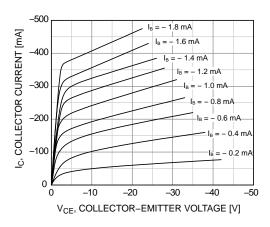


Figure 1. Static Characteristic

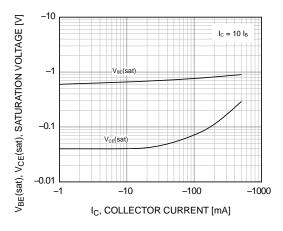


Figure 3. Base–Emitter Saturation Voltage and Collector–Emitter Saturation Voltage

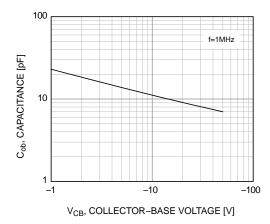


Figure 5. Collector Output Capacitance

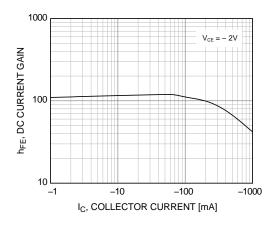


Figure 2. DC Current Gain

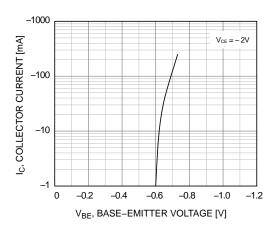
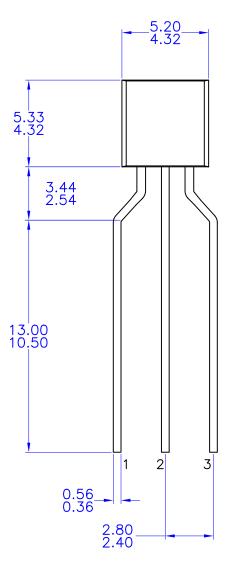


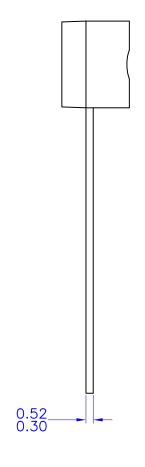
Figure 4. Base-Emitter On Voltage

## TO-92 3 4.83x4.76 LEADFORMED

CASE 135AR ISSUE O

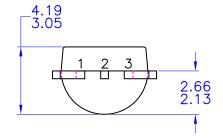
**DATE 30 SEP 2016** 





NOTES: UNLESS OTHERWISE SPECIFIED

- A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DRAWING CONFORMS TO ASME Y14.5M-1994



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