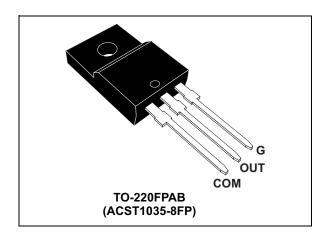


### **ACST1035-8FP**

### Overvoltage protected AC switch

Datasheet - production data



#### **Features**

- Triac with self overvoltage protection
- High static immunity and dynamic commutation
- 800 V V<sub>DRM</sub> / V<sub>RRM</sub>
- High junction temperature: T<sub>i</sub> = 150 °C max
- Complies with UL standards (File ref: E81734)
- TO-220FPAB-insulated package 1500V<sub>RMS</sub>
- ECOPACK<sup>®</sup>2 and RoHs compliant component

### **Applications**

• Motor control for home appliance:

This is information on a product in full production.

- Universal drum motor of washing machine
- Compressor of fridge or air conditioner
- A/C compressor

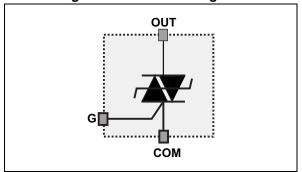
#### **Description**

The ACST1035-8FP offers 800 V blocking voltage, high commutation and noise immunity, all required for inductive load control.

Its high maximum junction temperature,  $T_j = 150$  °C offers easier thermal management in application.

ACST1035-8FP enables applications to be compliant with IEC 61000-4-4 and IEC 61000-4-5.

Figure 1. Functional diagram



**Table 1. Device summary** 

Order code	Package	V <sub>DRM</sub> /V <sub>RRM</sub>	I <sub>GT</sub>
ACST1035-8FP	TO-220FPAB	800 V	35 mA

Characteristics ACST1035-8FP

## 1 Characteristics

Table 2. Absolute ratings (limiting values)

Symbol	Parameter	Test co	nditions	Value	Unit
I <sub>T(RMS)</sub>	On-state rms current (full sine wave)	T <sub>c</sub> = 108 °C		10	Α
1.	Non repetitive surge peak on-state current	$t_p = 16.7 \text{ ms}$		95	Α
I <sub>TSM</sub>	(T <sub>j</sub> initial = 25 °C)	t <sub>p</sub> = 20 ms		90	^
l <sup>2</sup> t	I <sup>2</sup> t value for fusing (full cycle sine wave)	t <sub>p</sub> = 10 ms		54	A²s
dl/dt	Critical rate of rise on-state current $I_G = 2 \times I_{GT}$ , $t_r \le 100 \text{ ns}$	F = 120 Hz T <sub>j</sub> = 150 °C		100	A/µs
V <sub>DRM</sub> /V <sub>RRM</sub>	Repetitive peak off-state voltage	T <sub>j</sub> = 150 °C		800	V
V <sub>PP</sub> <sup>(1)</sup>	Non repetitive line peak pulse voltage	T <sub>j</sub> = 25 °C		2	kV
(dl/dt) <sub>BO</sub> <sup>(1)</sup>	Non repetitive critical current rate of rise at breakover	T <sub>j</sub> = 25 °C		150	A/µs
I <sub>GM</sub>	Peak gate current	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 150 °C	1	Α
$P_{GM}$	Peak gate power	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 150 °C	10	W
$P_{G(AV)}$	Average gate power dissipation $T_j = 150  ^{\circ}\text{C}$		0.1	W	
T <sub>stg</sub>	Storage junction temperature range				°C
Tj	Operating junction temperature range				°C
T <sub>L</sub>	Maximum lead temperature for soldering during 10 s				°C
V <sub>ins(rms)</sub>	Insulation rms voltage (60 seconds)			1.500	V

<sup>1.</sup> according to test described by standard IEC 61000-4-5 (see Figure 19)

**Table 3. Electrical characteristics** 

Symbol	Test conditions	Quadrant	Tj		Value	Unit
	1 V 40 V D 60 C		25 °C	MAX.	35	mΛ
I <sub>GT</sub>	$V_D = 12 \text{ V}, R_L = 33 \Omega$	VD = 12 V, NL = 33 S2	MIN.	1.75	mA	
V <sub>GT</sub>	$V_D = 12 \text{ V}, R_L = 33 \Omega$	1 - 11 - 111	25 °C	MAX.	1.0	V
V <sub>GD</sub>	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$ I - II - III		150 °C	MIN.	0.2	V
I <sub>H</sub> <sup>(1)</sup>	I <sub>T</sub> = 500 mA, gate open		25 °C	MAX.	30	mA
IL	I <sub>G</sub> = 1.2 x I <sub>GT</sub>	1 - 11 - 111	25 °C	MAX.	40	mA
dV/dt <sup>(1)</sup>	\/ - 679/ \/ \/ \/ gata anan		125 °C	MIN.	4000	V/µs
u v/ut· /	$V_D = 67\% V_{DRM}/V_{RRM}$ , gate open		150 °C	MIN.	2000	ν/μ5
(dl/dt)c <sup>(1)</sup> Without snubber			125 °C	MIN.	10	A/ms
(ui/ut)c· /	Williout Silubbei		150 °C	MIN.	5	AVIIIS
V <sub>CL</sub>	I <sub>CL</sub> = 0.1 mA, t <sub>p</sub> = 1 ms			MIN.	850	V

<sup>1.</sup> For both polarities of OUT pin referenced to COM pin

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ACST1035-8FP Characteristics

Tabla	1	Static characteristic	•

Symbol	Test conditions			Value	
V <sub>TM</sub> <sup>(1)</sup>	$I_{TM} = 14.1 \text{ A}, t_p = 380 \ \mu s$	T <sub>j</sub> = 25 °C	MAX.	1.5	V
V <sub>T0</sub> <sup>(1)</sup>	Threshold voltage	T <sub>j</sub> = 150 °C	MAX.	0.9	V
R <sub>d</sub> <sup>(1)</sup>	Dynamic resistance	T <sub>j</sub> = 150 °C	MAX.	40	mΩ
		T <sub>j</sub> = 25 °C		1	μΑ
I <sub>DRM</sub> I <sub>RRM</sub>	$V_D = V_{DRM} / V_R = V_{RRM}$	T <sub>j</sub> = 125 °C	MAX.	500	μΑ
IXIXIVI		T <sub>j</sub> = 150 °C		1.2	mA

<sup>1.</sup> For both polarities of OUT pin referenced to COM pin

**Table 5. Thermal characteristics** 

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (AC)	3.5	°C/W
R <sub>th(j-a)</sub>	Junction to ambient (AC)	60	°C/W

Figure 2. Maximum power dissipation versus rms on-state current (full cycle)

rms on-state current (full cycle)

temperature

P(W)

12

11

10

9

8

7

6

5

4

3

10

9

8

7

6

5

4

3

Figure 3. On-state rms current versus case temperature (full cycle)

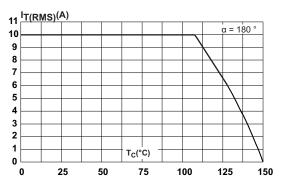


Figure 4. On-state rms current versus ambient temperature (free air convection)

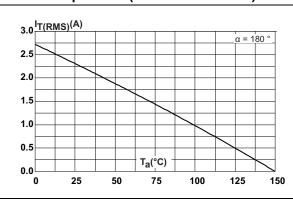
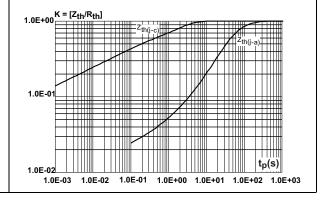


Figure 5. Relative variation of thermal impedance versus pulse duration



Characteristics ACST1035-8FP

Figure 6. On-state characteristics (maximum values)

100 <sup>I</sup><sub>™</sub>(A)

10

1

maximum values)

3

 $V_{TM}(V)$ 

5

4

Figure 7. Surge peak on-state current versus number of cycles

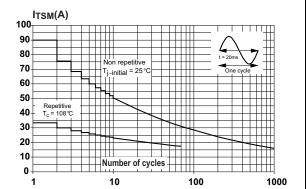


Figure 8. Non repetitive surge peak on-state current for a sinusoidal pulse with width tp < 10 ms, and corresponding value of I<sup>2</sup>

T<sub>j</sub> = 25°C

2

Figure 9. Relative variation of gate trigger current and gate trigger voltage versus junction temperature (typical values)

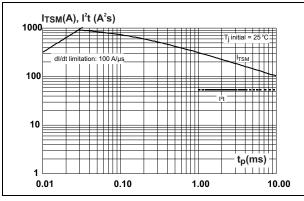
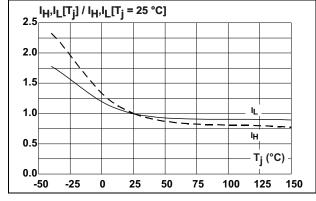
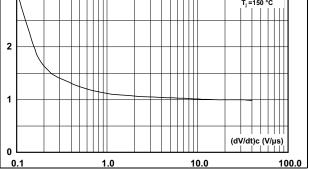


Figure 10. Relative variation of holding current and latching current versus junction temperature (typical values)

Figure 11. Relative variation of critical rate of decrease of main current (dl/dt)c versus reapplied (dV/dt)c

(dl/dt)c [ (dV/dt)c ] / Specified (dl/dt)c



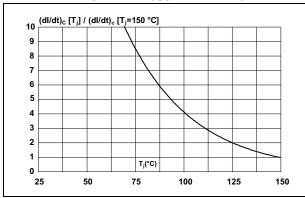


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ACST1035-8FP Characteristics

Figure 12. Relative variation of critical rate of decrease of main current versus junction temperature (typical values)

Figure 13. Relative variation of static dV/dt immunity versus junction temperature



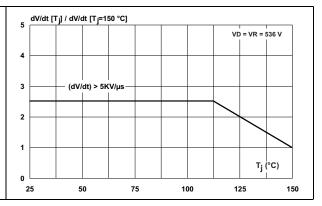
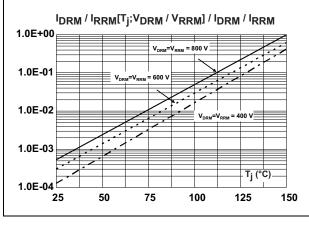
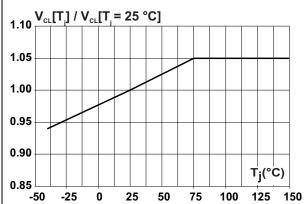


Figure 14. Relative variation of leakage current versus junction temperature for different values of blocking voltage (typical values)

Figure 15. Relative variation of the maximum clamping voltage versus junction temperature (minimum values)





## 2 Application information

## 2.1 Typical application description

The ACST1035-8FP device has been designed to control medium power load, such as AC motors in home appliances. Thanks to its thermal and turn-off commutation performances, the ACST1035-8FP switch is able to drive an inductive load up to 10 A with no turn-off additional snubber. It also provides high thermal performances in static and transient modes such as the compressor inrush current or high torque operating conditions of an AC motor.

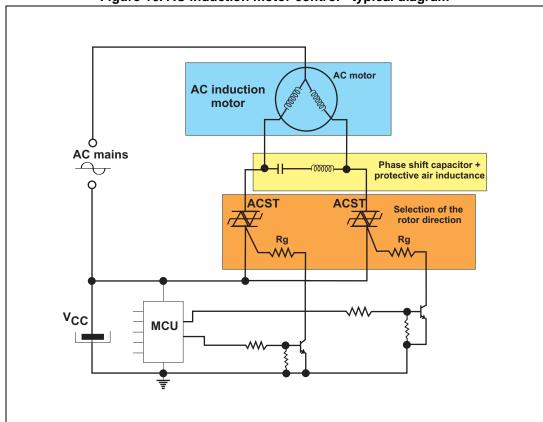


Figure 16. AC induction motor control - typical diagram

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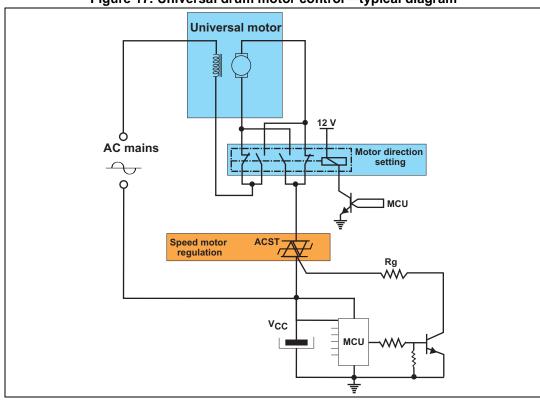


Figure 17. Universal drum motor control – typical diagram

The ACST1035-8FP device is also very effective in controlling resistive loads.

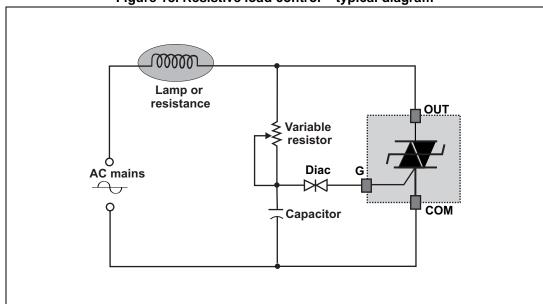


Figure 18. Resistive load control – typical diagram

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#### 2.2 AC line transient voltage ruggedness

In comparison with standard Triac, which need additional protection components against surge voltage, the ACST1035-8FP is self-protected against overvoltage, specified by the new parameter  $V_{CL}$ . The ACST1035-8FP switch can safely withstand AC line transient voltages either by clamping the low energy spikes, such as the inductive spikes at switch-off, or by switching to the on state (for less than 10 ms) to dissipate higher energy shocks through the load. This safety feature works even with high turn-on current ramp-up.

The test circuit of *Figure 19* represents the ACST1035-8FP application, and is used to stress the ACST switch according to the IEC 61000-4-5 standard conditions. With the additional effect of the load which limits the current, the ACST switch withstands the voltage spikes up to 2 kV on top of the peak line voltage. The protection is based on an overvoltage crowbar technology. The ACST1035-8FP folds back safely to the on state as shown in *Figure 20*. The ACST1035-8FP recovers its blocking voltage capability after the surge and the next zero crossing current. Such a non repetitive test can be done at least 10 times on each AC line voltage polarity.

R = 11  $\Omega$ , L = 3  $\mu$ H, V<sub>pp</sub> = 2 kV Rg = 62  $\Omega$ 2 kV surge

Regene

AC mains

AC mains

Figure 19. Overvoltage ruggedness test circuit for resistive and inductive loads for IEC 61000-4-5 standards

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V<sub>peak</sub>
1.2/50 μs voltage surge

V<sub>peak=180 A</sub>
8/20 μs current surge
(dl/dt)<sub>BO</sub> = 150 A/μs

Figure 20. Typical voltage and current waveforms across the ACST1035-8FP during IEC 61000-4-5 standard test



**Package information ACST1035-8FP** 

#### **Package information** 3

- Lead-free package
- Recommended torque: 0.4 to 0.6 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

山山 mim ф Н L6 L2 **L7** \_3 L5 L4 F2 G1 G

Figure 21. TO-220FPAB dimension definitions

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ACST1035-8FP Package information

Table 6. TO-220FPAB dimension values

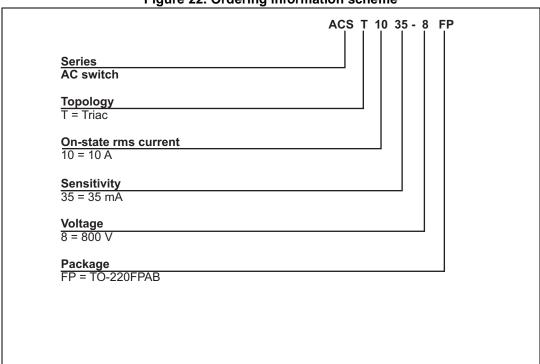
		Dimensions				
Ref.	Millim	neters	Inc	hes		
	Min.	Max.	Min.	Max.		
А	4.4	4.6	0.173	0.181		
В	2.5	2.7	0.098	0.106		
D	2.5	2.75	0.098	0.108		
E	0.45	0.70	0.018	0.027		
F	0.75	1	0.030	0.039		
F1	1.15	1.70	0.045	0.067		
F2	1.15	1.70	0.045	0.067		
G	4.95	5.20	0.195	0.205		
G1	2.4	2.7	0.094	0.106		
Н	10	10.4	0.393	0.409		
L2	16	Гур.	0.63	Тур.		
L3	28.6	30.6	1.126	1.205		
L4	9.8	10.6	0.386	0.417		
L5	2.9	3.6	0.114	0.142		
L6	15.9	16.4	0.626	0.646		
L7	9.00	9.30	0.354	0.366		
Dia.	3.00	3.20	0.118	0.126		



Ordering information ACST1035-8FP

# 4 Ordering information

Figure 22. Ordering information scheme



**Table 7. Ordering information** 

Order code	Marking	Package	Weight	Base qty	Packing mode
ACST1035-8FP	ACST1035-8FP	TO-220FPAB	2.0 g	50	Tube

# 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
21-Feb-2014	1	First issue.

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