Preferred Devices

General Purpose Transistors

PNP Silicon

Features

• Pb-Free Packages are Available

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	BC856 BC857 58, BC859	V _{CEO}	-65 -45 -30	V
Collector-Base Voltage BC856 BC857 BC858, BC859		V _{CBO}	-80 -50 -30	V
Emitter-Base Voltage	V _{EBO}	-5.0	V	
Collector Current - Continu	I _C	-100	mAdc	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

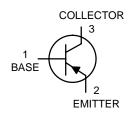
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

- 1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = $0.4 \times 0.3 \times 0.024$ in 99.5% alumina.



ON Semiconductor®

http://onsemi.com





SOT-23 CASE 318 STYLE 6

MARKING DIAGRAM



xx = Device Code

xx = (Refer to page 5)

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteris	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector – Emitter Breakdown Voltage (I _C = –10 mA)	V _{(BR)CEO}	-65 -45 -30	- - -	- - -	V	
Collector – Emitter Breakdown Voltage ($I_C = -10 \mu A, V_{EB} = 0$)	V _(BR) CES	-80 -50 -30	- - -	- - -	V	
Collector – Base Breakdown Voltage (I _C = –10 μA)	BC856 Series BC857 Series BC858, BC859 Series	V _{(BR)CBO}	-80 -50 -30	- - -	- - -	V
Emitter – Base Breakdown Voltage (I _E = –1.0 μA)	V _{(BR)EBO}	-5.0 -5.0 -5.0	- - -	- - -	V	
Collector Cutoff Current ($V_{CB} = -30 \text{ V}$) ($V_{CB} = -30 \text{ V}$, T_{AB}	I _{CBO}	_ _	_ _	-15 -4.0	nA μA	
ON CHARACTERISTICS			•	•		
$(I_C = -10 \mu A, V_{CE} = -5.0 \text{ V})$ BC856	6A, BC857A, BC858A 6B, BC857B, BC858B 7C, BC858C	h _{FE}	- - -	90 150 270	- - -	_
$(I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V})$ BC856 BC856 BC857		125 220 420	180 290 520	250 475 800		
Collector – Emitter Saturation Voltage (I_C = -10 mA, I_B = -0.5 mA) (I_C = -100 mA, I_B = -5.0 mA)	V _{CE(sat)}	_ _	- -	-0.3 -0.65	V	
Base – Emitter Saturation Voltage (I_C = -10 mA, I_B = -0.5 mA) (I_C = -100 mA, I_B = -5.0 mA)	V _{BE(sat)}	- -	-0.7 -0.9	- -	V	
Base – Emitter On Voltage (I_C = -2.0 mA, V_{CE} = -5.0 V) (I_C = -10 mA, V_{CE} = -5.0 V)	V _{BE(on)}	-0.6 -	- -	-0.75 -0.82	V	
SMALL-SIGNAL CHARACTERISTICS						
Current-Gain - Bandwidth Product (I _C = -10 mA, V _{CE} = -5.0 Vdc, f = 100	f _T	100	_	-	MHz	
Output Capacitance (V _{CB} = -10 V, f = 1.0 MHz)	C _{ob}	-	-	4.5	pF	
Noise Figure (I_C = -0.2 mA, V_{CE} = -5.0 Vdc, R_S = 2. BC856 BC858	NF	_ _	- -	10 4.0	dB	

BC857/BC858/BC859

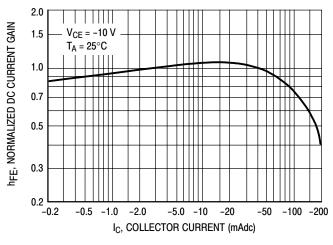


Figure 1. Normalized DC Current Gain

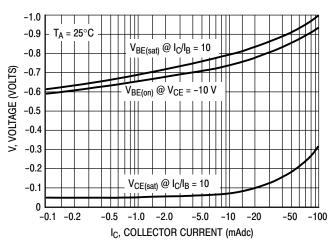


Figure 2. "Saturation" and "On" Voltages

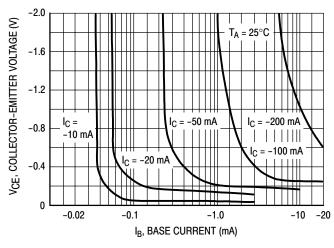


Figure 3. Collector Saturation Region

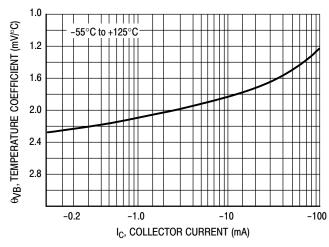


Figure 4. Base-Emitter Temperature Coefficient

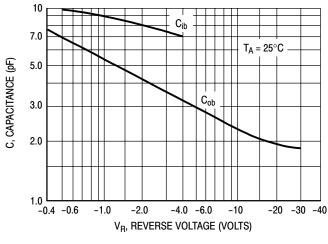


Figure 5. Capacitances

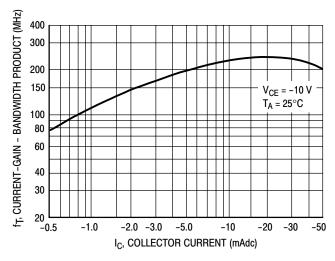


Figure 6. Current-Gain - Bandwidth Product

BC856

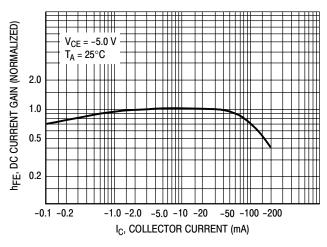


Figure 7. DC Current Gain

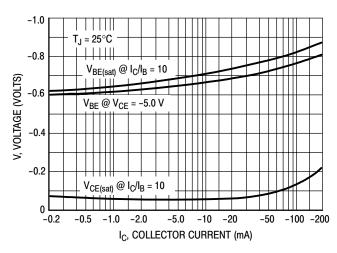


Figure 8. "On" Voltage

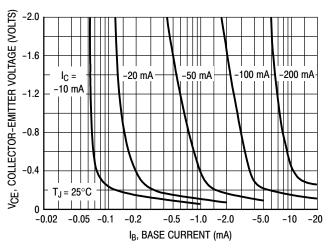


Figure 9. Collector Saturation Region

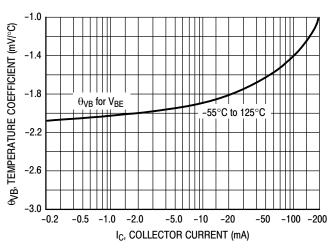


Figure 10. Base-Emitter Temperature Coefficient

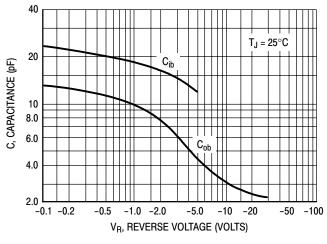


Figure 11. Capacitance

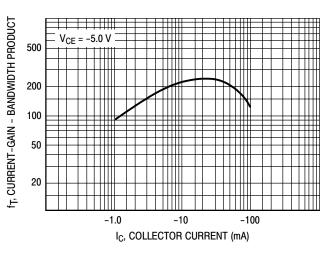


Figure 12. Current-Gain - Bandwidth Product

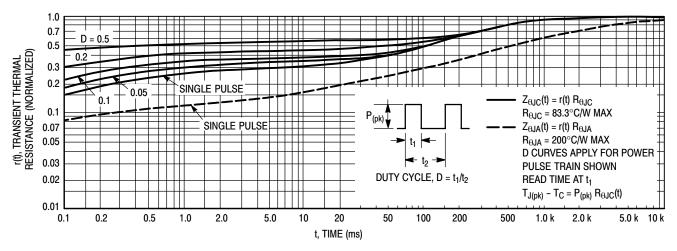


Figure 13. Thermal Response

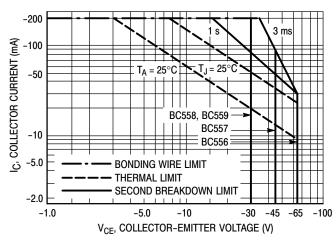


Figure 14. Active Region Safe Operating Area

The safe operating area curves indicate I_C – V_{CE} limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 14 is based upon $T_{J(pk)} = 150$ °C; T_C or T_A is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided $T_{J(pk)} \le 150$ °C. $T_{J(pk)}$ may be calculated from the data in Figure 13. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
BC856ALT1		SOT-23	
BC856ALT1G	3A	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BC856ALT3	3A	SOT-23	
BC856ALT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel
BC856BLT1		SOT-23	
BC856BLT1G	3B	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BC856BLT3	ЭБ	SOT-23	
BC856BLT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

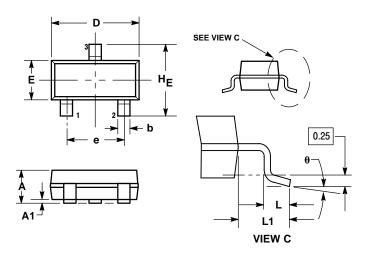
ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
BC857ALT1		SOT-23	
BC857ALT1G	3E	SOT-23 (Pb-Free)	0 000 /Taxa 0 Paul
BC857BLT1		SOT-23	3,000 / Tape & Reel
BC857BLT1G	25	SOT-23 (Pb-Free)	
BC857BLT3	3F	SOT-23	
BC857BLT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel
BC857CLT1		SOT-23	
BC857CLT1G	3G	SOT-23 (Pb-Free)	
BC858ALT1		SOT-23	
BC858ALT1G	31	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BC858BLT1		SOT-23	
BC858BLT1G	ЗК	SOT-23 (Pb-Free)	
BC858BLT3		SOT-23	
BC858BLT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel
BC858CLT1		SOT-23	
BC858CLT1G	3L	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BC858CLT3		SOT-23	
BC858CLT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel
BC859BLT1		SOT-23	
BC859BLT1G	45	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BC859BLT3	4B	SOT-23	
BC859BLT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel
BC859CLT1		SOT-23	
BC859CLT1G	40	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BC859CLT3	4C	SOT-23	
BC859CLT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AM



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD
 FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

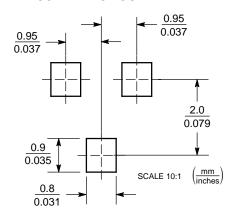
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

STYLE 6:

BASE PIN 1.

- **EMITTER**
- 3 COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative

BC856ALT1/D