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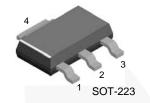
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# NZT660 / NZT660A PNP Low Saturation Transistor

# Description

These devices are designed with high-current gain and low saturation voltage with collector currents up to 3 A continuous.



1. Base 2,4. Collector 3. Emitter

# **Ordering Information**

Part Number	Number Marking Package		Packing Method	
NZT660	660	SOT-223 4L	Tape and Reel	
NZT660A	660A	SOT-223 4L	Tape and Reel	

# Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at Values are at T<sub>A</sub> = 25°C unless otherwise noted.

Symbol	Parameter	Val	Unit	
	Falance	NZT660	NZT660A	Onne
V <sub>CEO</sub>	Collector-Emitter Voltage	-60	-60	V
V <sub>CBO</sub>	Collector-Base Voltage	-80	-60	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	-5	V
Ι <sub>C</sub>	Collector Current - Continuous	-3	-3	А
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	-55 to +150	°C

#### Notes:

1. These ratings are based on a maximum junction temperature of 150°C.

2. These are steady state limits. Fairchild Semiconductor should be consulted on application involving pulsed or low-duty cycle operation.

## Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
PD	Total Device Dissipation	2	W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	62.5	°C/W

#### Note:

3. PCB size: FR-4 76 x 114 x 1.57 mm<sup>3</sup> (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

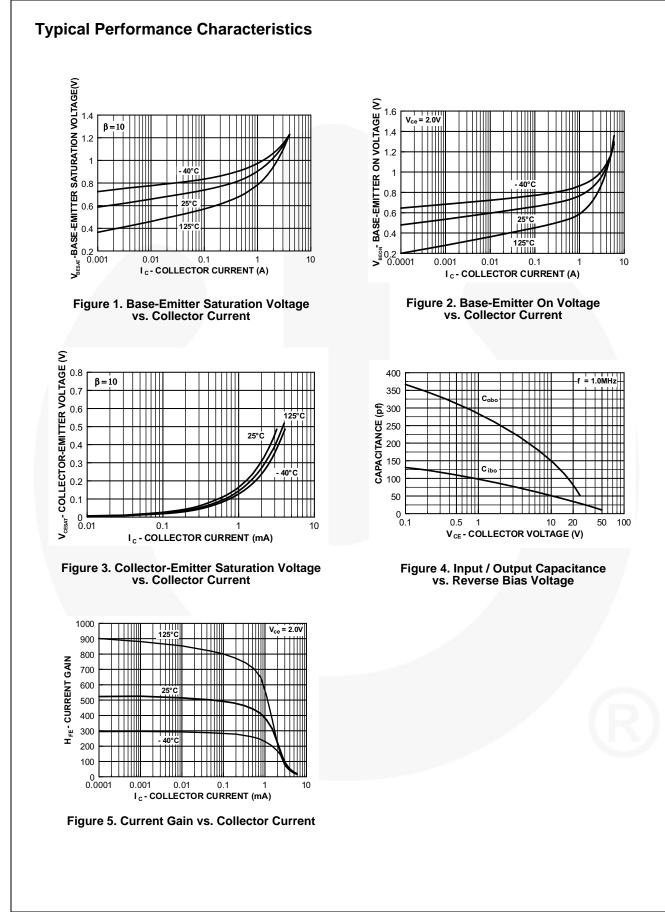
## **Electrical Characteristics**

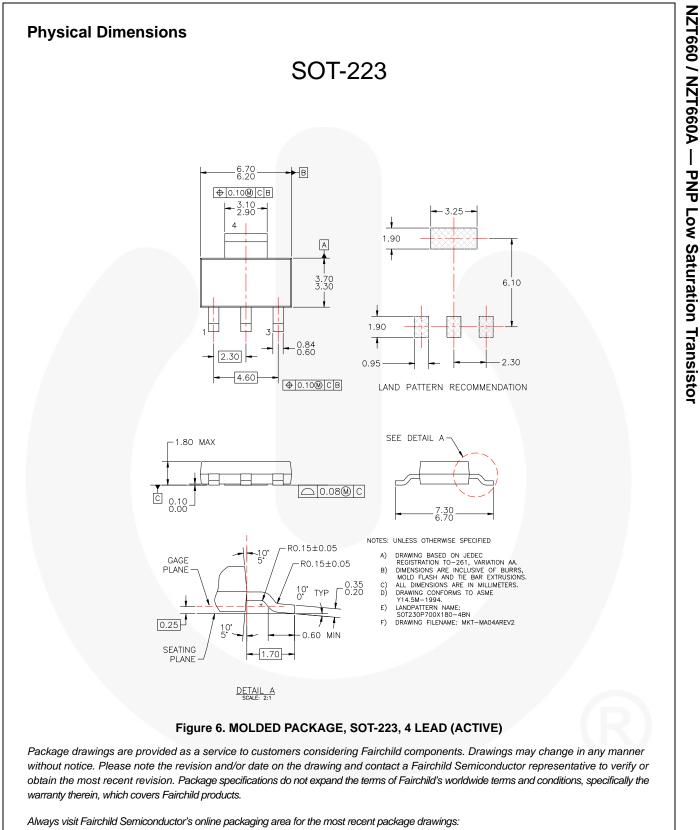
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions		Min.	Max.	Unit
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10 mA		-60		V
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -100 μA	NZT660	-80		- V
			NZT660A	-60		
$BV_{EBO}$	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -100 μA		-5		V
	Collector-Base Cut-Off Current	V <sub>CB</sub> = -30 V			-100	nA
I <sub>CBO</sub> (		V <sub>CB</sub> = -30 V, T <sub>A</sub> = 100°C			-10	μA
I <sub>EBO</sub>	Emitter-Base Cut-Off Current	V <sub>EB</sub> = -4 V			-100	nA
h <sub>FE</sub>	DC Current Gain <sup>(4)</sup>	$I_{C} = -100 \text{ mA}, V_{CE} = -2 \text{ V}$		70		
		I <sub>C</sub> = -500 mA, V <sub>CE</sub> = -2 V	NZT660	100	300	
			NZT660A	250	550	
		I <sub>C</sub> = -1 A, V <sub>CE</sub> = -2 V		80		
		I <sub>C</sub> = -3 A, V <sub>CE</sub> = -2 V		25		
\/ (oot)		I <sub>C</sub> = -1 A, I <sub>B</sub> = -100 mV			-300	
	Collector-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = -3 A, I <sub>B</sub> = -300 mV	NZT660		-550	mV
			NZT660A		-500	
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = -1 A, I <sub>B</sub> = -100 mV			-1.25	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(4)</sup>	$I_{C} = -1 \text{ A}, V_{CE} = -2 \text{ V}$			-1	V
C <sub>ob</sub>	Output Capacitance	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$			45	pF
f <sub>T</sub>	Transition Frequency	I <sub>C</sub> = -100 mA, V <sub>CE</sub> = -5 V, f = 100 MHz		75		MHz

#### Note:

4. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2.0%.





http://www.fairchildsemi.com/dwg/MA/MA04A.pdf.

For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area: <u>http://www.fairchildsemi.com/packing\_dwg/PKG-MA04A\_BK.pdf</u>.

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
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