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## **ON Semiconductor**®

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## BD175/177/179

## Medium Power Linear and Switching Applications

Complement to BD 176/178/180 respectively



### **NPN Epitaxial Silicon Transistor**

Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Paramet	er	Value	Units
V <sub>CBO</sub>	*Collector-Base Voltage	: BD175	45	V
		: BD177	60	V
		: BD179	80	V
/ <sub>CEO</sub>	Collector-Emitter Voltage	: BD175	45	V
	_	: BD177	60	V
		: BD179	80	V
/ <sub>EBO</sub>	Emitter-Base Voltage		5	V
C	Collector Current (DC)		3	А
СР	*Collector Current (Pulse)		7	А
°c	Collector Dissipation (T <sub>C</sub> =25°C)		30	W
Г <sub>Ј</sub>	Junction Temperature		150	°C
Г <sub>STG</sub>	Storage Temperature		- 65 ~ 150	°C

#### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter		Test Condition	Min.	Тур.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining	Voltage					
		: BD175	$I_{\rm C} = 100 {\rm mA}, I_{\rm B} = 0$	45			V
		: BD177		60			V
		: BD179		80			V
I <sub>CBO</sub>	Collector Cut-off Current	: BD175	$V_{CB} = 45V, I_E = 0$			100	μΑ
		: BD177	$V_{CB} = 60V, I_E = 0$			100	μΑ
		: BD179	$V_{CB} = 80V, I_E = 0$			100	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current		$V_{EB} = 5V, I_{C} = 0$			1	mA
h <sub>FE1</sub>	* DC Current Gain		$V_{CE} = 2V, I_{C} = 150 \text{mA}$	40		250	
h <sub>FE2</sub>			$V_{CE} = 2V, I_{C} = 1A$	15			
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation	Voltage	I <sub>C</sub> = 1A, I <sub>B</sub> = 0.1A			0.8	V
V <sub>BE</sub> (on)	* Base-Emitter On Voltage		$V_{CE} = 2V, I_{C} = 1A$			1.3	V
f <sub>T</sub>	Current Gain Bandwidth Proc	luct	$V_{CE} = 10V, I_{C} = 250mA$	3			MHz

\* Pulse Test: PW=300µs, duty Cycle=1.5% Pulsed

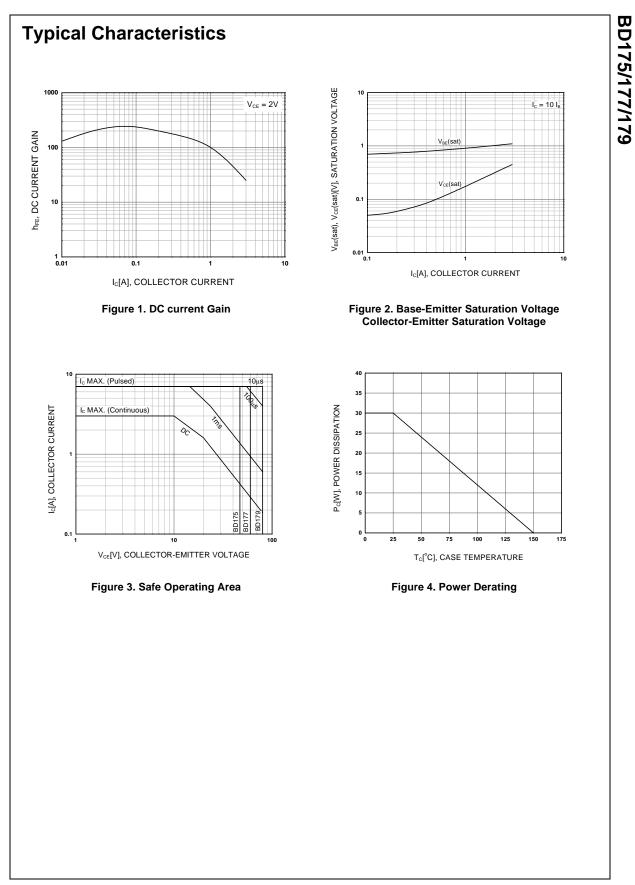
### h<sub>FE</sub> Classificntion

Classification	6	10	16	
h <sub>FE1</sub>	40 ~ 100	63 ~ 160	100 ~ 250	
* Classification 16: Only BD175				

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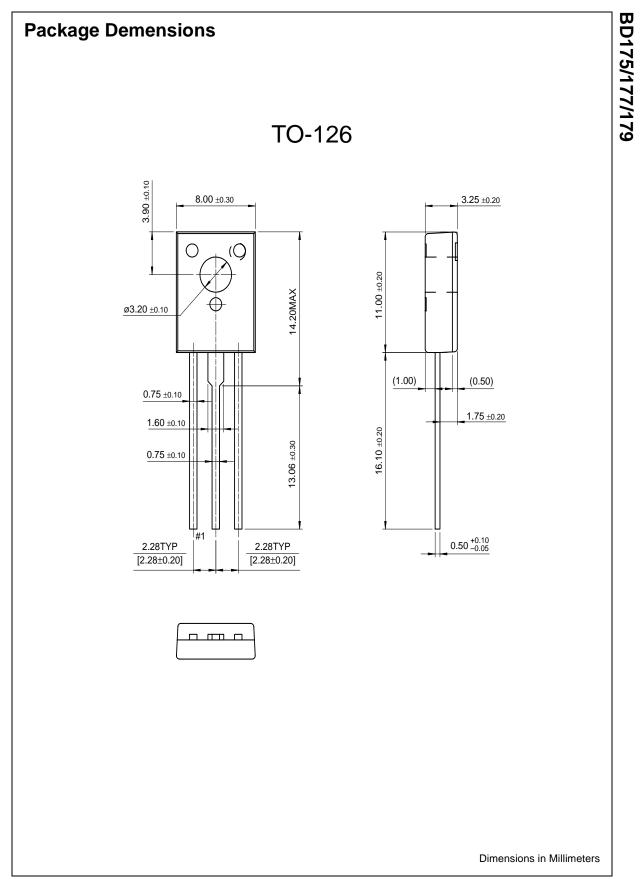
Rev. A, February 2000

BD175/177/179



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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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