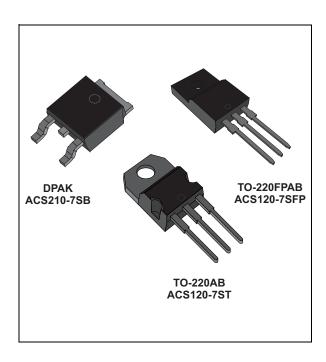


### AC line switch

Datasheet - production data



### **Features**

- Blocking voltage: V<sub>DRM</sub> / V<sub>RRM</sub> = +/- 700 V
- Avalanche controlled: V<sub>CL</sub> typ. = 1100 V
- Nominal conducting current: I<sub>T(RMS)</sub> = 2A
- Gate triggering current: I<sub>GT</sub> < 10mA</li>
- · Switch integrated driver
- High noise immunity: static dV/dt > 500 V/µs

#### **Benefits**

- Needs no more external protection snubber or varistor
- Enables equipment to meet IEC 61000-4-5
- Reduces component count up to 80%
- Interfaces directly with the micro controller
- Eliminates any gate kick back on the microcontroller
- Allows straightforward connection of several AC switches on same cooling pad.

### **Applications**

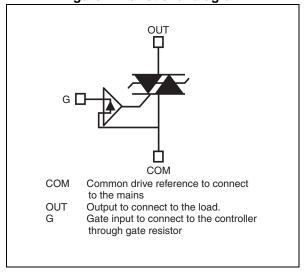
- AC static switching in appliance control systems
- Drive of low power high inductive or resistive loads like:
  - relay, valve, solenoid, dispenser
  - pump, fan, micro-motor
  - defrost heater

### **Description**

The ACS120 belongs to the AC line switch family. This high performance switch circuit is able to control a load of up to 2 A.

The AC switch embeds a high voltage clamping structure to absorb the inductive turn off energy and a gate level shifter driver to separate the digital controller from the main switch. It is triggered with a negative gate current flowing out of the gate pin.

Figure 1. Functional diagram



Characteristics ACS120

## 1 Characteristics

**Table 1. Absolute ratings** (limiting values)
For either positive or negative polarity of pin OUT voltage in respect to pin COM voltage

Symbol	Parameter	Value	Unit		
V <sub>DRM</sub> /V <sub>RRM</sub>	Repetitive peak off-state voltage				V
		DPAK	DPAK T <sub>c</sub> = 119 °C		
I <sub>T(RMS)</sub>	On-state rms current full cycle sine wave 50 to 60 Hz	TO-220FPAB	T <sub>C</sub> = 117 °C	2	Α
	00.00 00 112	TO-220AB	T <sub>C</sub> = 119 °C		
	Non repetitive surge peak on-state current	20	А		
ITSM	$T_j$ initial = 25 °C, full cycle sine wave		F = 60 Hz	21	Α
l <sup>2</sup> t	Fusing capability $t_p = 10 \text{ ms}$				A <sup>2</sup> s
dI/dt	Repetitive on-state current critical rate of rise $I_G = 10$ mA ( $t_r < 100$ ns)	50	A/µs		
V <sub>PP</sub>	Non repetitive line peak pulse voltage <sup>(1)</sup>	2	kV		
T <sub>stg</sub>	Storage temperature range	- 40 to + 150	°C		
T <sub>j</sub>	Operating junction temperature range	- 30 to + 125	°C		
T <sub>I</sub>	Maximum lead soldering temperature during 10 s			260	°C

<sup>1.</sup> According to test described by IEC 61000-4-5 standard and Figure 5

Table 2. Switch Gate characteristics (maximum values)

Symbol	Parameter	Value	Unit
$P_{G(AV)}$	Average gate power dissipation	0.1	W
$I_{GM}$	Peak gate current (t <sub>p</sub> = 20 μs)	1	Α
$V_{GM}$	Peak positive gate voltage (in respect to pin COM)	5	V

**Table 3. Thermal resistances** 

Symbol	Parameter	Parameter			
		$S = 0.5 \text{ cm}^{2(1)}$	DPAK	70	°C/W
R <sub>th (j-a)</sub>	Junction to ambient	TO-220FPAB		60	°C/W
		TO-220AB		60	°C/W
			ΛK	2.6	°C/W
R <sub>th (j-c)</sub>	Junction to tab/lead for full cycle sine wave conduction	TO-220FPAB		3.5	°C/W
		TO-220AB		2.6	°C/W

<sup>1.</sup> S = Copper surface under tab

57

ACS120 Characteristics

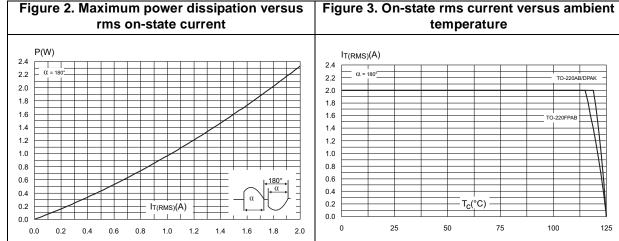
**Table 4. Parameter description** 

Parameter Symbol	Parameter description
I <sub>GT</sub>	Triggering gate current
V <sub>GT</sub>	Triggering gate voltage
V <sub>GD</sub>	Non-triggering gate voltage
I <sub>H</sub>	Holding current
IL	Latching current
V <sub>TM</sub>	Peak on-state voltage drop
V <sub>TO</sub>	On state threshold voltage
R <sub>d</sub>	On state dynamic resistance
I <sub>DRM</sub> / I <sub>RRM</sub>	Maximum forward or reverse leakage current
dV/dt	Critical rate of rise of off-state voltage
(dV/dt)c	Critical rate of rise of commutating off-state voltage
(dl/dt)c	Critical rate of decrease of commutating on-state current
V <sub>CL</sub>	Clamping voltage
I <sub>CL</sub>	Clamping current

**Table 5. Electrical characteristics** 

Symbol	Test conditions			Values	Unit	
I <sub>GT</sub>	$V_{OUT}$ = 12V (DC), $R_L$ = 140 $\Omega$	QII -QIII	$T_j = 25^{\circ}C$	MAX	10	mA
V <sub>GT</sub>	$V_{OUT}$ = 12V (DC), $R_L$ = 140 $\Omega$	QII -QIII	T <sub>j</sub> = 25 °C	MAX	1	V
V <sub>GD</sub>	$V_{OUT} = V_{DRM}, R_L = 3.3 \text{ k}\Omega$		T <sub>j</sub> = 125 °C	MIN	0.15	V
I <sub>H</sub>	I <sub>OUT</sub> = 100 mA gate open		T <sub>j</sub> = 25 °C	MAX	45	mA
IL	I <sub>G</sub> = 20 mA		T <sub>j</sub> = 25 °C	MAX	65	mA
V <sub>TM</sub>	I <sub>OUT</sub> = 2.8 A, t <sub>p</sub> = 380 μs		T <sub>j</sub> = 25 °C	MAX	1.3	V
V <sub>TO</sub>			T <sub>j</sub> = 125 °C	MAX	0.85	V
R <sub>d</sub>			T <sub>j</sub> = 125 °C	MAX	200	mΩ
1 /1	V - 700 V		T <sub>j</sub> = 25 °C	MAX	2	
I <sub>DRM</sub> /I <sub>RRM</sub>	$V_{OUT} = 700 \text{ V}$		T <sub>j</sub> = 125 °C	MAX	200	μΑ
dV/dt	V <sub>OUT</sub> = 460 V gate open		T <sub>j</sub> = 110 °C	MIN	500	V/µs
(dl/dt)c	(dV/dt)c = 20 V/µs		T <sub>j</sub> = 125 °C	MIN	1	A/ms
V <sub>CL</sub>	$I_{CL} = 1 \text{ mA}, t_p = 1 \text{ ms}$		T <sub>i</sub> = 25 °C	TYP	1100	V

**ACS120 Characteristics** 



temperature T<sub>C</sub>(°C) 50 75

Figure 4. On-state rms current versus ambient temperature IT(RMS)(A) 1.8 α = 180° —
Printed circuit board FR4 —
Natural convection
S = 0.5 cm² 1.4 1.2 1.0 0.6 0.2 T<sub>amb</sub>(°C) 0.0

Figure 5. Relative variation of thermal impedance versus pulse duration  $\mathsf{K} = [\mathsf{Z}_{\mathsf{th}}/\mathsf{R}_{\mathsf{th}}]$ 1.E+00 1.E-01 t<sub>p</sub>(s) 1.E-02 1.E-01 1.E+02 1.E+03 1.E-02 1.E+00 1.E+01

holding and latching versus current junction temperature  $l_{GT}, l_{H}, l_{L}[T_j] \, / \, l_{GT}, l_{H}, l_{L} \, [T_j = 25^{\circ}C]$ 4.0 Typical values 3.5 3.0 2.5 2.0 1.5 1.0 0.5 0.0 -40 -30 -20 -10 0 10 20 30 40 50 60 70 80 90 100 110 120 130

Figure 6. Relative variation of gate trigger,

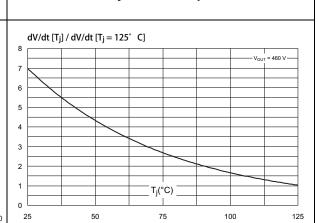


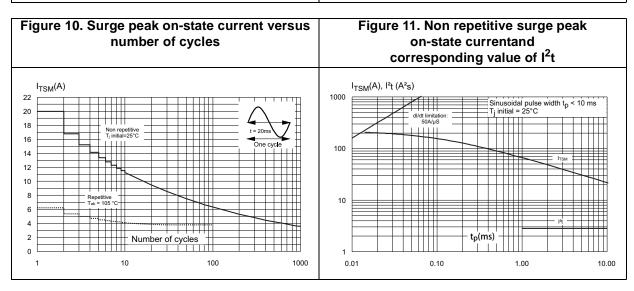
Figure 7. Relative variation of static dV/dt

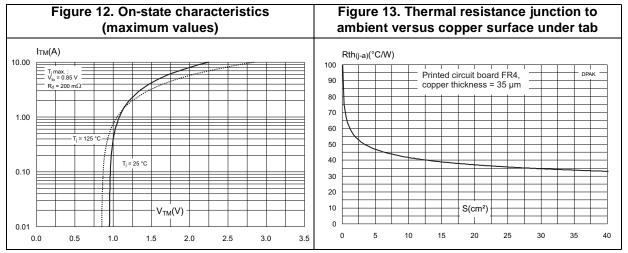
versus junction temperature

DocID9469 Rev 3 4/16

ACS120 Characteristics

Figure 8. Relative variation of critical rate of Figure 9. Relative variation of critical rate of decrease of main current versus reapplied decrease of main current versus junction dV/dt (typical values) temperature  $(dI/dt)_c [T_j] / (dI/dt)_c [T_j = 125 °C]$  $(dI/dt)_c [(dV/dt)_c] / Specified (dI/dt)_c$ 20 5.0 18 4.5 16 4.0 3.5 14 12 3.0 2.5 10 2.0 1.5 1.0 0.5  $T_j(^{\circ}C)$ (dV/dt)<sub>c</sub> (V/µs) 0.0 0 25







## 2 AC line switch basic application

The ACS120 device is well adapted to washing machine, dishwasher, tumble drier, refrigerator, air-conditioning systems, and cookware. It has been designed especially to switch on and off low power loads such as solenoid, valve, relay, dispenser, micro-motor, pump, fan and defrost heaters.

This AC switch is triggered by a negative gate current flowing out of the gate pin G. It can be driven directly by the digital MCU through a resistor as shown on the typical application diagram.

Thanks to its thermal and turn off commutation performances, the ACS120 switch can drive, with no additional turn off snubber, an inductive load up to 2 A.

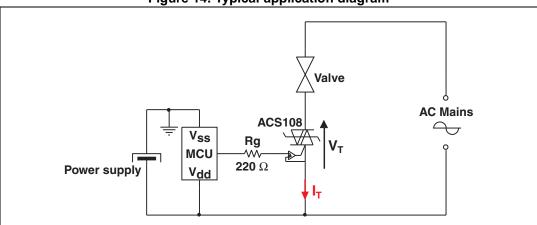
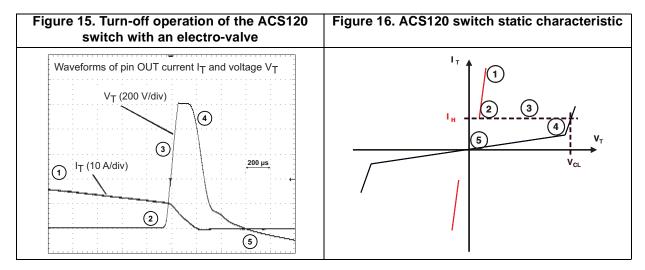
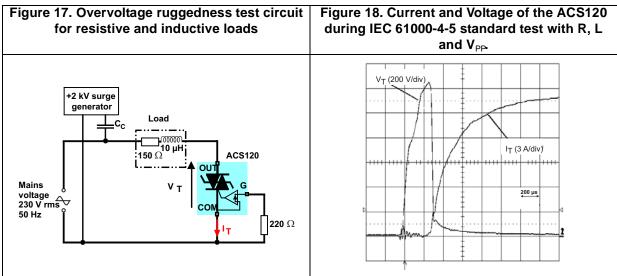


Figure 14. Typical application diagram

577







**Package information ACS120** 

#### **Package information** 3

- Epoxy meets UL94, V0
- Lead-free package
- Recommended torque: 0.4 to 0.6 N·m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

В Dia L6 L2 L7 L3 L5 **F**1 L4 F2

Figure 19. TO-220FPAB dimension definitions



Table 6. TO-220FPAB dimension values

			Dimer	nsions		
Ref.		Millimeters			Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	4.4		4.6	0.173		0.181
В	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
Е	0.45		0.70	0.018		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.70	0.045		0.067
F2	1.15		1.70	0.045		0.067
G	4.95		5.20	0.195		0.205
G1	2.4		2.7	0.094		0.106
Н	10		10.4	0.393		0.409
L2		16			0.63	
L3	28.6		30.6	1.126		1.205
L4	9.8		10.6	0.386		0.417
L5	2.9		3.6	0.114		0.142
L6	15.9		16.4	0.626		0.646
L7	9.00		9.30	0.354		0.366
Dia.	3.00		3.20	0.118		0.126



Package information ACS120

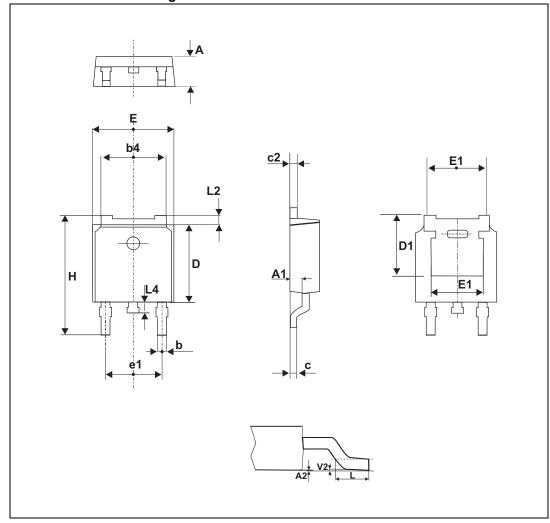


Figure 20. DPAK dimension definitions

Note: This package drawing may slightly differ from the physical package. However, all the specified dimensions are guaranteed.

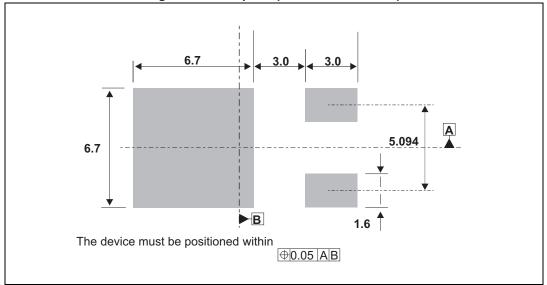
5/

ACS120 Package information

Table 7. DPAK dimension values

			Dime	nsions		
Ref.		Millimeters			Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	2.18		2.40	0.086		0.094
A1	0.90		1.10	0.035		0.043
A2	0.03		0.23	0.001		0.009
b	0.64		0.90	0.025		0.035
b4	4.95		5.46	0.195		0.215
С	0.46		0.61	0.018		0.024
c2	0.46		0.60	0.018		0.023
D	5.97		6.22	0.235		0.244
D1	5.10			0.201		
Е	6.35		6.73	0.250		0.264
E1		4.32			0.170	
e1	4.40		4.70	0.173		0.185
Н	9.35		10.40	0.368		0.409
L	1.00		1.78	0.039		0.070
L2			1.27			0.05
L4	0.60		1.02	0.023		0.040
V2	0°		8°	0°		8°

Figure 21. Footprint (dimensions in mm)





DocID9469 Rev 3 11/16

Package information ACS120

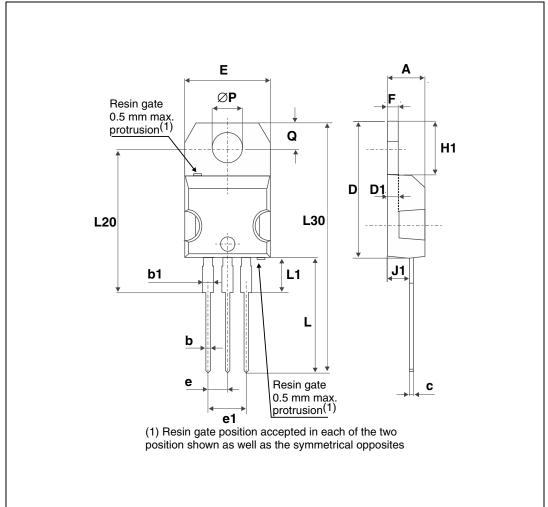


Figure 22. TO-220AB dimension definitions



ACS120 Package information

Table 8. TO-220AB dimension values

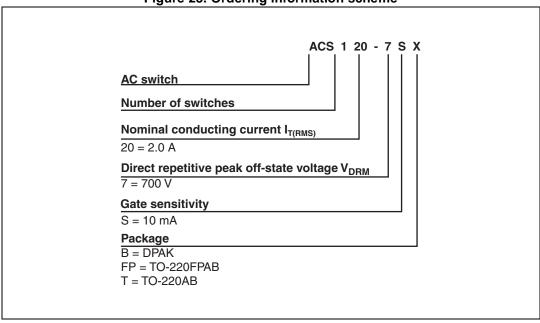
	Dimensions				
Ref.	Millin	Millimeters		hes	
	Min.	Max.	Min.	Max.	
А	4.40	4.60	0.17	0.18	
b	0.61	0.88	0.024	0.035	
b1	1.14	1.70	0.045	0.067	
С	0.48	0.70	0.019	0.027	
D	15.25	15.75	0.60	0.62	
D1	1.27 typ.		0.05	typ.	
Е	10	10.40	0.39	0.41	
е	2.40	2.70	0.094	0.106	
e1	4.95	5.15	0.19	0.20	
F	1.23	1.32	0.048	0.052	
H1	6.20	6.60	0.24	0.26	
J1	2.40	2.72	0.094	0.107	
L	13	14	0.51	0.55	
L1	3.50	3.93	0.137	0.154	
L20	16.40 typ.		0.64 typ.		
L30	28.90 typ.		1.13	typ.	
ØP	3.75	3.85	0.147	0.151	
Q	2.65	2.95	0.104	0.116	



Ordering information ACS120

## 4 Ordering information

Figure 23. Ordering information scheme



**Table 9. Ordering information** 

Order code	Marking	Package	Weight	Base Qty	Packing mode
ACS120-7SB	ACS1207S	DPAK	0.3 g	75	Tube
ACS120-7SB-TR	ACS1207S	DPAK	0.3 g	2500	Tape and reel
ACS120-7SFP	ACS1207S	TO-220FPAB	2.4 g	50	Tube
ACS120-7ST	ACS1207S	TO-220AB	2.3 g	250	Bulk



ACS120 Revision history

# 5 Revision history

Table 10. Document revision history

Date	Revision	Changes
Apr-2004	1	Previous release.
28-Jan-2011	2	Added ECOPACK statement. Updated T <sub>c</sub> values in <i>Table 1</i> .
28-May-2014	3	Updated DPAK package information and reformatted to current standard.

#### Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

ST PRODUCTS ARE NOT DESIGNED OR AUTHORIZED FOR USE IN: (A) SAFETY CRITICAL APPLICATIONS SUCH AS LIFE SUPPORTING, ACTIVE IMPLANTED DEVICES OR SYSTEMS WITH PRODUCT FUNCTIONAL SAFETY REQUIREMENTS; (B) AERONAUTIC APPLICATIONS; (C) AUTOMOTIVE APPLICATIONS OR ENVIRONMENTS, AND/OR (D) AEROSPACE APPLICATIONS OR ENVIRONMENTS. WHERE ST PRODUCTS ARE NOT DESIGNED FOR SUCH USE, THE PURCHASER SHALL USE PRODUCTS AT PURCHASER'S SOLE RISK, EVEN IF ST HAS BEEN INFORMED IN WRITING OF SUCH USAGE, UNLESS A PRODUCT IS EXPRESSLY DESIGNATED BY ST AS BEING INTENDED FOR "AUTOMOTIVE, AUTOMOTIVE SAFETY OR MEDICAL" INDUSTRY DOMAINS ACCORDING TO ST PRODUCT DESIGN SPECIFICATIONS. PRODUCTS FORMALLY ESCC, QML OR JAN QUALIFIED ARE DEEMED SUITABLE FOR USE IN AEROSPACE BY THE CORRESPONDING GOVERNMENTAL AGENCY.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2014 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

